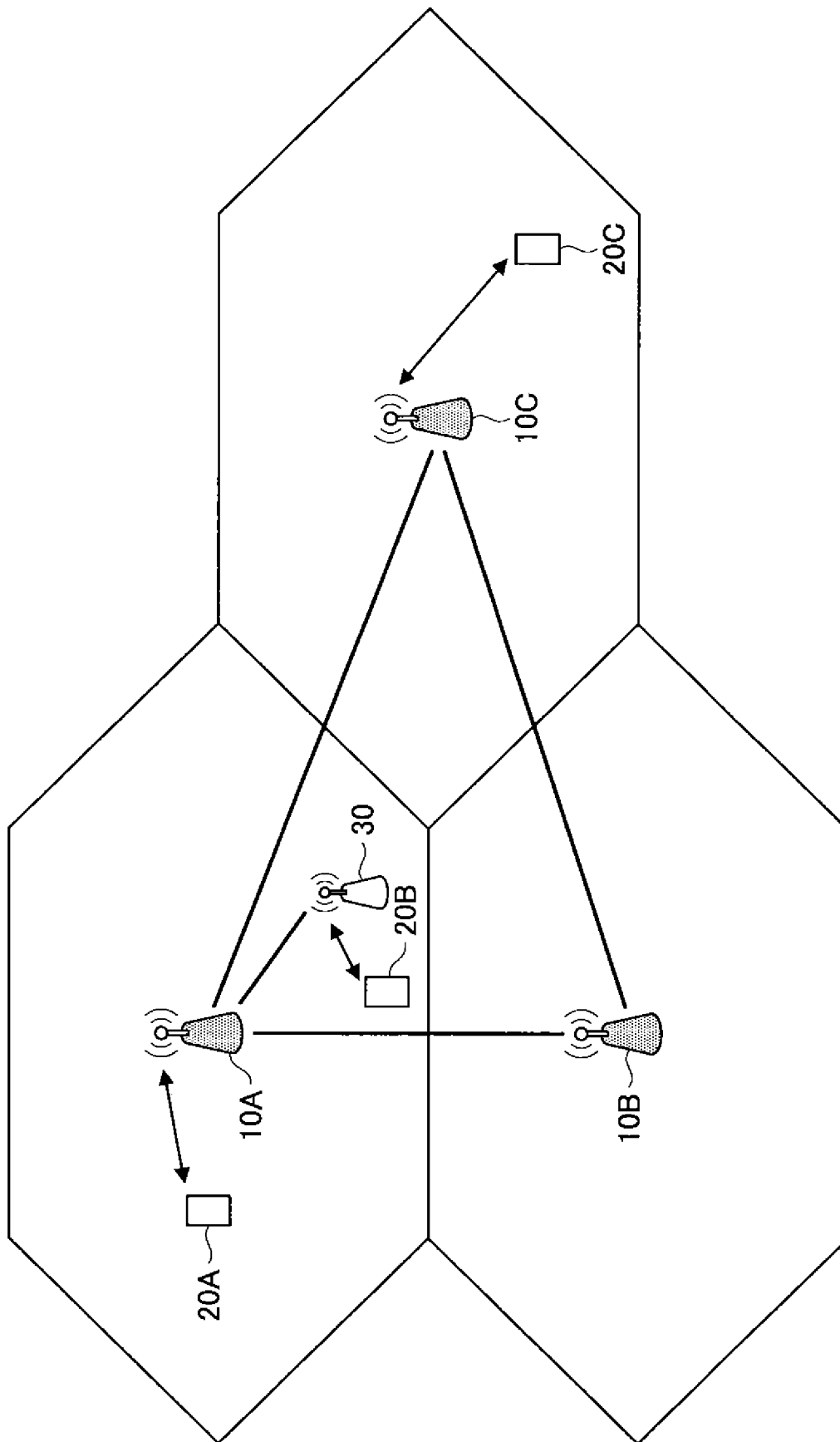
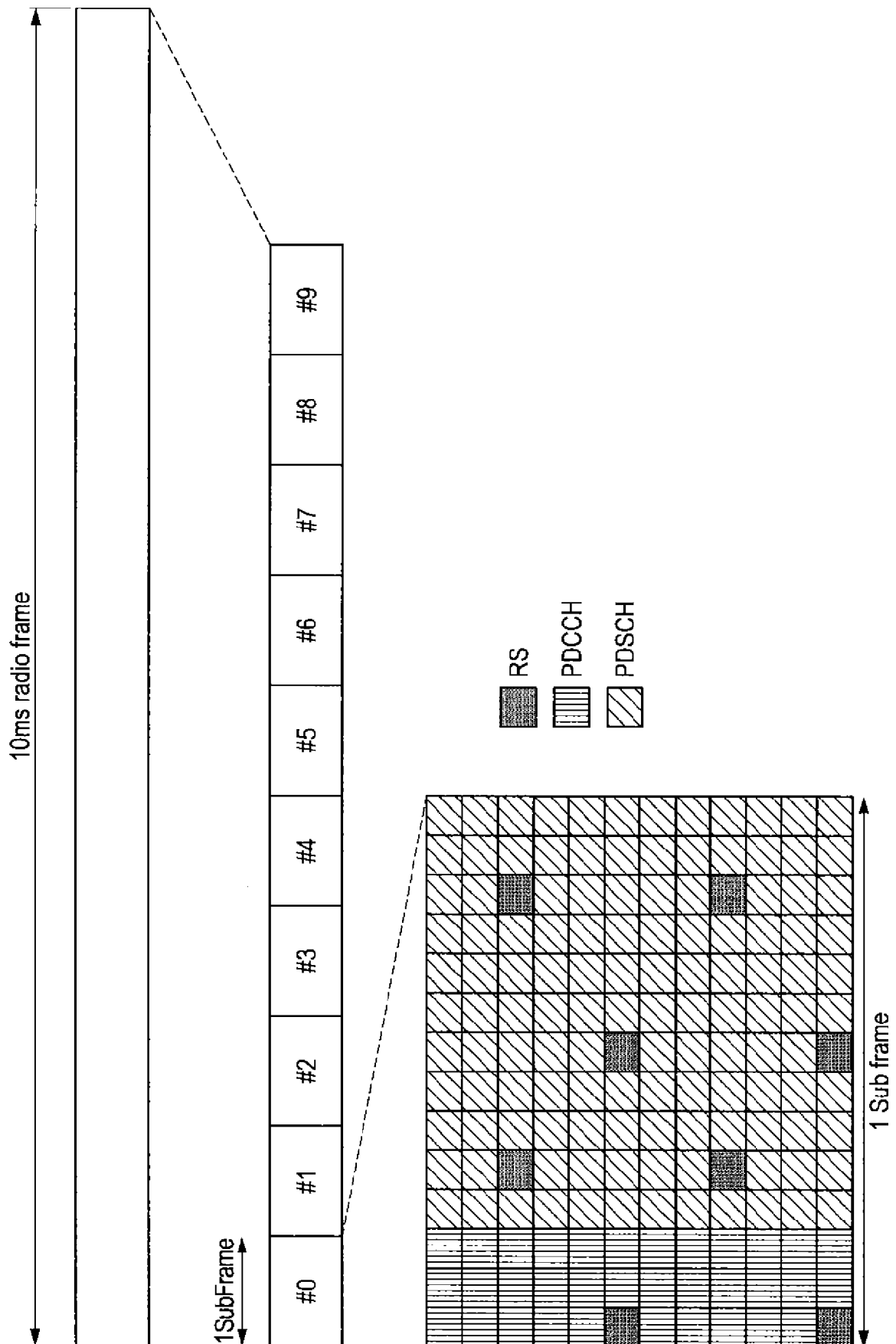


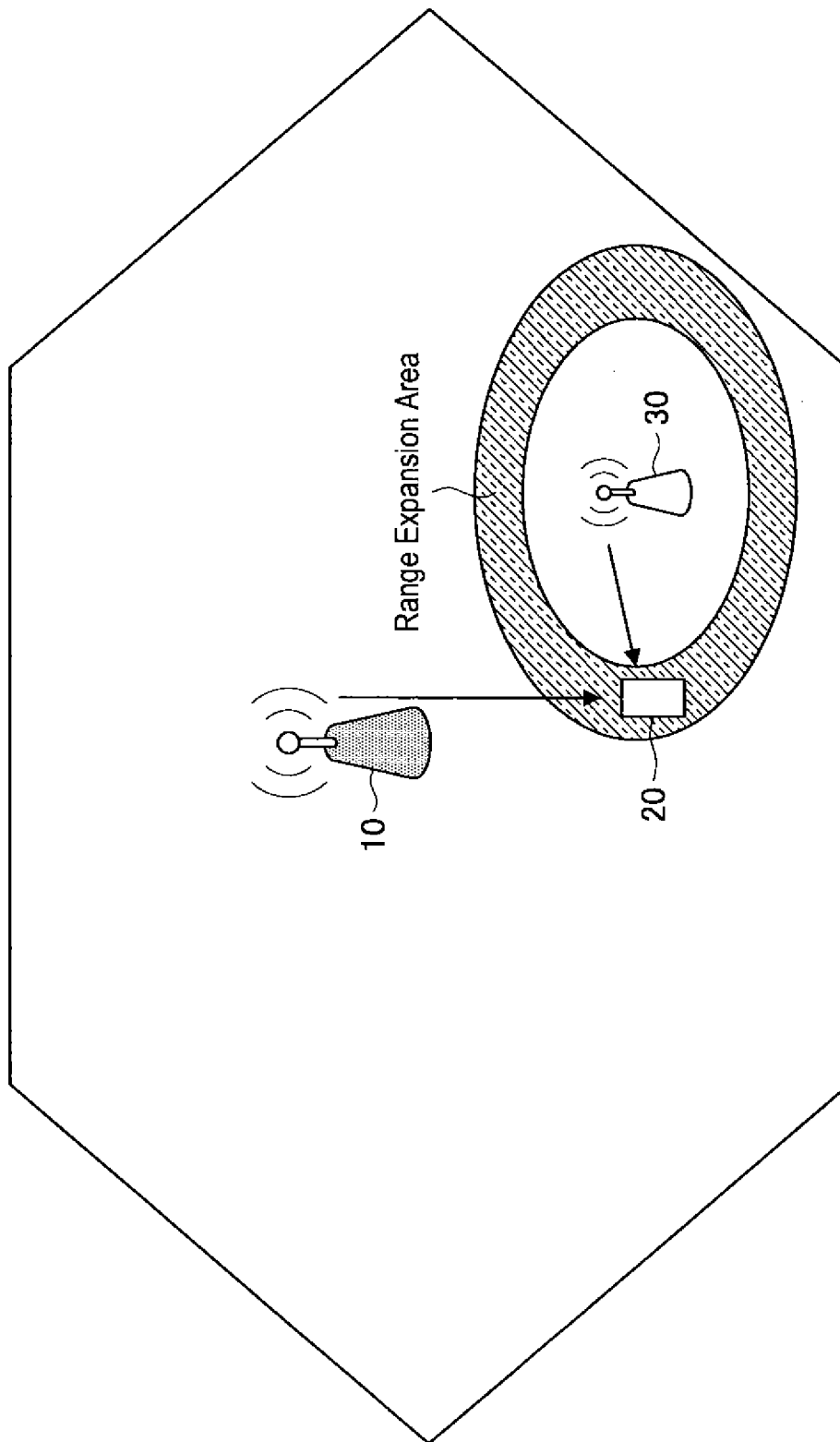
[Fig. 1]



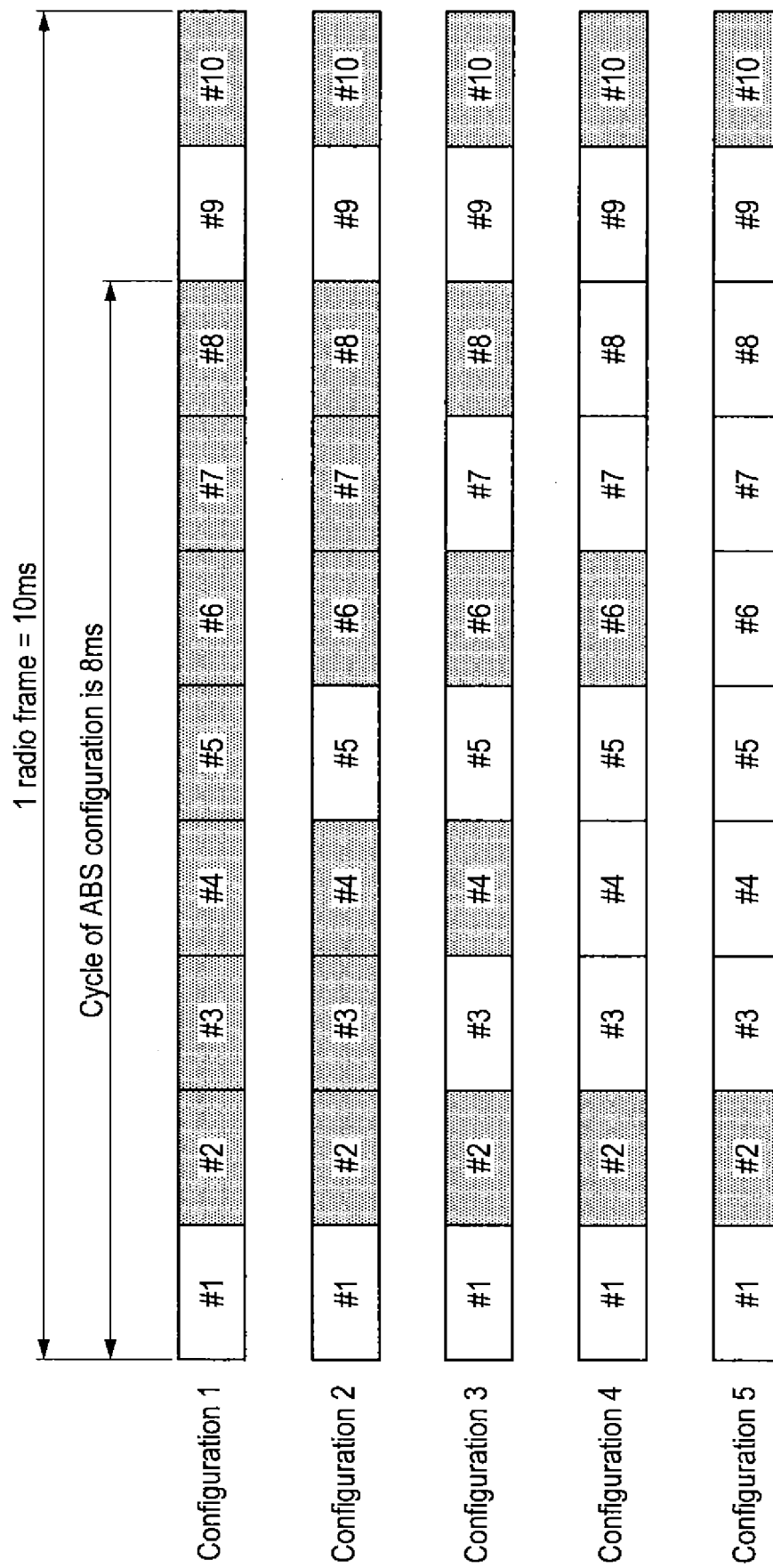
[Fig. 2]



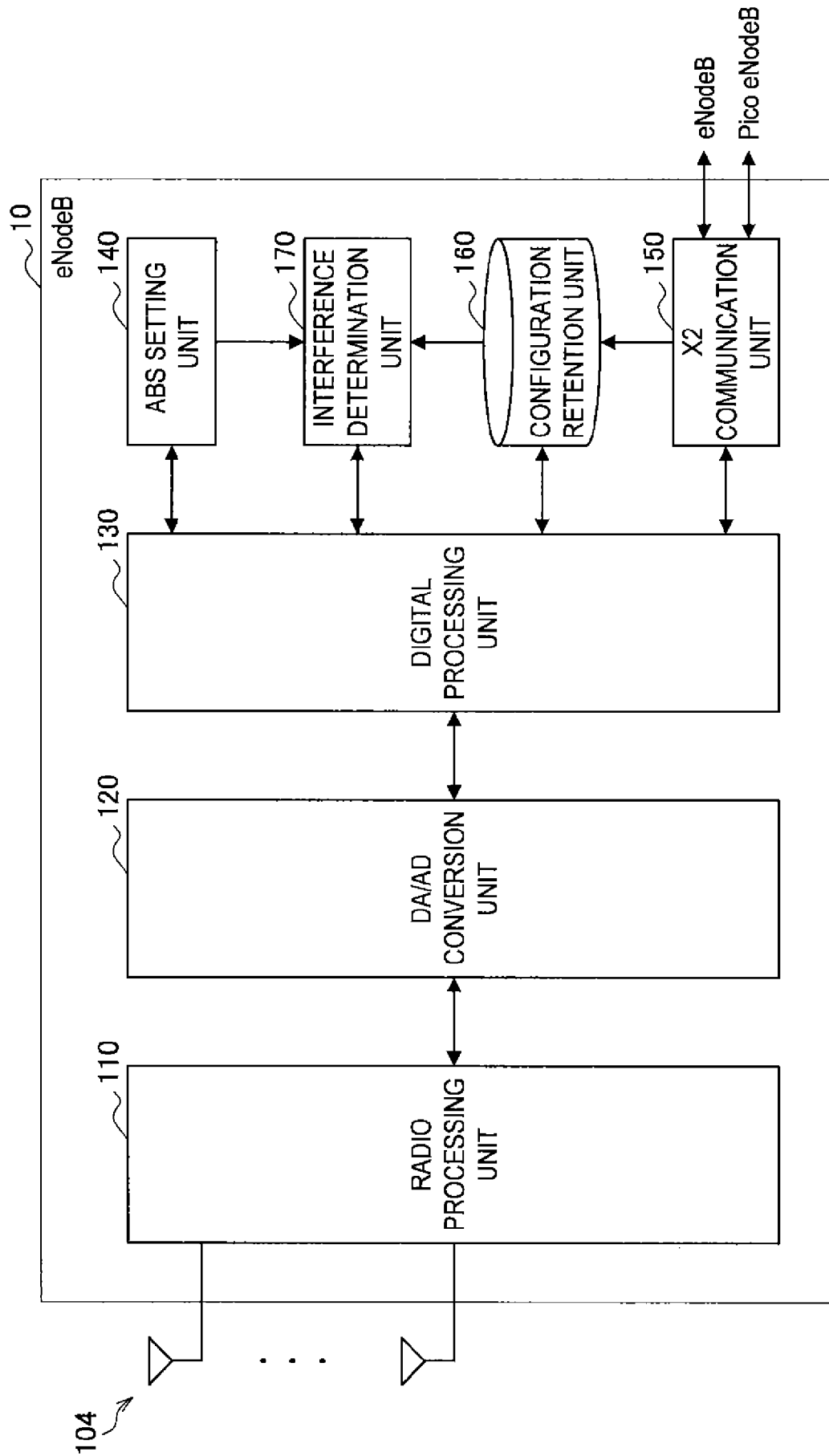
[Fig. 3]



The diagram illustrates a memory layout. On the left, a 4x4 grid of cells is labeled "CONTROL AREA" below it. To the right of this is a larger grid of cells, labeled "DATA AREA" below it. A horizontal arrow at the bottom points from the left edge of the control area to the right edge of the data area, with the labels "CONTROL AREA" and "DATA AREA" positioned below the arrow's segments.



[Fig. 7]



The diagram illustrates the mapping of eNodeBs to ABS configurations. It shows four configurations (3, 2, 4, X) and their corresponding protection status (Not Protected, Protected, Partial Protected).

Configuration 3 (eNodeB10B): Not Protected (Not ABS). The eNodeB is represented by a white box. The configuration is a sequence of 10 cells, labeled #1 to #10. Cells #1, #2, #3, #4, #5, #6, #7, #8, #9, and #10 are all white.

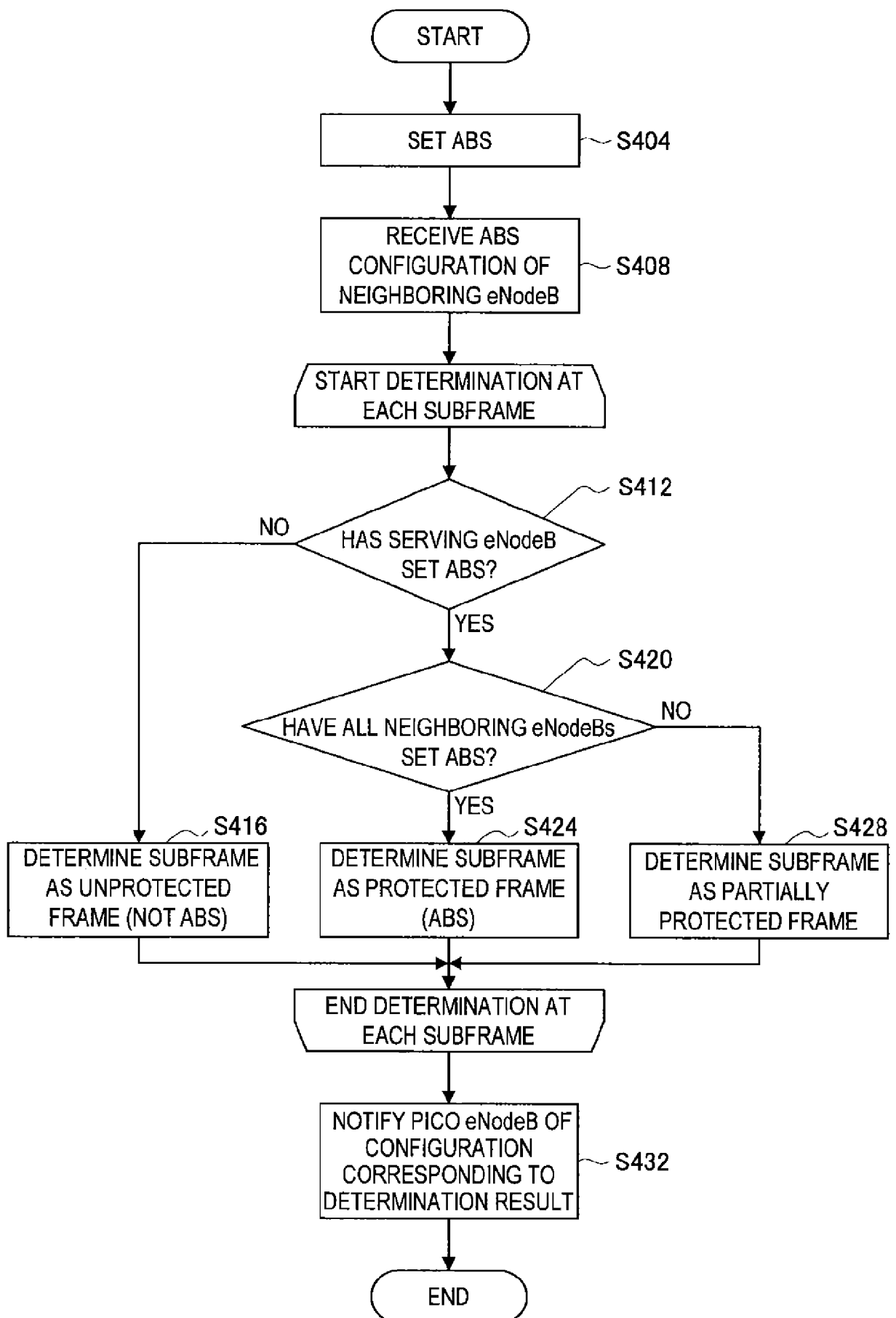
Configuration 2 (eNodeB10A): Protected (ABS). The eNodeB is represented by a box with a dense grid pattern. The configuration is a sequence of 10 cells, labeled #1 to #10. Cells #1, #2, #3, #4, #5, #6, #7, #8, #9, and #10 are all filled with the dense grid pattern.

Configuration 4 (eNodeB10C): Partial Protected. The eNodeB is represented by a box with a sparse grid pattern. The configuration is a sequence of 10 cells, labeled #1 to #10. Cells #1, #2, #3, #4, #5, #6, #7, #8, #9, and #10 are all filled with the sparse grid pattern.

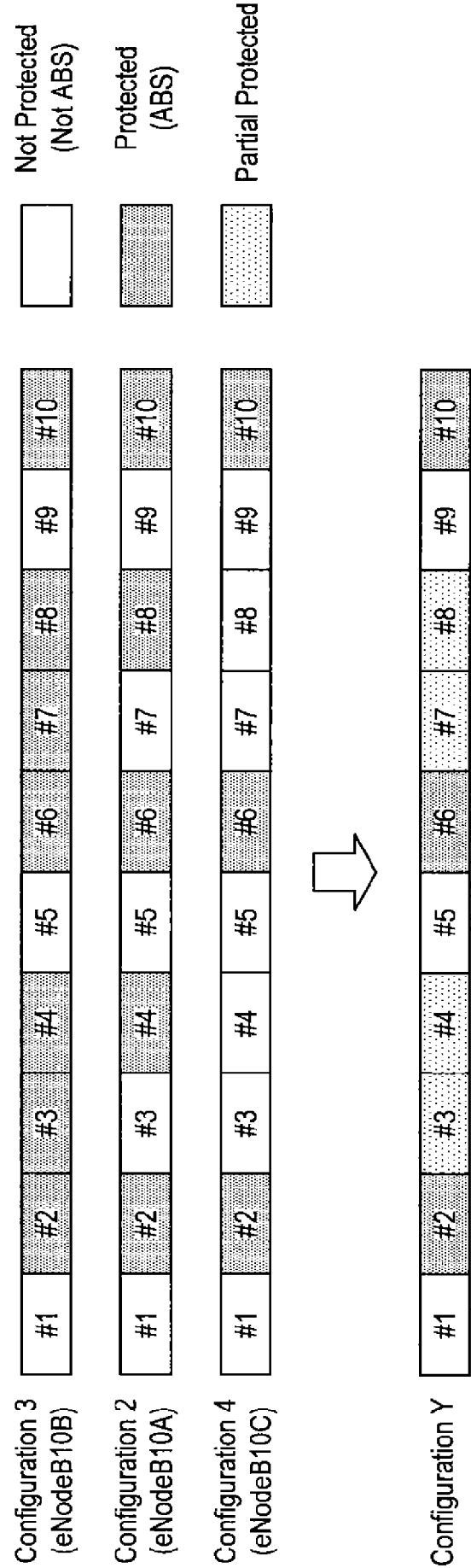
Configuration X: The eNodeB is represented by a box with a sparse grid pattern. The configuration is a sequence of 10 cells, labeled #1 to #10. Cells #1, #2, #3, #4, #5, #6, #7, #8, #9, and #10 are all filled with the sparse grid pattern.

A large arrow points from the configurations to the right, indicating a mapping or transformation.

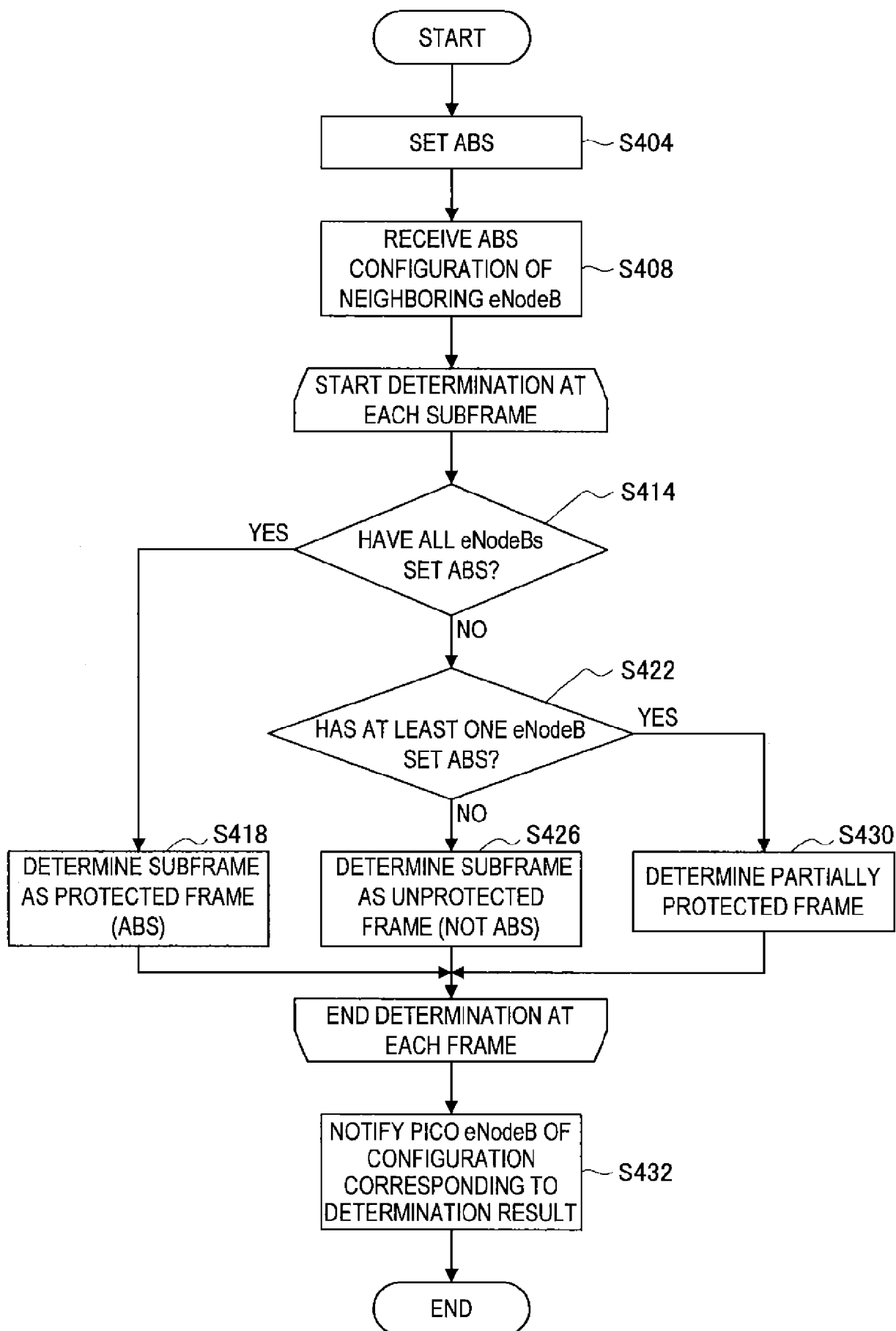
[Fig. 9]



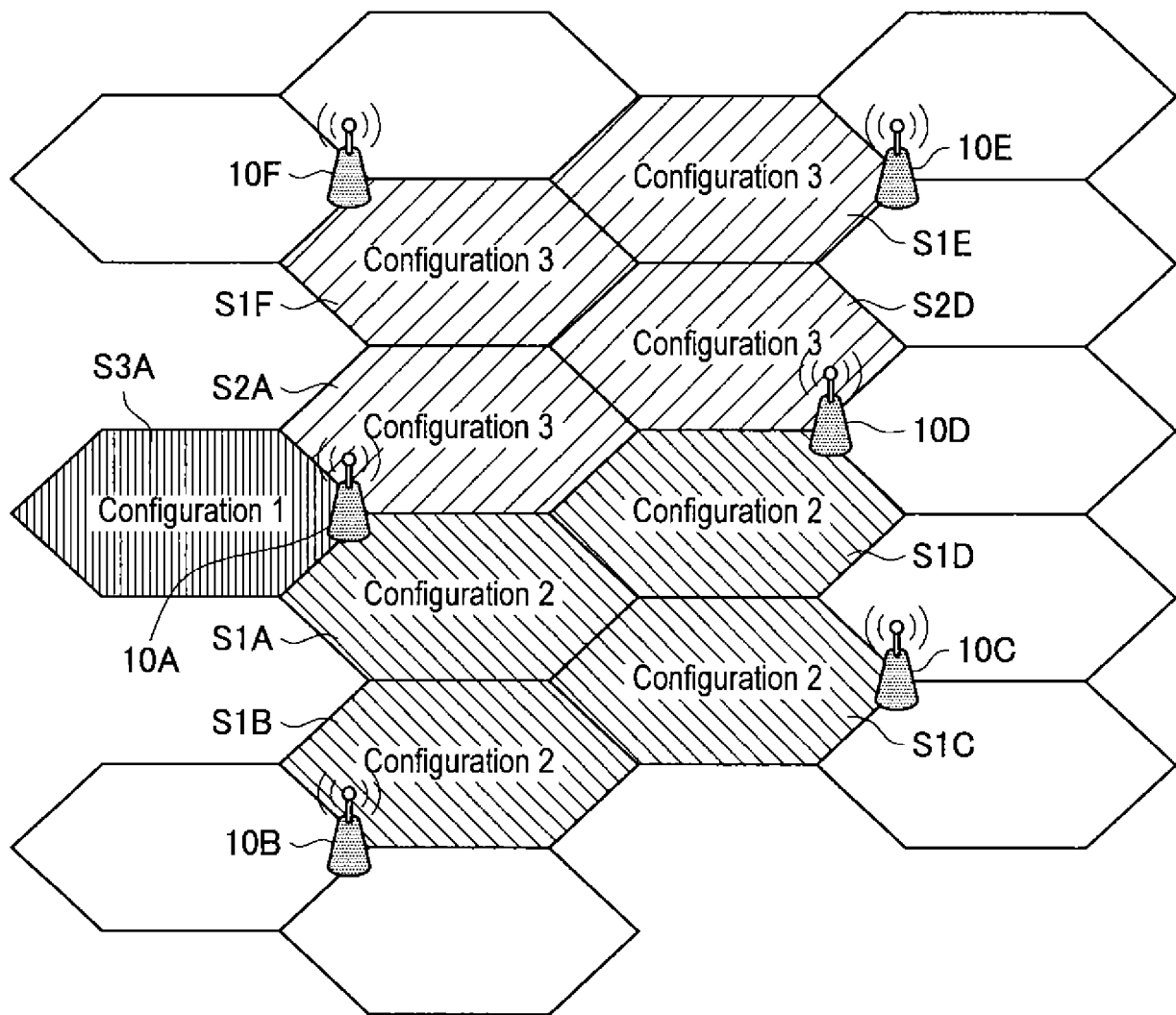
[Fig. 10]



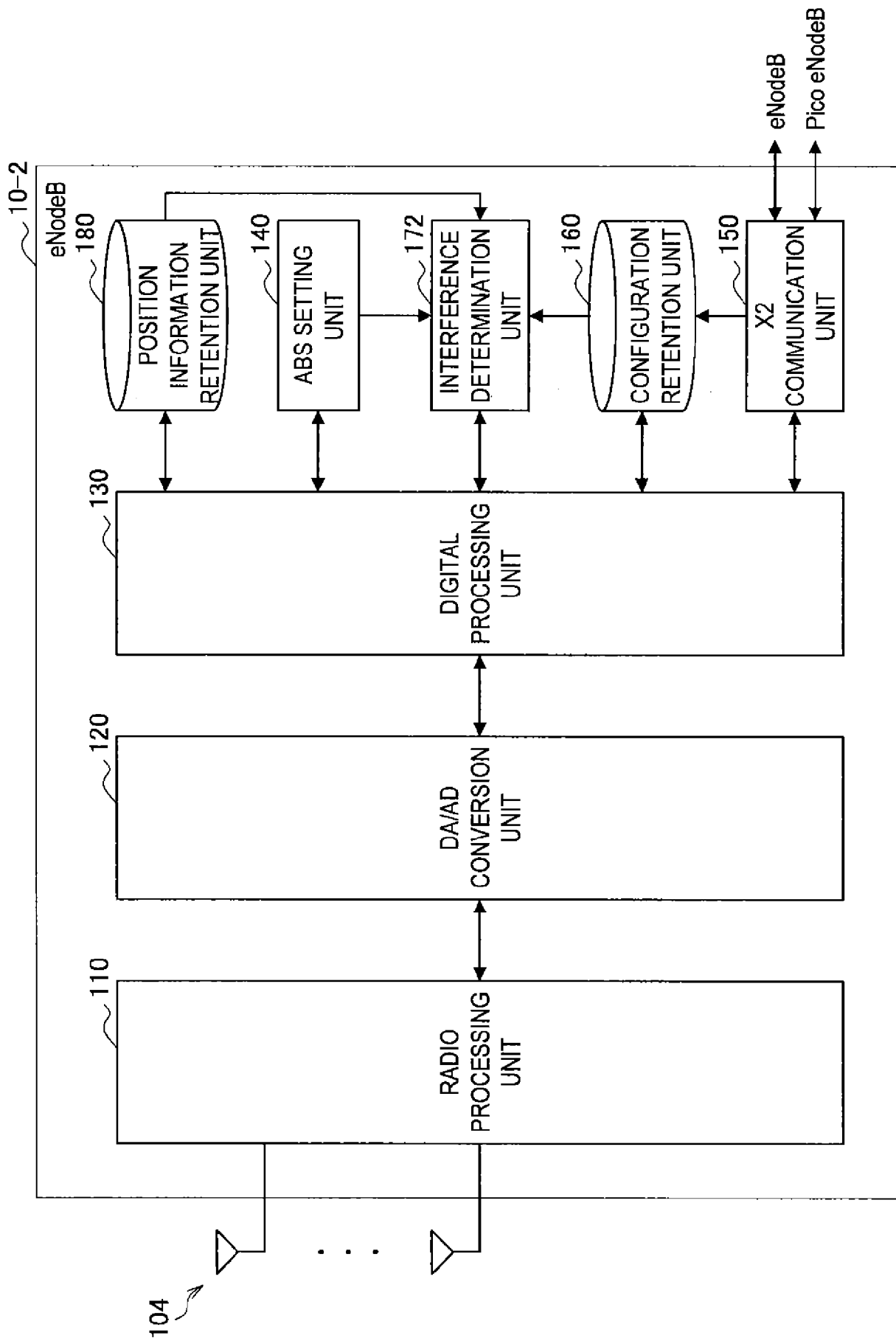
[Fig. 11]



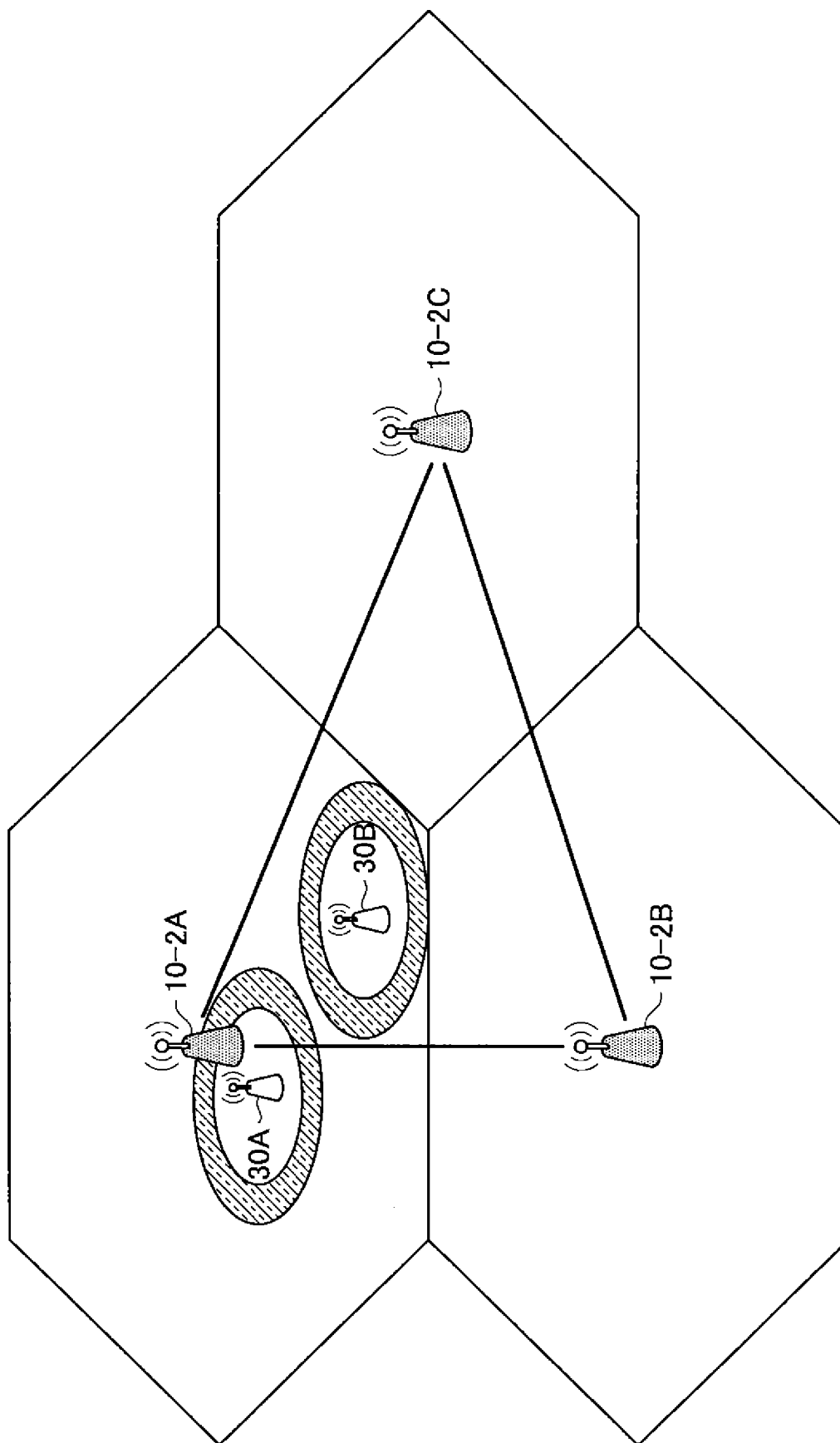
[Fig. 12]



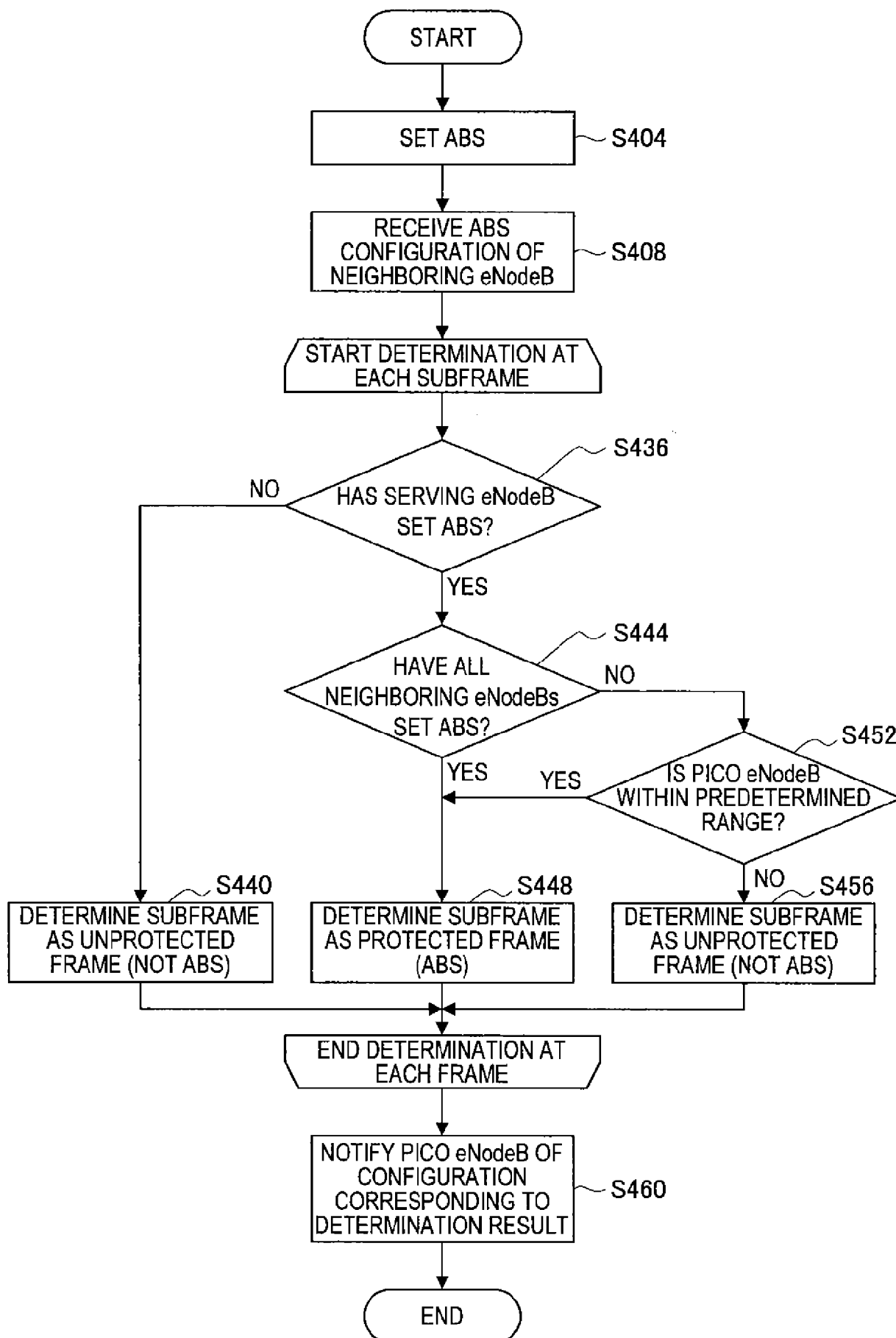
[Fig. 13]



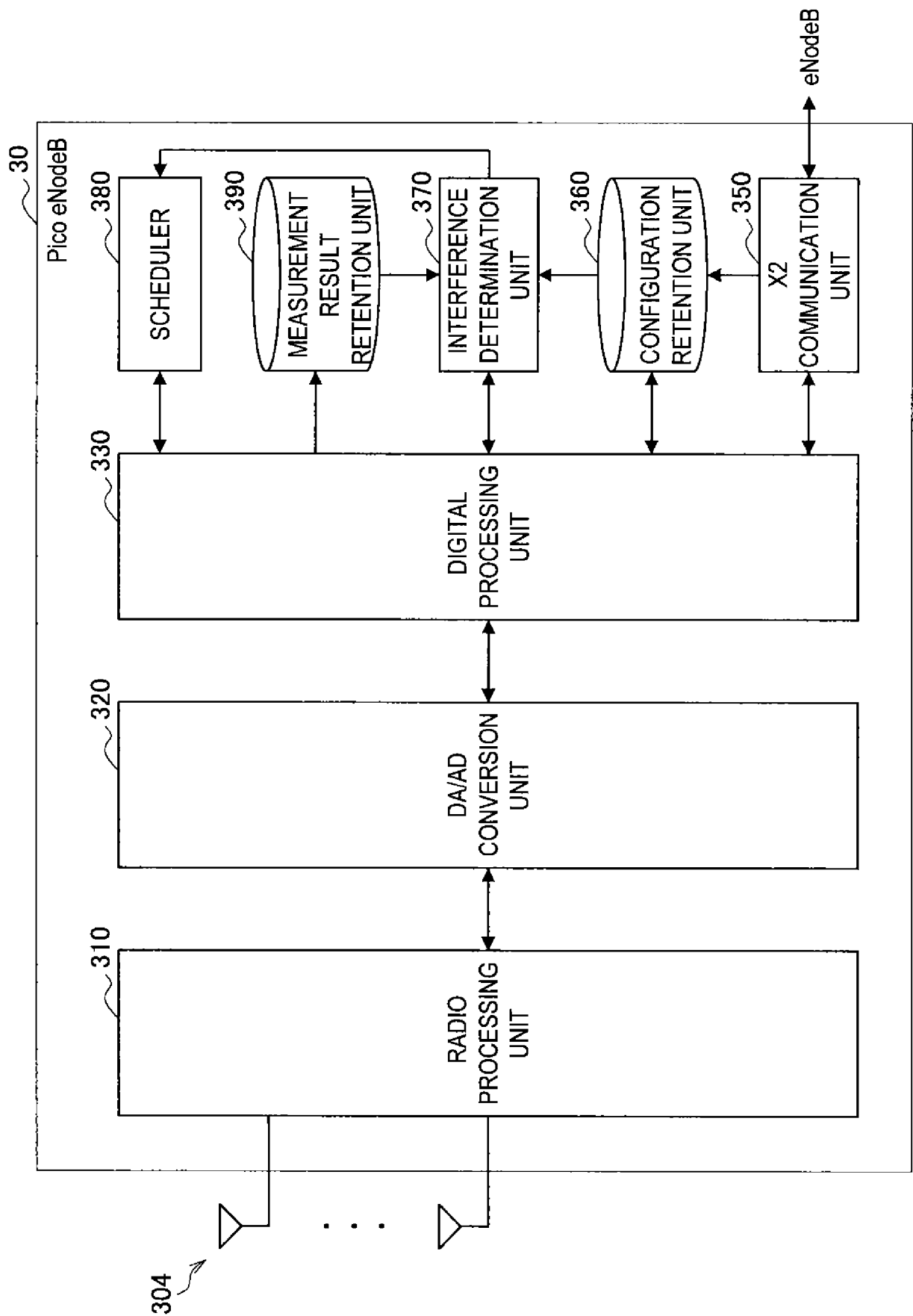
[Fig. 14]



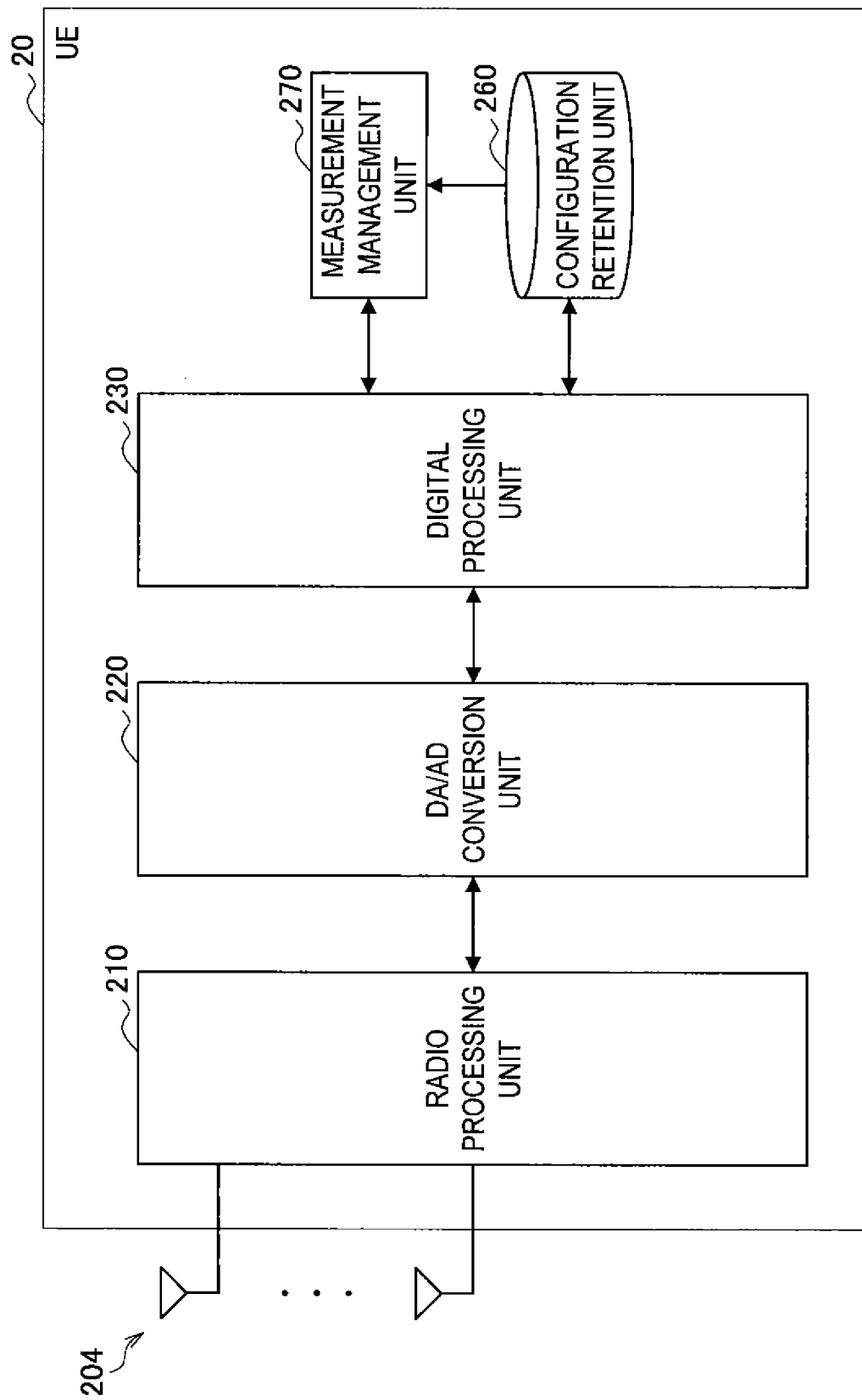
[Fig. 15]



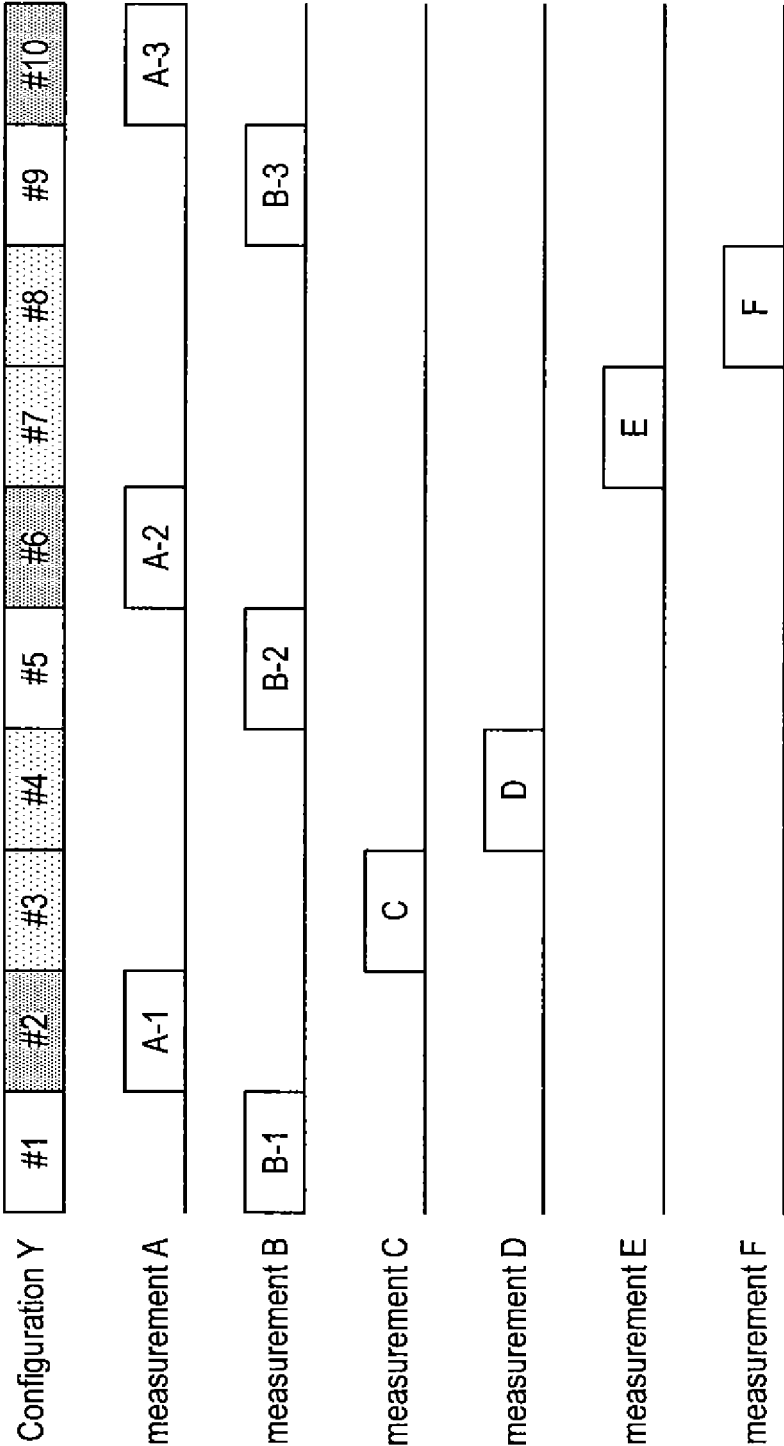
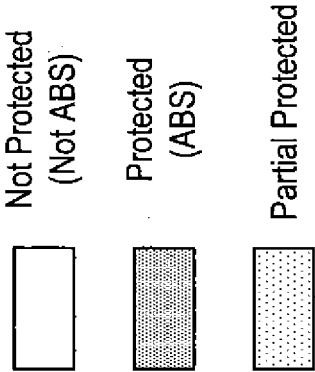
[Fig. 16]



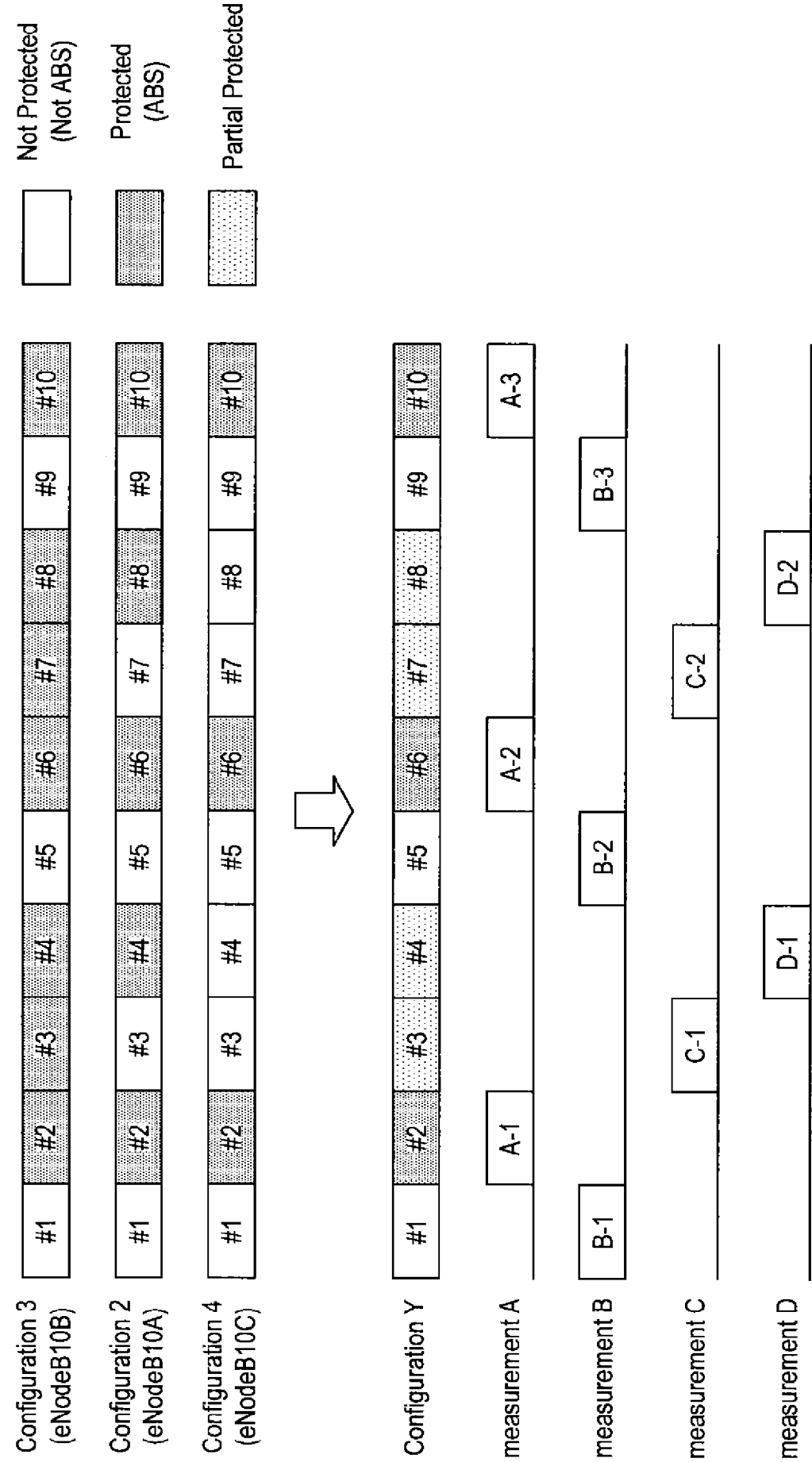
[Fig. 17]



[Fig. 18]



[Fig. 19]



[Fig. 20]

