

FIG. 1

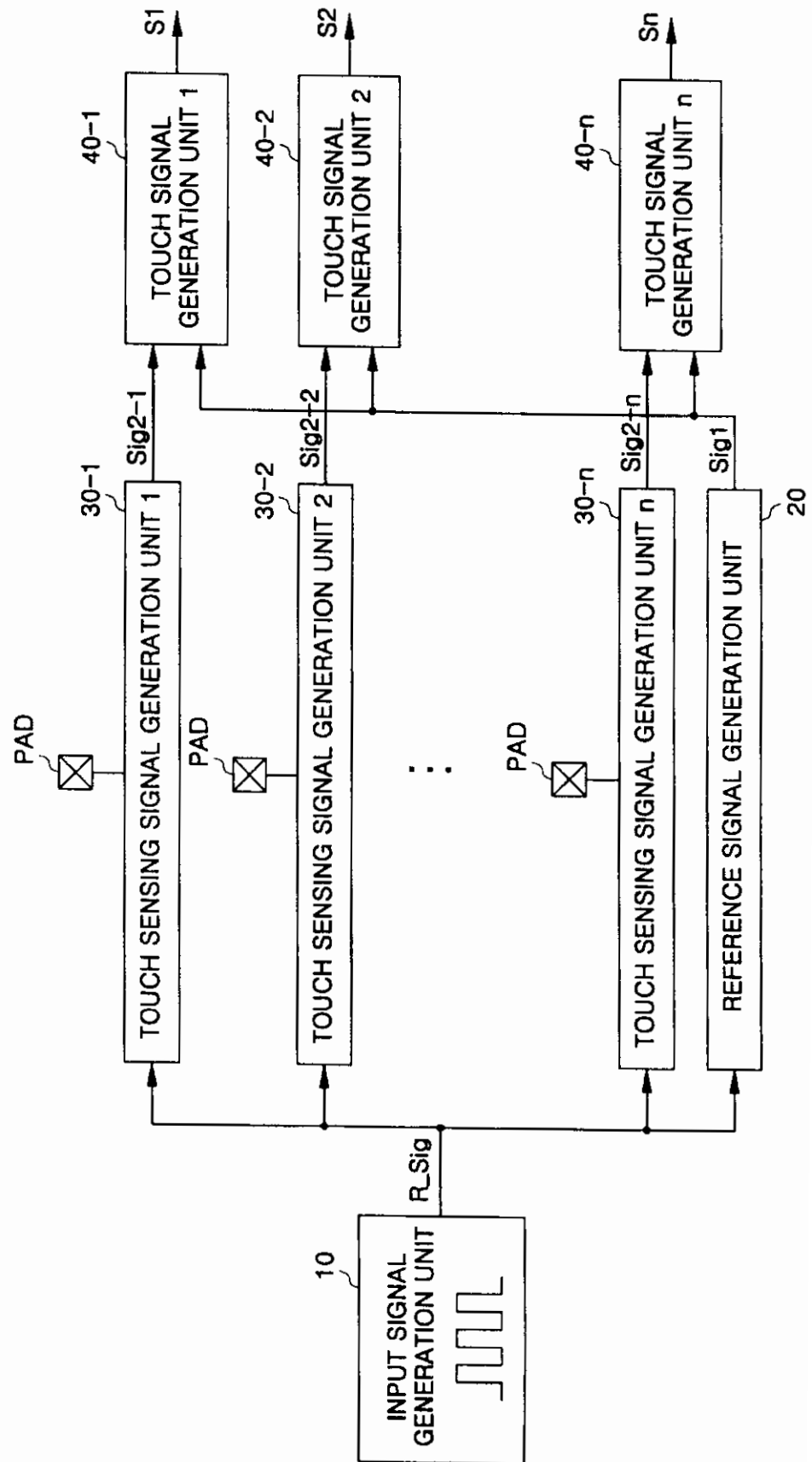


FIG. 2

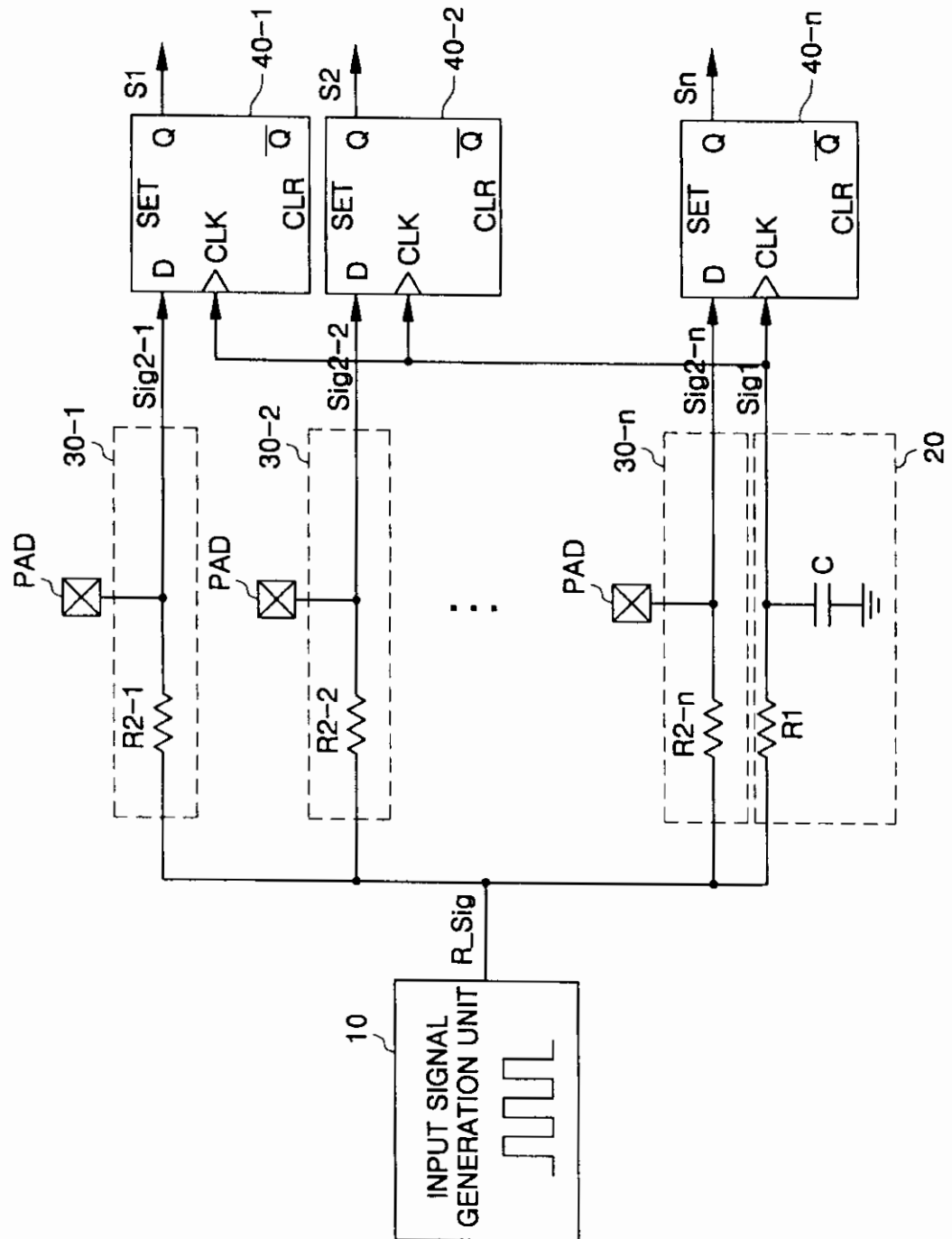


FIG. 3

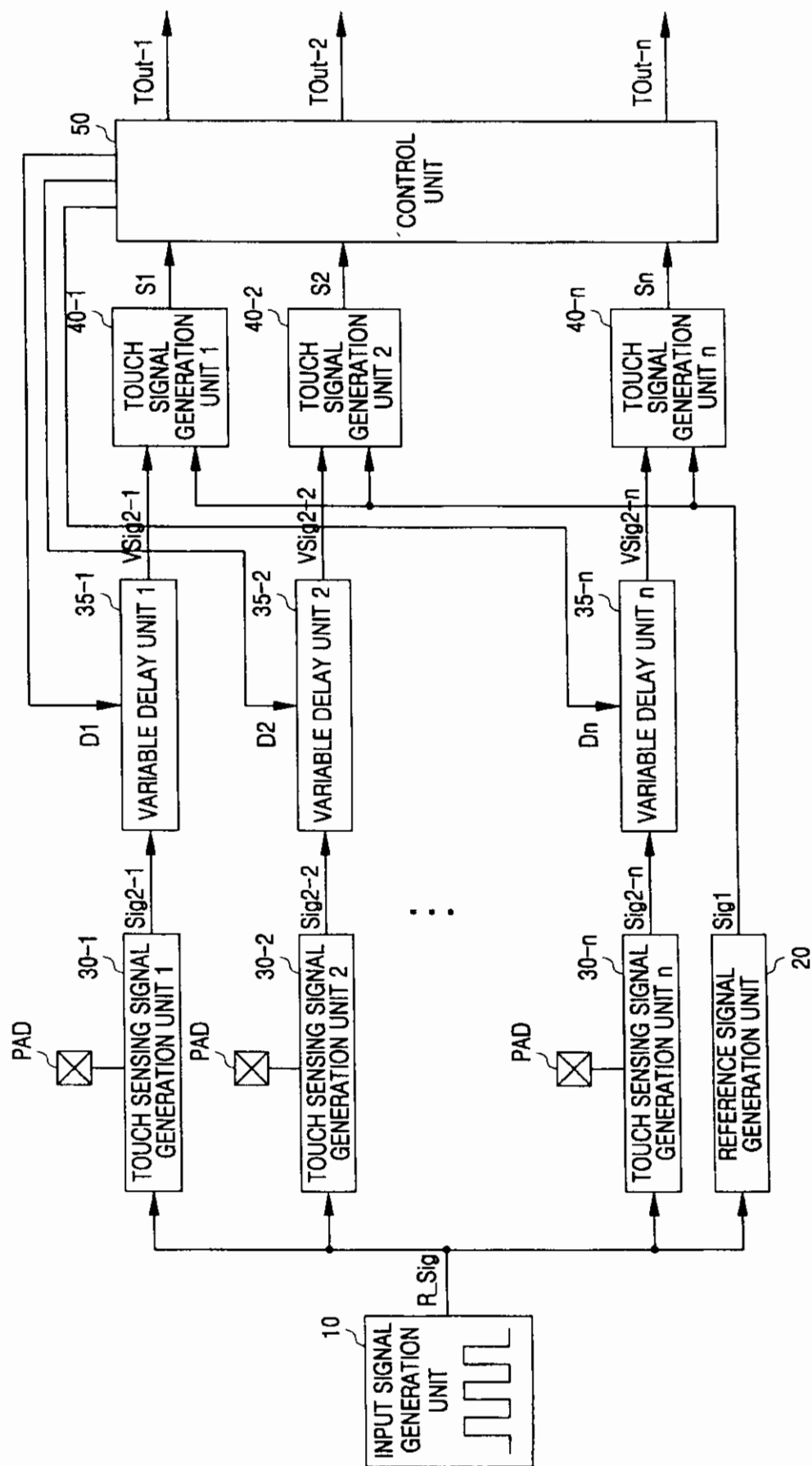


FIG. 4A

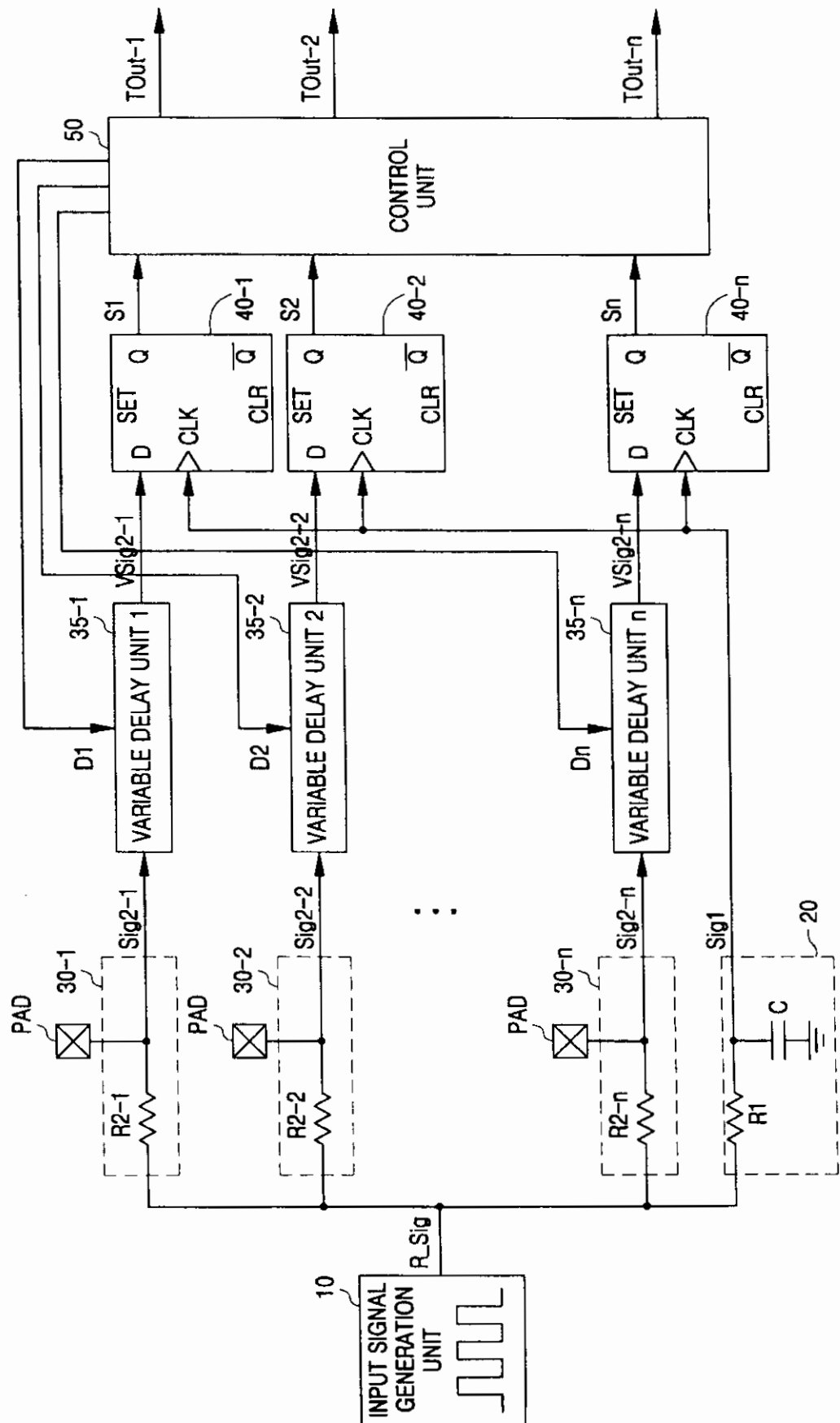


FIG. 4B

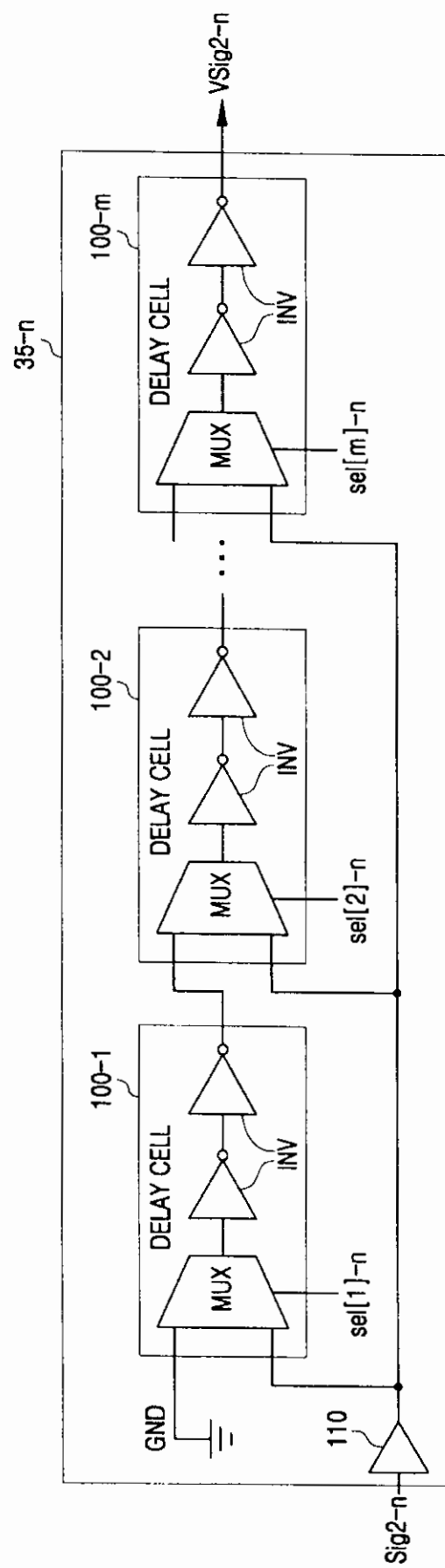


FIG. 4C

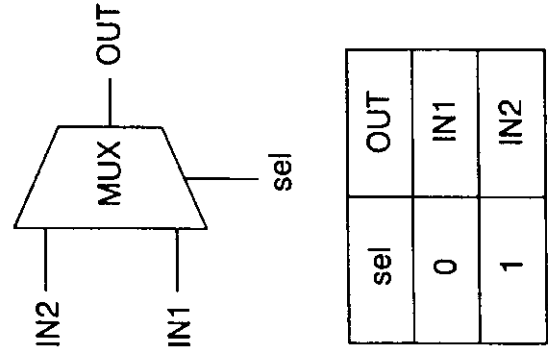
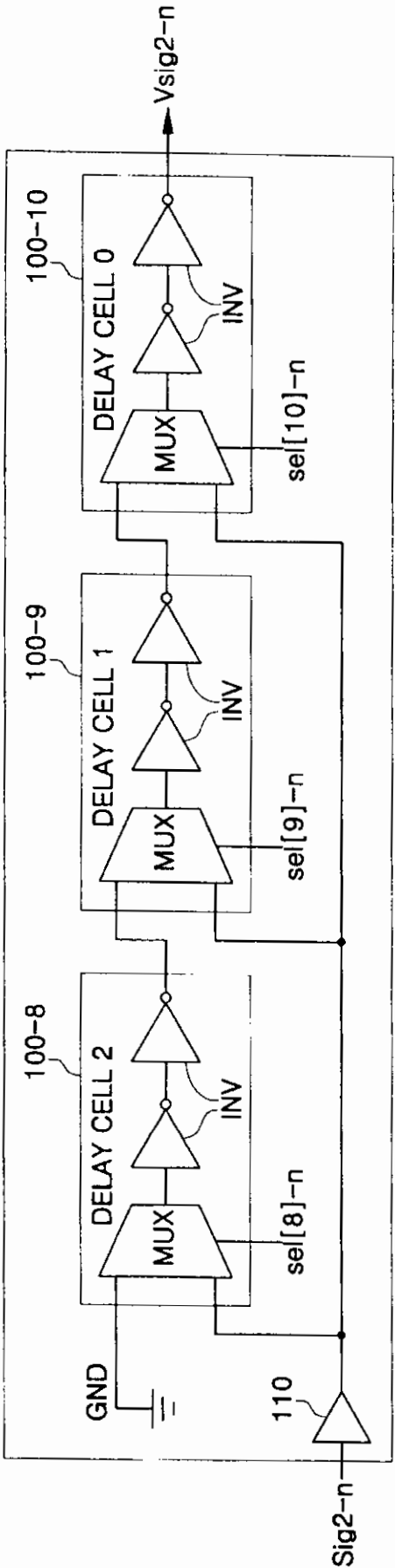


FIG. 4D



$sel[8]-n$	$sel[9]-n$	$sel[10]-n$	ACTIVATED DELAY CELL
x	x	0	100-10
x	0	1	100-9, 100-10
0	1	1	100-8, 100-9, 100-10

x : DON'T CARE

FIG. 5

