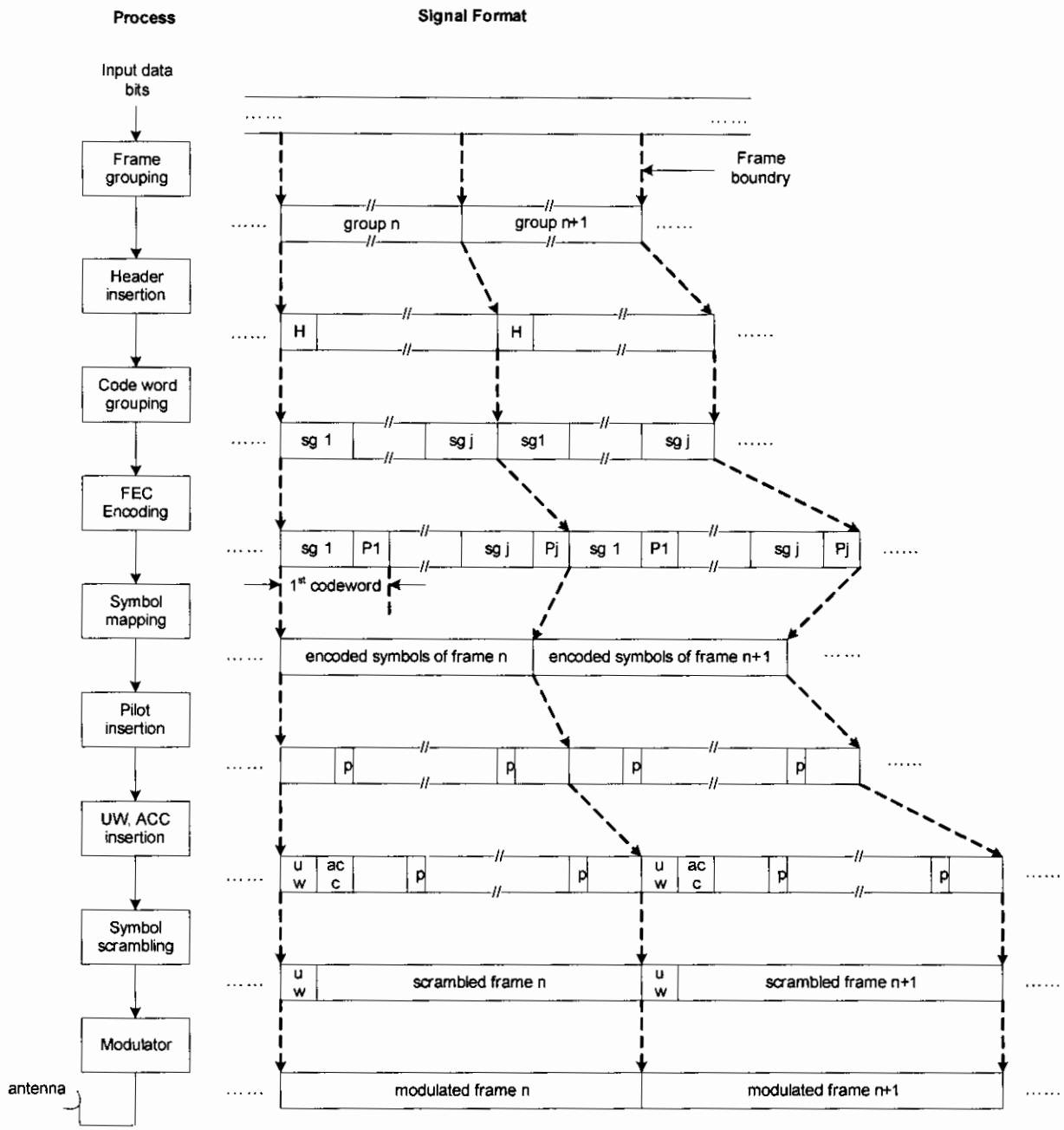

Process
Signal Format
Input data bits
Frame grouping
Header insertion
Code word grouping
FEC Encoding
Symbol mapping
Pilot insertion
UW, ACC insertion
Symbol scrambling
Modulator
antenna
Frame boundry
group n
group n+1
H
sg 1
sg j
P1
Pj
1st codeword
encoded symbols of frame n
encoded symbols of frame n+1
p
uw
acc
scrambled frame n
scrambled frame n+1
modulated frame n
modulated frame n+1

Figure 1

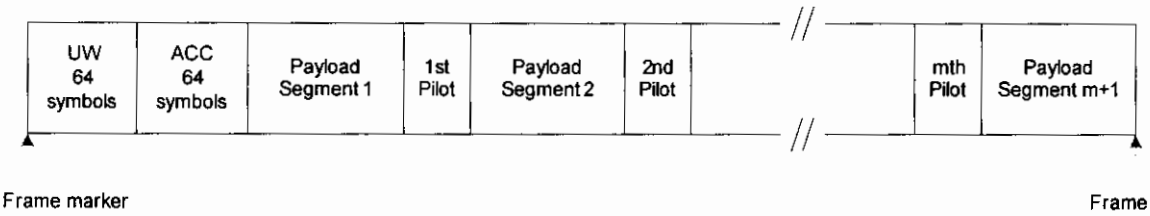


Figure 22

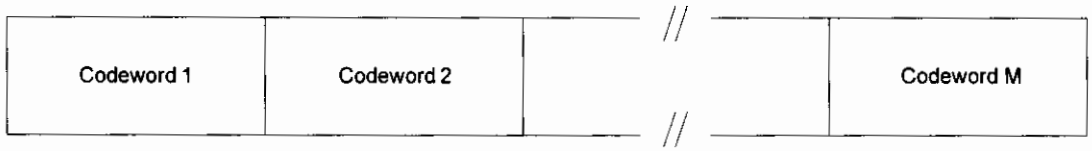


Figure 33

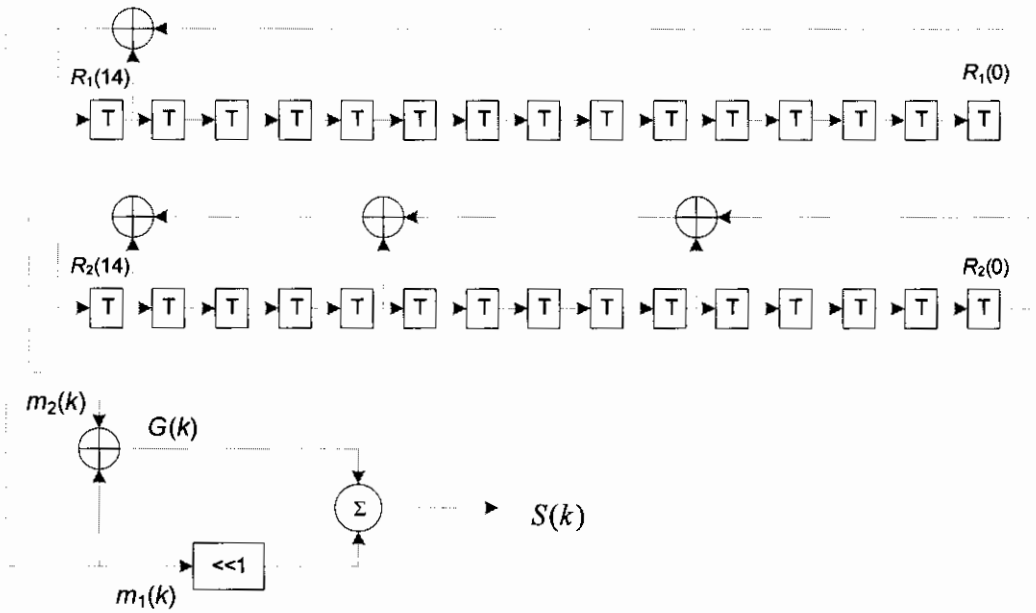


Figure 44

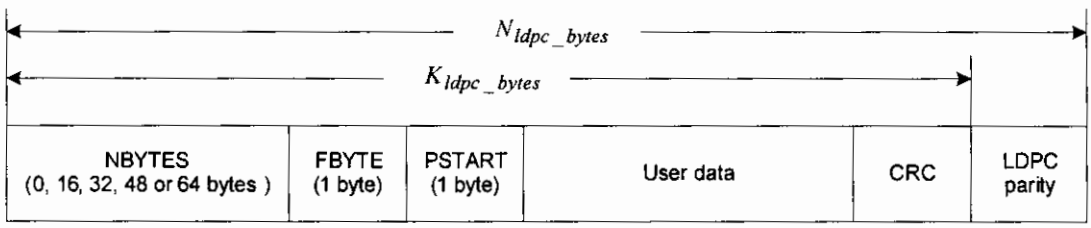


Figure 55

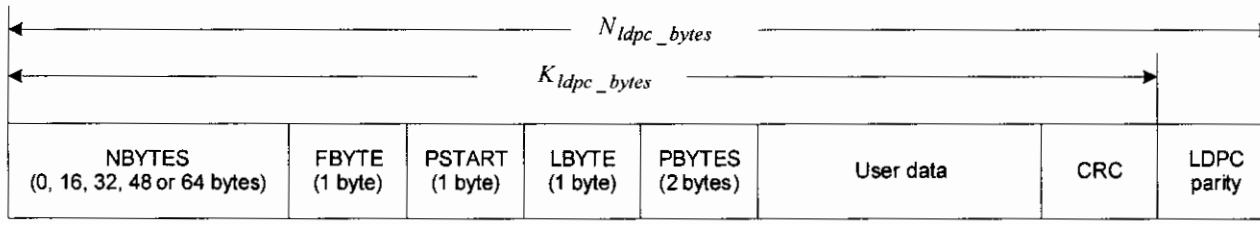


Figure 66

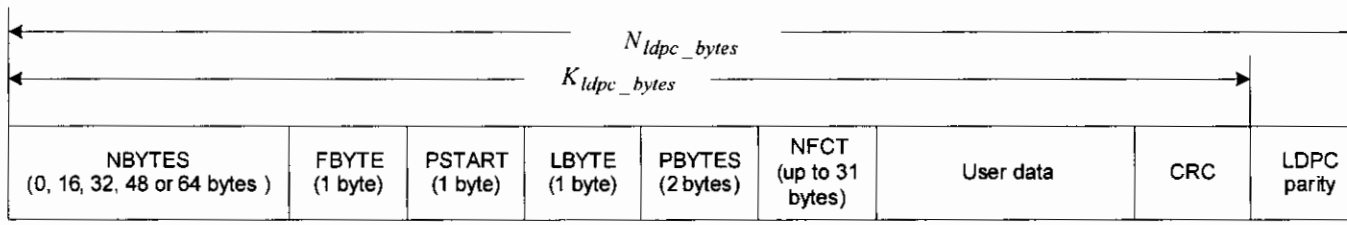


Figure 77

Codeword_ count_ in_next_fram	Reserved	$i = 0$					.....	i = codeword_count_in_next_frame				
		Status	Mod	Code rate	Pad	Resv		Status	Mod	Code rate	Pad	Resv
4 bits	4 bits	1 bit	2 bits	4 bits	16 bits	1 bit		1 bit	2 bits	4 bits	16 bits	1 bit
1 byte		3 bytes						3 bytes				

Figure 88

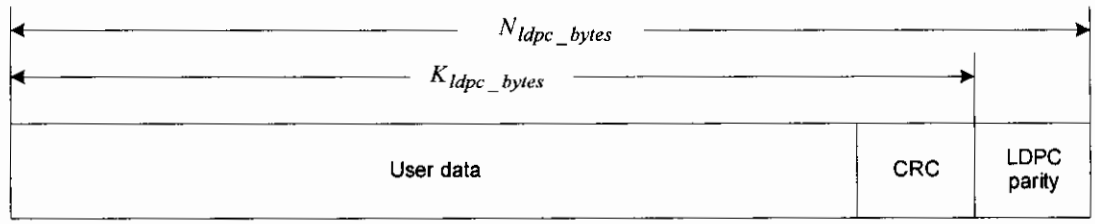


Figure 99

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va-173550