

A CIRCUIT FOR COMPRESSION
OR EXPANSION OF AN ELECTRICAL SIGNAL

The invention relates to a circuit for compression or expansion of an electrical signal.

Comanders are used to improve the signal-to-noise ratio during signal transmission across long distances and during signal storage. The mode of operation consists of raising or emphasizing weak signals in terms of their level before transmission or storage and in lowering or de-emphasizing them after transmission or reproduction. Thus interference signals also on the transmission paths are also lowered or de-emphasized. Moreover, interference signals may be obscured by useful signals to a greater or lesser extent. This obscuring or masking effect is a physiological phenomenon and depends on how much the useful signal and the interference signal differ in frequency from each other. Therefore it is advisable to subdivide the low frequency spectrum into several frequency selective channels and to control the effective transmission factor in

these channels only in dependence on the useful signals occurring therein. A masking effect which would be sufficient for professional purposes may be achieved with four frequency selective channels. For semi-professional and consumer purposes, a lower cost is desirable. Considering that the cost is approximately proportional to the number of channels the restriction to two channels is sensible for semi-professional purposes.

However, it has proved that one such compander, which was conceived according to the principles of a four-channel compander - with the exception of the crossover frequencies - already has a noise effect in the case of certain extreme signal performance, e.g. strong spectrally pure signals in the base note range. If it were only important to process these signals free of noise then the crossover frequency of the two compander channels could be lowered. However, this results in unfavourable noise characteristics in the descant range.

According to a first aspect of the present invention there is provided a compander circuit comprising at least two frequency channels containing adjusting elements as effective transmission factor controlling means, and a combining circuit for the output of the channels, only the lower frequency channel comprising means for emphasising higher frequency signals during compression of electrical signals, and

3.

means for de-emphasising higher frequency signals during expansion of electrical signals.

In a preferred embodiment the means for emphasising higher frequency signals and the means for de-emphasising higher frequency signals are constituted by a single network. During compression of electrical signals said network may be arranged in a negative feedback path between an output and an inverting input of a differential amplifier and during expansion of electrical signals said network may be arranged in a signal path in series with said differential amplifier. In a modification a single high-pass network may be arranged during expansion of electrical signals in a negative feedback path between an output and an inverting input of a differential amplifier and during compression of electrical signals arranged in a signal path in series with said differential amplifier.

The following considerations take part in the solution in accordance with the circuit of the present invention. The noise output allocated to a frequency interval increases as the frequency increases. The amplitude statistics of conventional audio signals show a lower amplitude path in the middle and

4.

high frequency range than in the lower frequency range.

It was discovered that the higher frequency signal components are also weaker in the low frequency compander channel than are the low frequency signal components. From these considerations the conclusion was drawn that a higher suppression of noise may be achieved if signal companding takes place in the upper frequency range of the low frequency channel in the case of smaller signal amplitudes than in the low frequency range. An increase in noise pumping in the low frequency range due to this measure should not be expected effectively because of the weaker high frequency signal components.

A preferred embodiment of the present invention will now be described, by way of example only, with reference to the accompanying figure of the drawing which shows a compander circuit in accordance with the present invention.

The drawing shows a circuit for optional automatic dynamic range compression or expansion with subdivision of the frequency spectrum into two channels. The input signals are supplied to the compander via an input terminal 1 and are applied to the non-inverting input 5 of a differential amplifier 4 via a capacitor 2. A resistor 3 is arranged between the non-inverting input 5 and earth and serves to set a certain input resistance. A negative feedback path is supplied from the output 7 of the differential amplifier 4

5.

to the inverting input 6. In the "compression" type of operation the negative feedback path for the upper frequency band passes via a controllable filter arrangement 16 and for the lower frequency band via a controllable filter arrangement 22 to a combining circuit 38. The combining circuit 38 is formed as a non-inverting summing amplifier. The negative feedback path continues from the output of the combining circuit via a capacitor 15, a first switch 12, a second switch 10 and a further capacitor 9, to the inverting input 6 of the differential amplifier 4. A direct negative feedback signal path of the amplifier 4 is interrupted by the low-resistance output of the combining circuit 38 by means of a resistor 8. This path only serves additionally to set the operating point.

The signal path runs from the output of the differential amplifier 4 via a capacitor 11 to an output terminal 14. In the "compression" type of operation the compressed useful signals may be tapped at this output terminal. In the "expansion" type of operation the switch 10 is put into the position which is not shown in the drawing. The negative feedback path of the differential amplifier 4 then passes from the output 7 via the resistor 8 to the inverting input 6. The circuit elements which lie in the negative feedback

path during the "compression" type

of operation are arranged in the signal path during "expansion".

The signal path runs from the output 7 of the differential amplifier 4 via the controllable filter arrangements 16, 22 already mentioned to the combining circuit 38 during the "expansion" type of operation. The signal path runs from the output of the combining circuit via the capacitor 15 and the switch 12 to an output terminal 13.

If the switch 12 is brought into the position which is not shown in the drawings then the compander is ineffective and the input signals applied to the input terminal 1 are passed to the output terminals 13 and 14 at constant amplification via the differential amplifier 4 and the capacitor 11.

Control of the effective transmission factor in each controllable filter takes place via a branch path associated with the filter arrangements in each case, said branch path having a control voltage generator. A branch path 21 is provided for the high frequency channel and a branch path 31 is provided for the low frequency channel. The branch paths are constructed largely the same except for weighting filters applied to their inputs and the control voltage generators and the time constants in the rectifiers which serve for generating control voltage. The construction is illustrated here at the branch path for the low frequency channel. The signals reduced in

dynamic range are supplied via the weighting filter 32 and a trimming potentiometer 33 to the input of the controllable amplifier 34. This amplifier may be controlled in amplification as may the corresponding controllable filter 22 by means of a variable resistor 35. Amplification is controlled so that the signals applied to the input terminal 1 are reduced at the input of the branch path 31 in the stationary condition to half the value of the dynamic range and are reduced after the amplifier 34 to the dynamic value zero. The output signals of the amplifier 34 are supplied via a weighting filter 36 to a control voltage generator 37.

The control voltage generator 37 is connected to the control inputs of the variable resistor 35 and a variable resistor 30 arranged in the controllable filter 22 and controls the resistors in the sense of a reduction in resistance as the useful signal amplitude increases. The two controllable filter arrangements subdivide the frequency spectrum into two frequency bands. The frequency characteristics are preferably so dimensioned that the frequency responses of adjacent channels intersect at -3 dB.

The channel for the high frequency band 16 contains a high-pass filter comprising a capacitor 17 and a series connection of a resistor 18 and 20. The signal is tapped from the common connection point of the resistors 18 and 20 on the one hand for the branch path 21 and on the other hand the signal is supplied direct to a variable resistor 19 which serves as an adjusting

element. The variable resistor 19 is connected at its other end to the input of the combining circuit 38. The voltage divider used for matching the level of the signal to the adjusting element comprising resistors 18 and 20 is included in the frequency-determining high-pass filter.

The low frequency channel contains a low-pass filter corresponding to the high pass filter 17, 18, 20 for the purpose of separating the channel and the said low-pass filter comprises a resistor 24 and a capacitor 23. A voltage follower 25 which serves as a separating amplifier is connected to the low-pass filter. The signal for the branch path 31 is tapped from the output of the separating amplifier 25. The signal of amplifier 25 is also supplied to a network, which is also formed as a low-pass filter and has resistors 26 and 28 as well as a capacitor 27. This network has a lower limit frequency than the filter 23, 24 and serves for emphasis in the "compression" type of operation and for de-emphasis in the "expansion" type of operation. The complementary frequency characteristics required during "compression" are achieved by arranging the network in the negative feedback path of the amplifier 4 during "compression". The connection point of the resistor 26 with the capacitor 27 is connected via an impedance converter 29 and a damping element 39, 40 designed for matching the level corresponding to the damping element 18, 20 in the high frequency channel to one connection to a variable resistor 30.

The other connection of the variable resistor 30 is connected to the combining circuit 38. The combining circuit 38 is formed as a non-inverting summing amplifier in order to balance out again the damping caused by the controllable filters.

The weighting filter 36 has the same frequency characteristics as the network 26, 27, 28 operating in the negative feedback operation. Thus the same frequency characteristics for the controlled distributed amplifier 4 (in the low frequency channel) and 34 are achieved. This is in the interests of a straight and parallel path of the frequency-dependent characteristic groups in the compression-expansion diagram. It is not necessary for the filter 36 to be immediately in front of the rectifier 37; it may alternatively be arranged in front of the amplifier 34 or may be formed jointly with the filter 32 which causes channel separation.

An advantage of the above described circuit is that based on subdivision of the transmission range into two frequency ranges with subjectively approximately the same noise output, it improves the process of freeing low frequency signals of interference and noise.



19

CLAIMS

1. A compander circuit comprising at least two frequency channels containing adjusting elements as effective transmission factor controlling means, and a combining circuit for the output of the channels, only the lower frequency channel comprising means for emphasising higher frequency signals during compression of electrical signals, and means for de-emphasising higher frequency signals during expansion of electrical signals.
2. A circuit according to Claim 1, wherein the lower frequency channel contains a higher frequency emphasis network in the compression mode of operation and a higher frequency de-emphasis network in the expansion mode of operation.
3. A circuit according to Claims 1 or 2, having control voltage regulator means comprising a series connection of a controllable amplifier and a rectifier circuit connected to said adjusting elements.
4. A circuit according to Claims 2 and 3, having a

network similar to said higher frequency emphasis network connected to the rectifier circuit as a weighting filter.

5. A circuit according to any preceding Claim wherein the means for emphasising higher frequency signals and the means for de-emphasising higher frequency signals are constituted by a single network.

6. A circuit according to Claim 5, wherein during compression of electrical signals said network is arranged in a negative feedback path between an output and an inverting input of a differential amplifier and during expansion of electrical signals said network is arranged in a signal path in series with said differential amplifier.

7. A circuit according to any of Claims 1 to 4, wherein a single network serves for emphasis and de-emphasis, the network being arranged during expansion of electrical signals in the negative feedback path between an output and an inverting input of a differential amplifier and during compression of electrical signals being arranged in a signal path in series with said differential amplifier.

8. A circuit substantially as herein described with reference to the accompanying drawing.

The text of the specification has been reproduced by photocopying the applicants original typescript. It may contain a few amendments which are difficult to read. The original typescript containing these amendments may be inspected on the premises of the Patent Office