

ABSTRACT

A parabolic voltage generating circuit includes an arrangement (IC1) for generating a sawtooth vertical deflection current (i_V) that flows in a vertical deflection winding (L_V), in a DC blocking capacitor (C_V) and in a deflection current sampling resistor (R_S). An emitter electrode of a transistor (Q3) is coupled between the deflection winding and the capacitor. A base electrode of the transistor is coupled between the capacitor and the resistance. A collector current (i_C) of the transistor varies in a vertical rate parabolic manner and has a DC average value that is determined by a DC average value of a voltage that is developed in the capacitor. The collector current is applied to an East-West raster distortion correction circuit for modulating an amplitude of a horizontal deflection current.

This Sheet Has Been Replaced