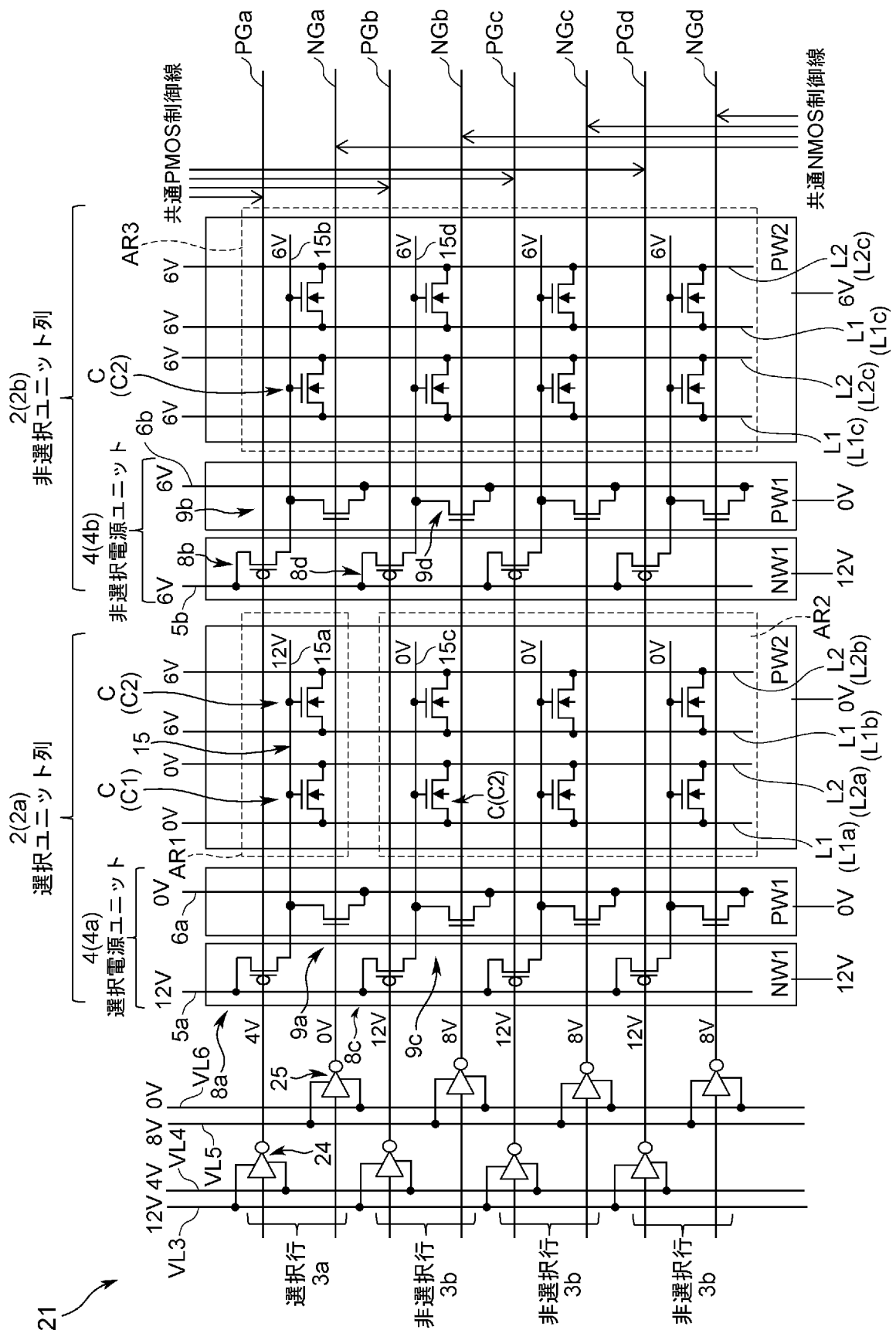
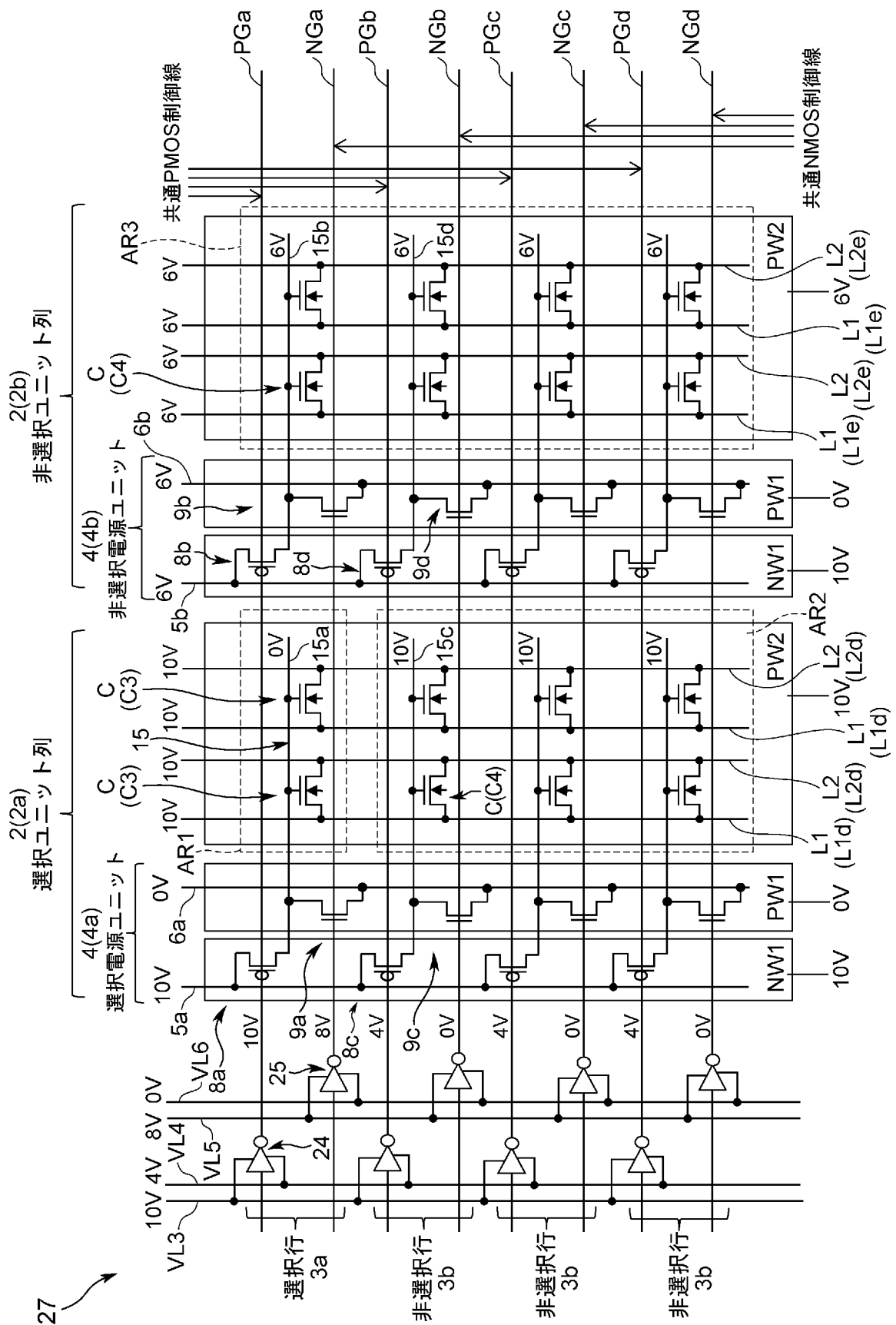




21 



[図3]





[図5]

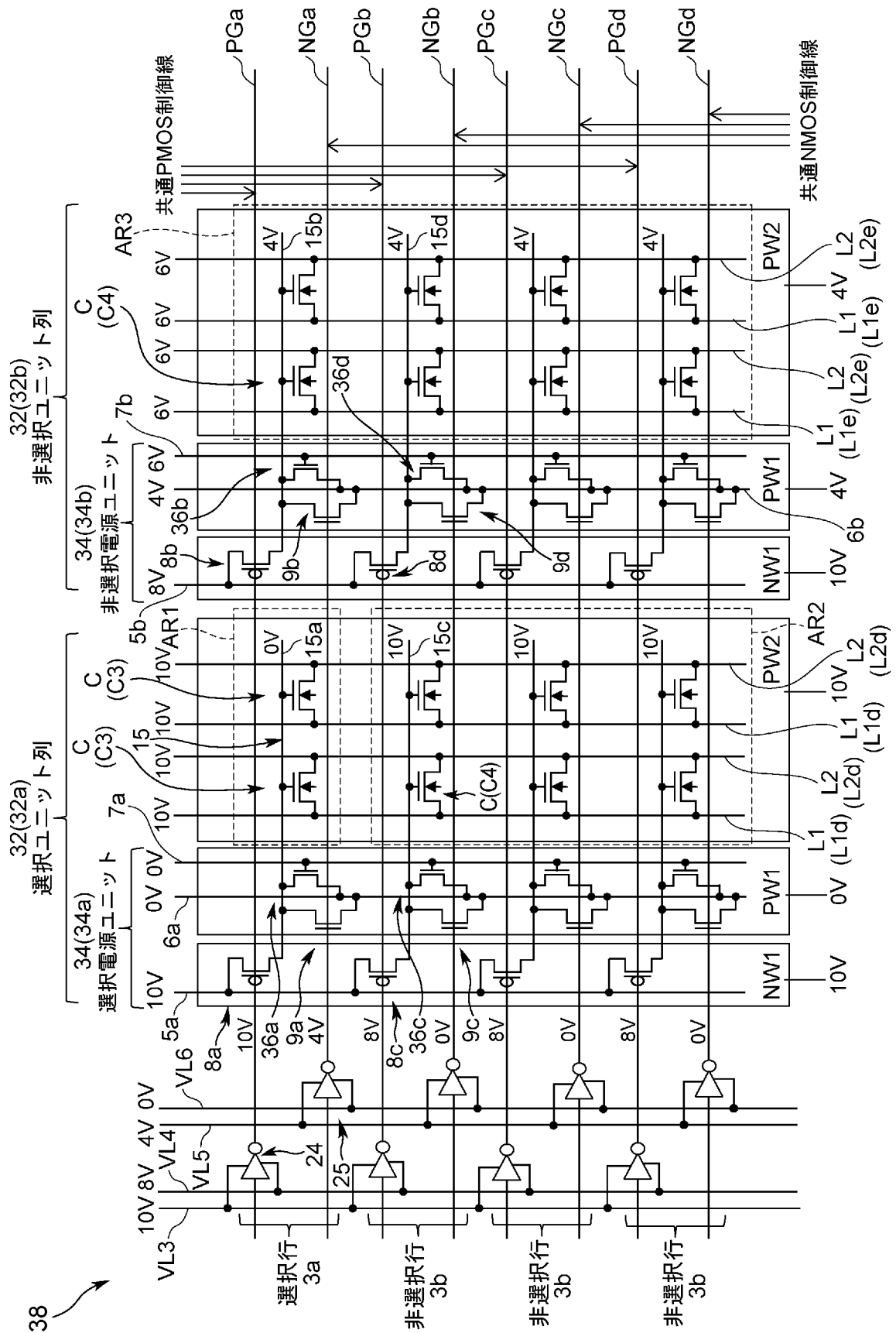
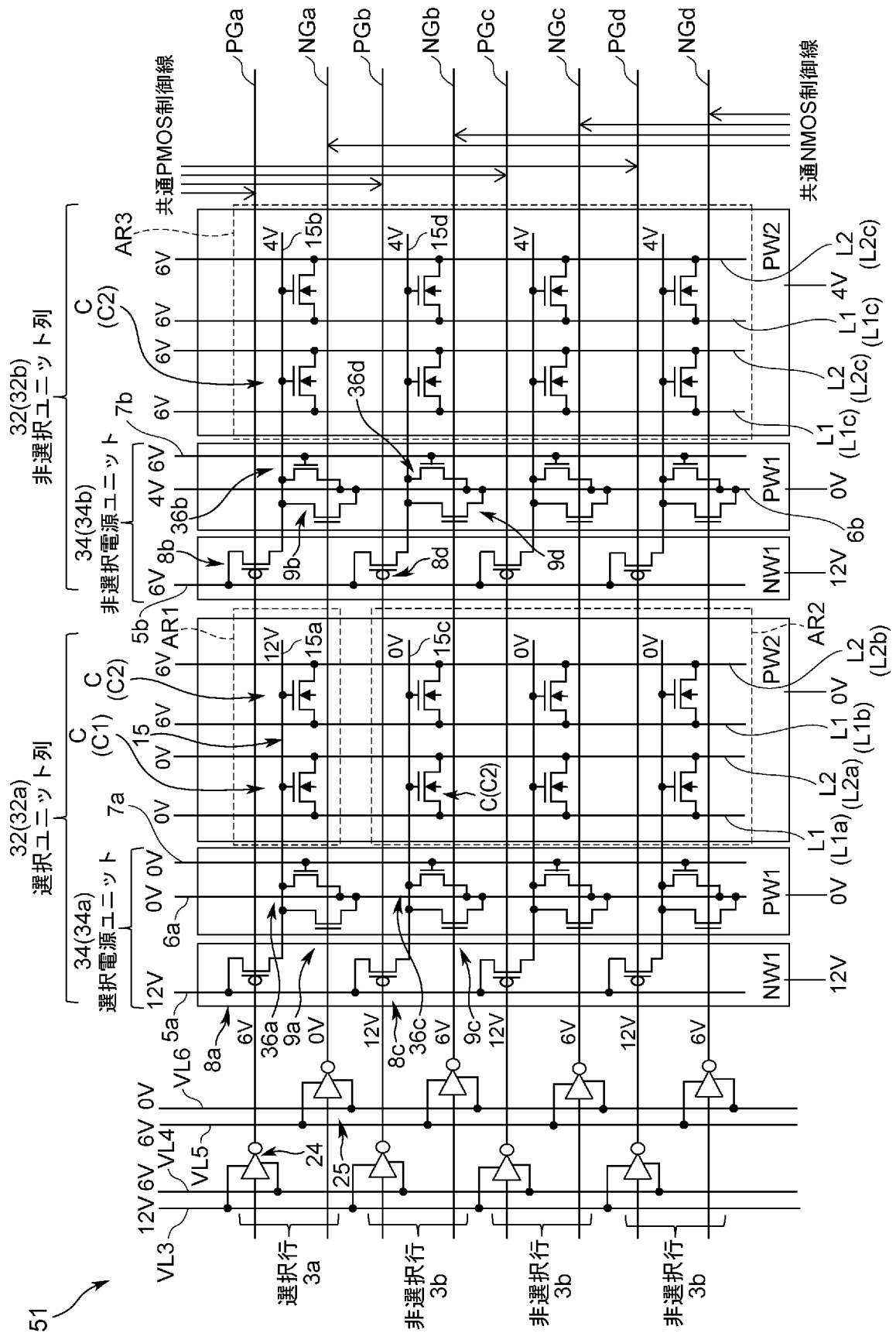
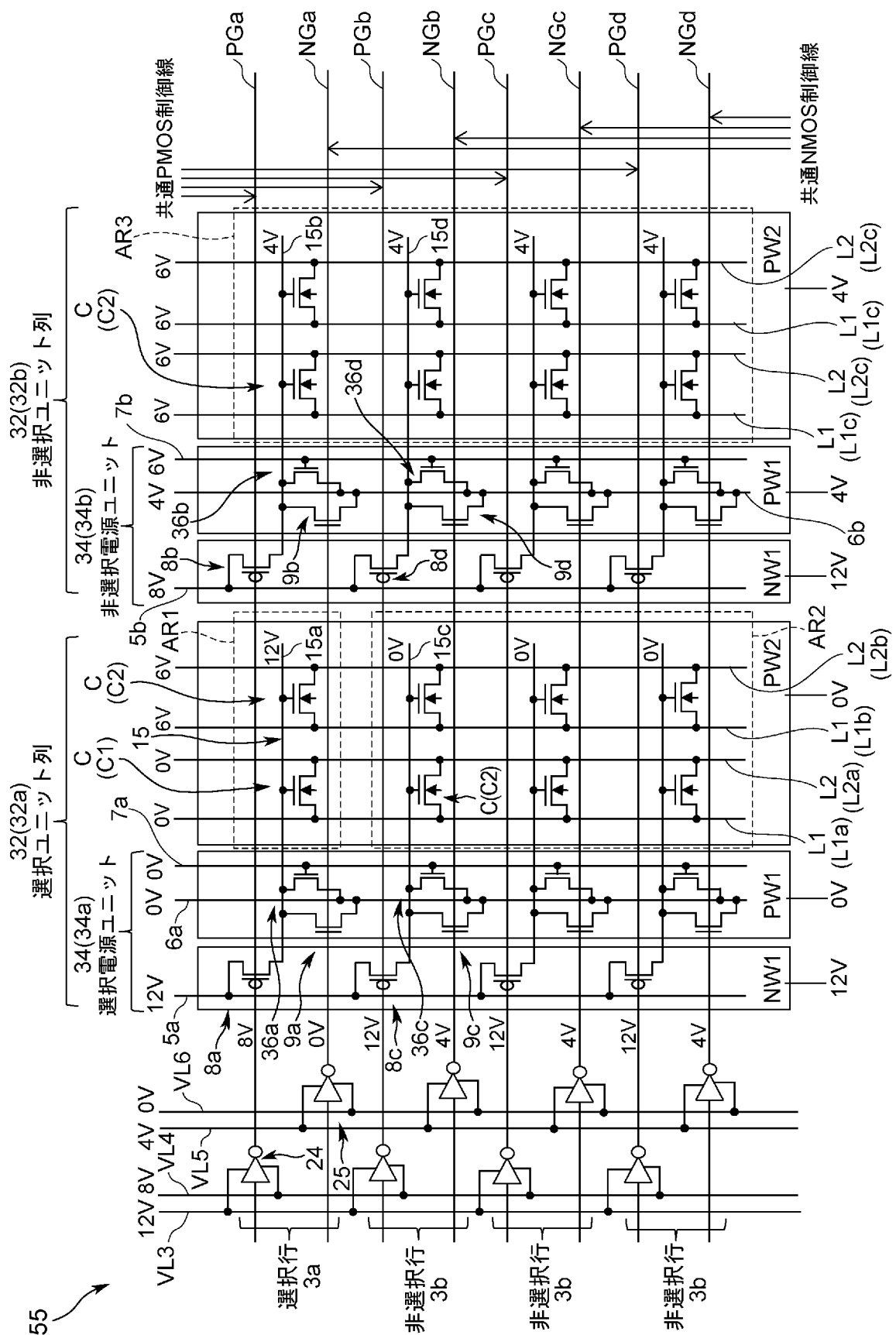


Figure 1 is a schematic diagram of a semiconductor device 41. The device is divided into a selection unit 45a and a non-selection unit 45b. The selection unit 45a includes a selection source unit 44(44a) and a selection unit 46a. The non-selection unit 45b includes a non-selection source unit 44(44b) and a non-selection unit 46b. The selection unit 46a includes a selection unit 46a and a non-selection unit 46b. The non-selection unit 46b includes a selection unit 46a and a non-selection unit 46b. The device is controlled by a common PMOS control line. The diagram shows various transistors, capacitors, and interconnects, with labels for various nodes and signals.

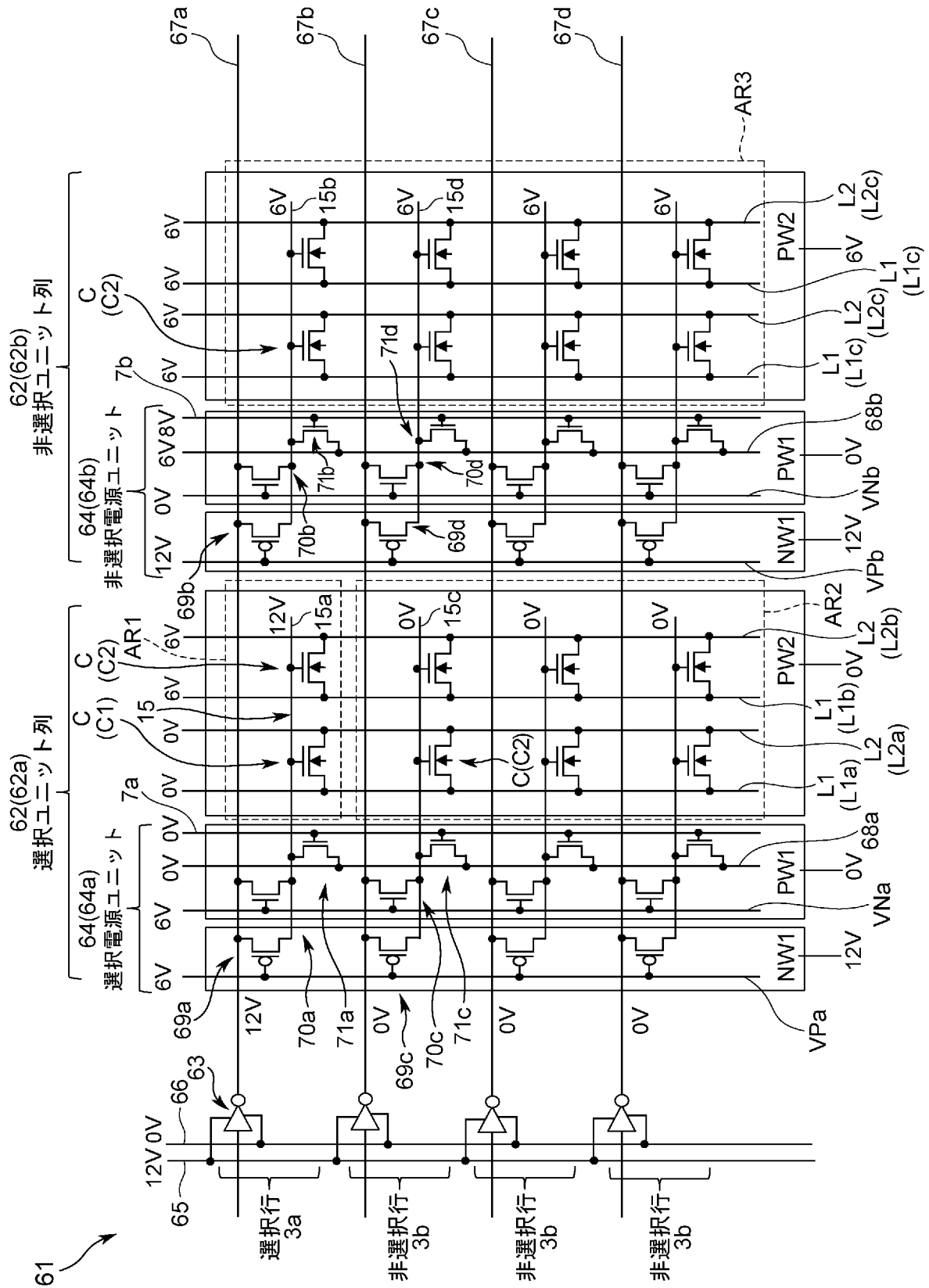
[図7]



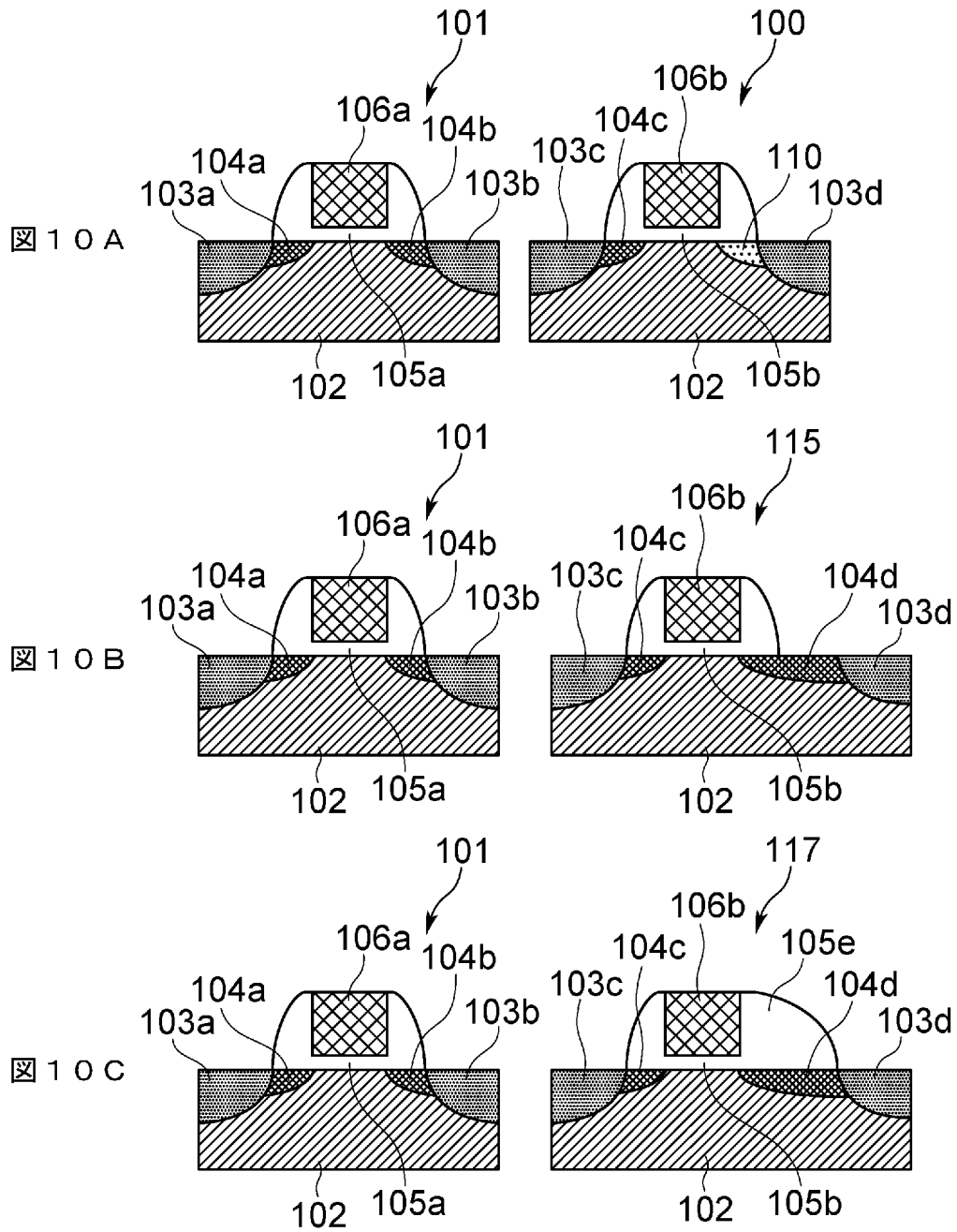
55 



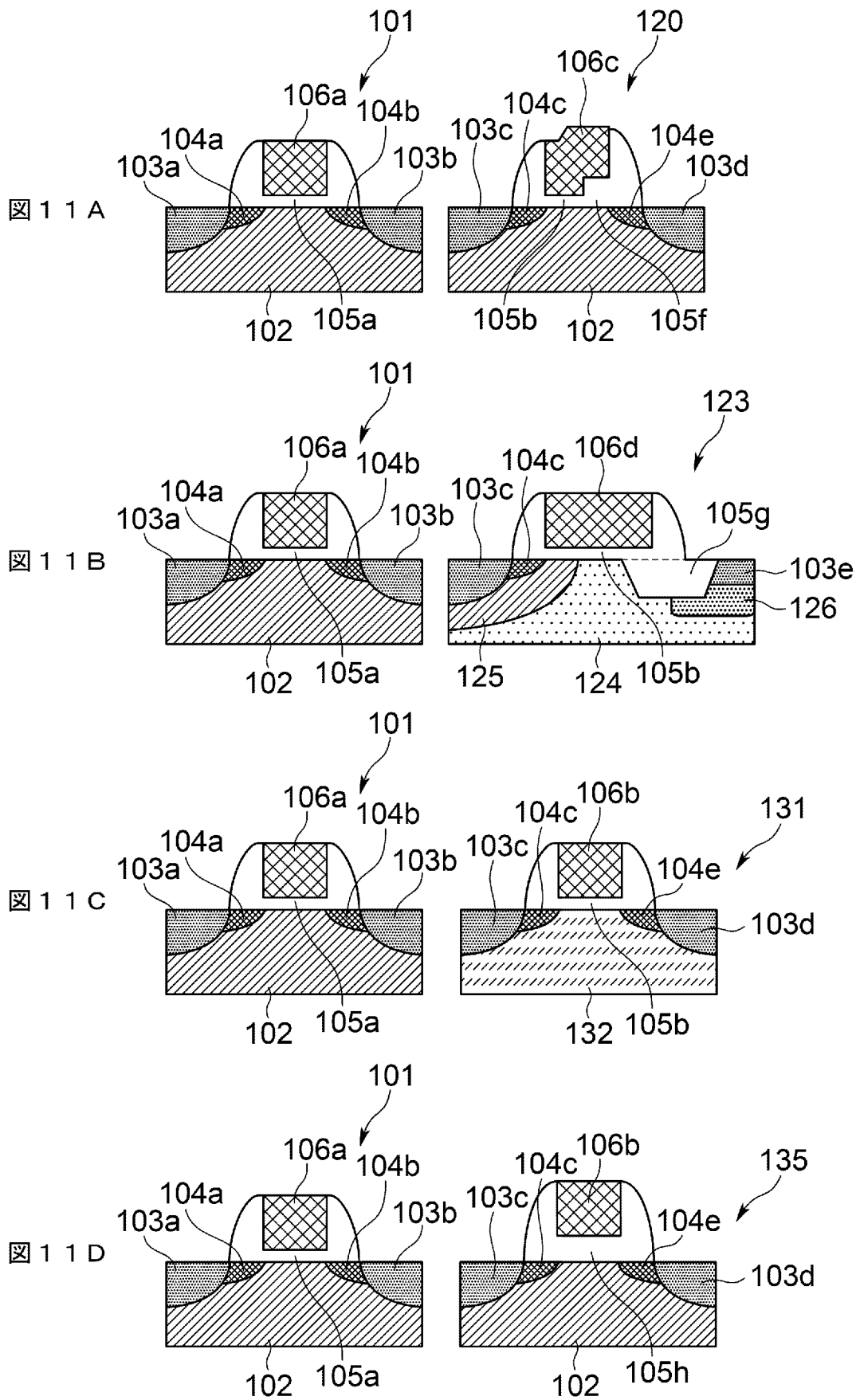
[図9]



[図10]



[図11]





[図13]

