(6)

Abstract of the Disclosure

A progressive scan processor (30) includes an input circuit (30A) which produces a video difference signal (Y12) representative of a difference between a first low frequency component (Y6) derived from a current line of a video input signal (Y3) and a second low frequency component (Y10) derived by motion adaptive processing (42-50) from a selected previous line of the video input signal. A line-rate doubling output circuit (30B) combines a line of the double line-rate difference signal (Y14) with every other line of the double line-rate input signal (Y4) to form a progressively scanned output signal (Y2). Advantageously, the system exhibits the relatively high vertical resolution and motion artifact immunity characteristic of motion adaptive systems as well as substantially reduced system memory requirements characteristic of "dual band" processors and it provides "dual band" processing of high and low frequency video components without the need for complementary band splitting filters having special amplitude and phase response characteristics.

This Sheet Has Been Replaced

