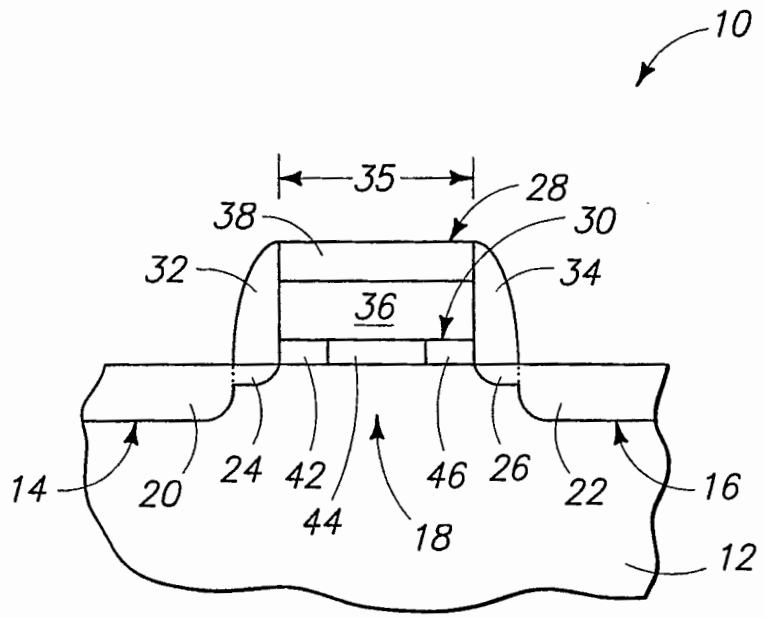

10
35
28
38
30
32
34
36
14
20
24
42
44
46
26
22
16
18
12

Fig. 1

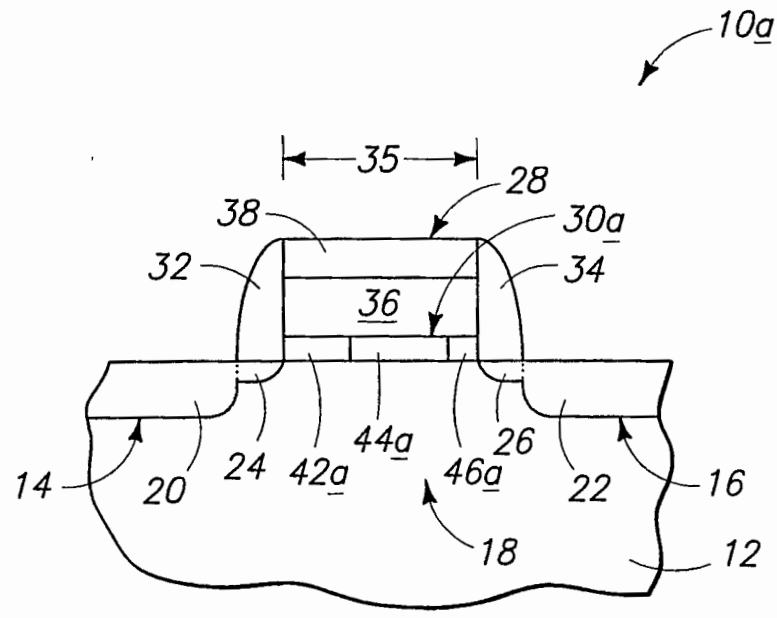

10a
35
28
38
30a
32
34
36
14
20
24
42a
44a
46a
26
22
16
18
12

Fig. 2

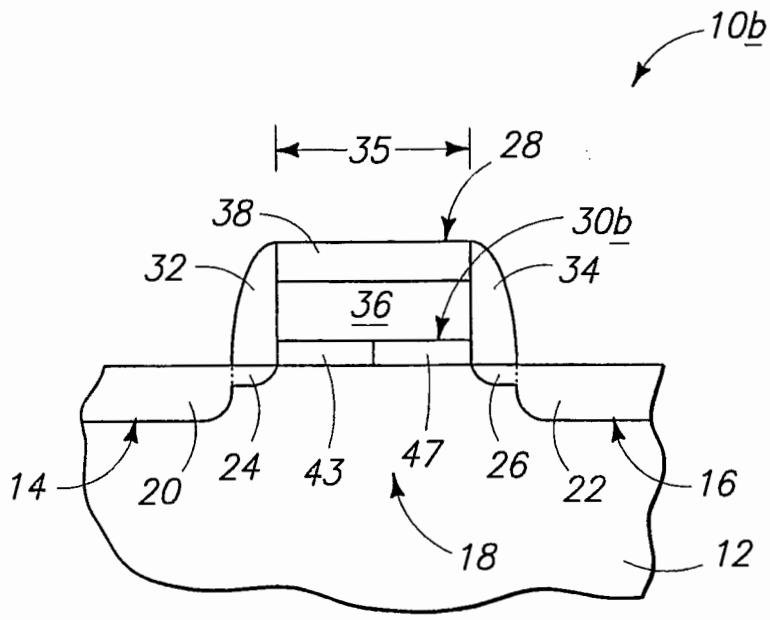

10b
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30b
32
34
36
14
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24
43
47
26
22
16
18
12

Fig. 3

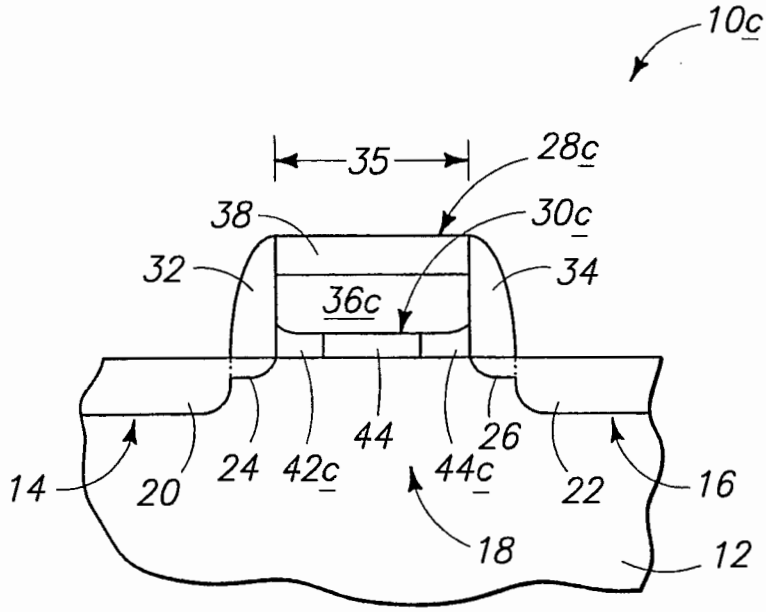

10c
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28c
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30c
32
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36c
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42c
44
44c
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22
16
18
12

Fig. 4

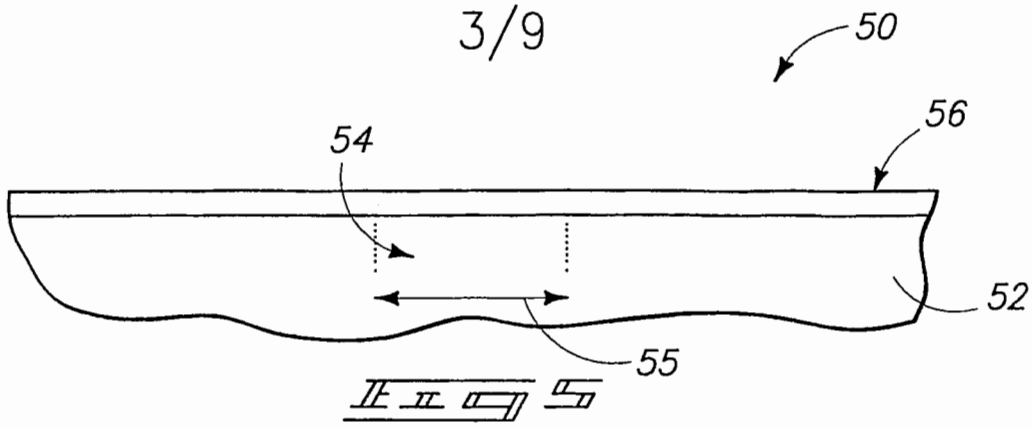


Fig 5

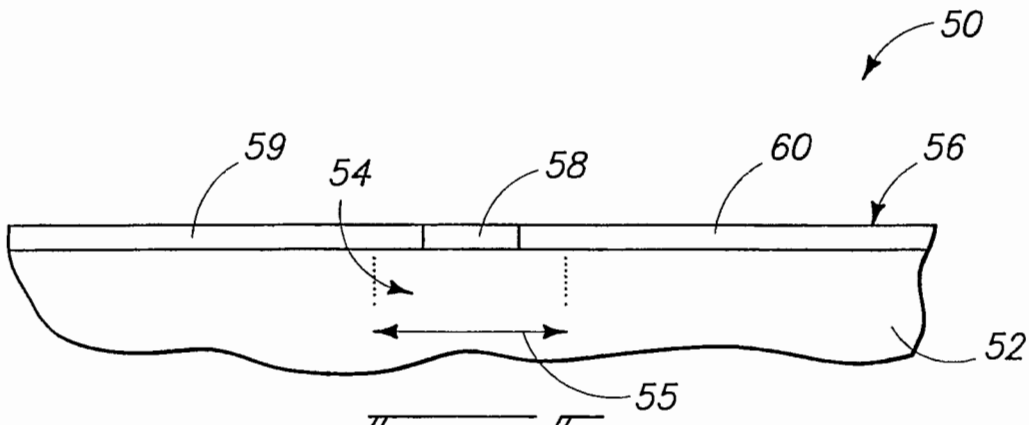


Fig 6

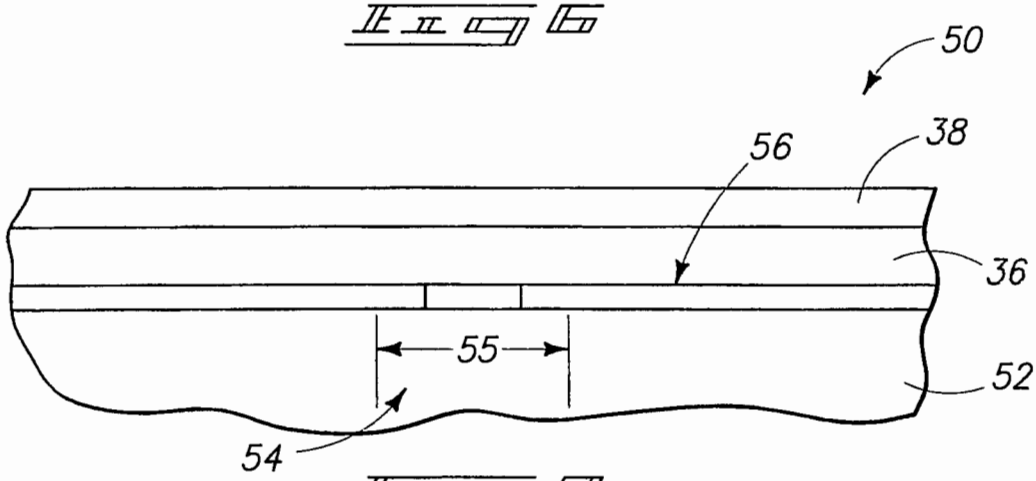


Fig 7

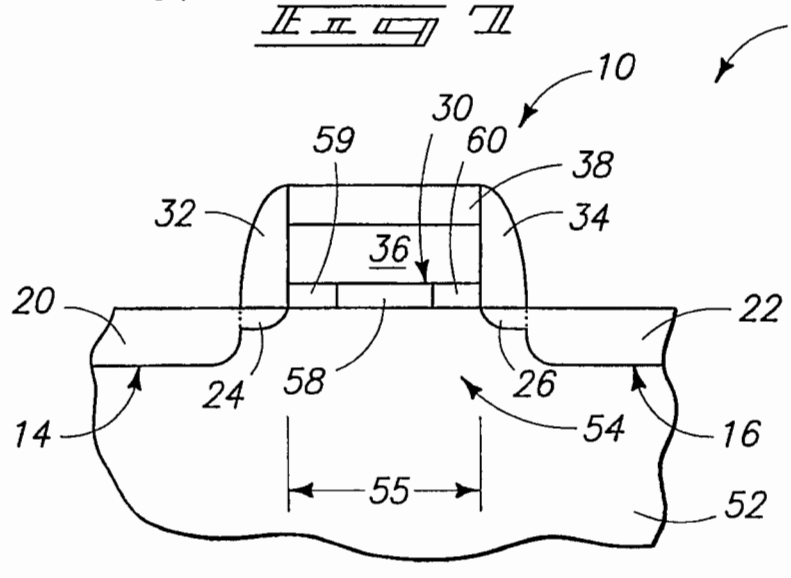


Fig 8

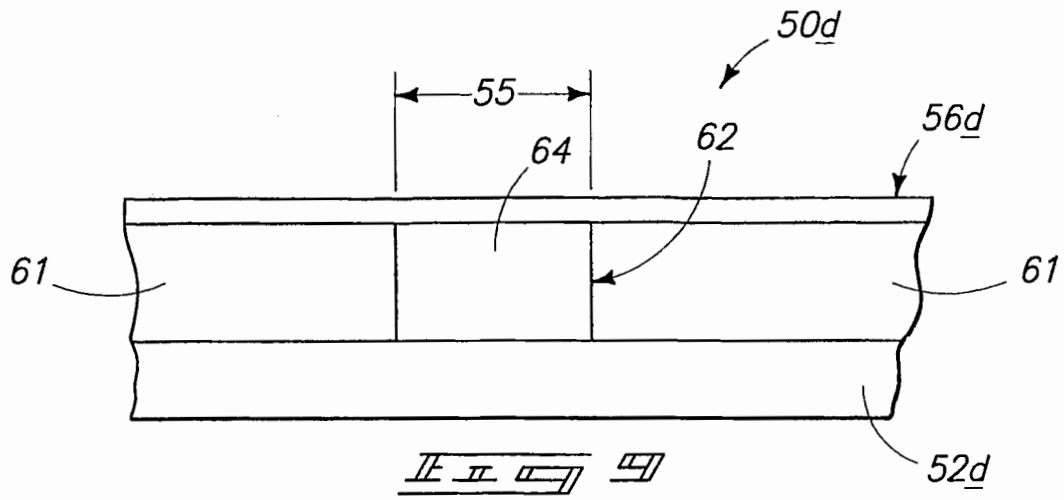


Fig 9

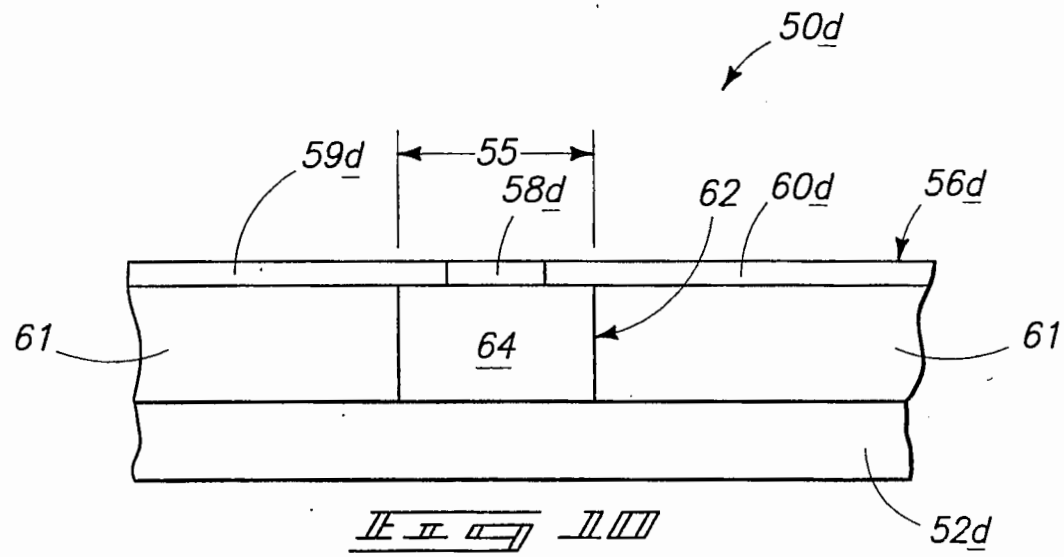


Fig 10

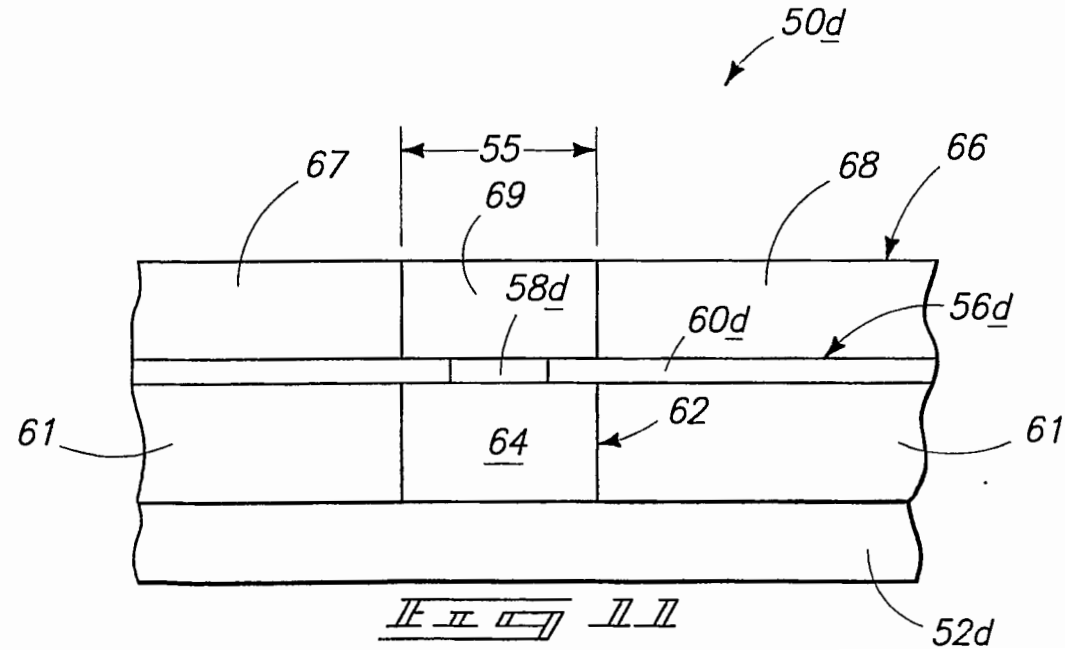


Fig 11

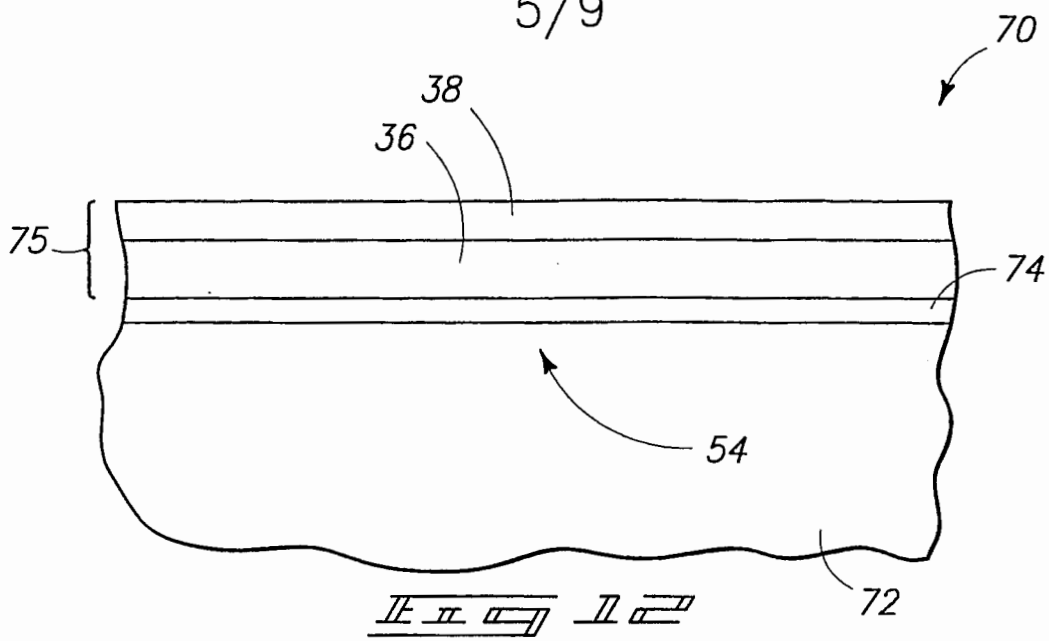


Fig. 12

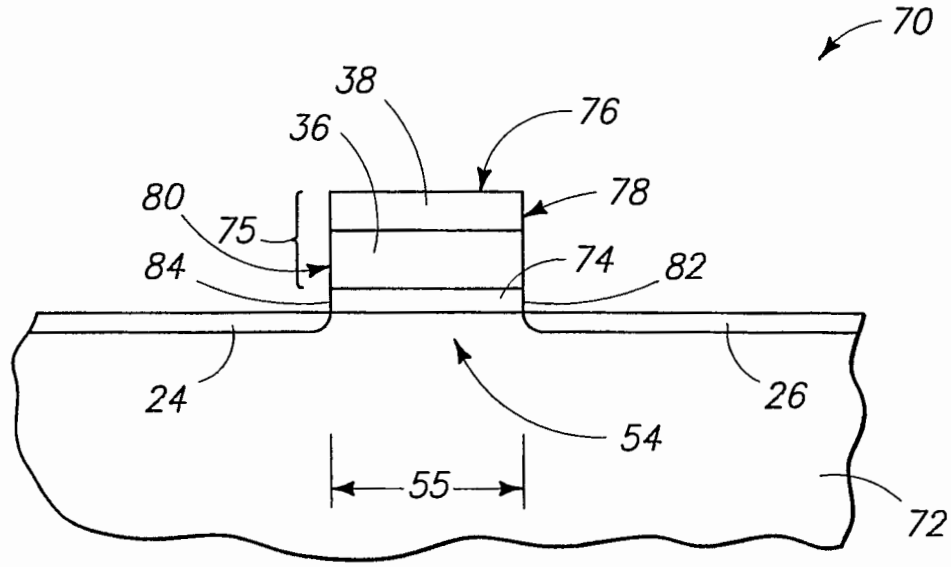


Fig. 13

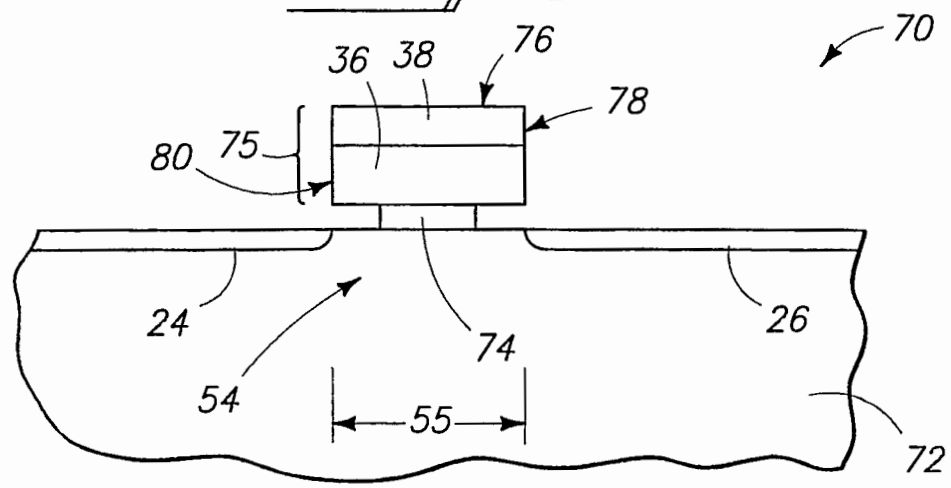


Fig. 14

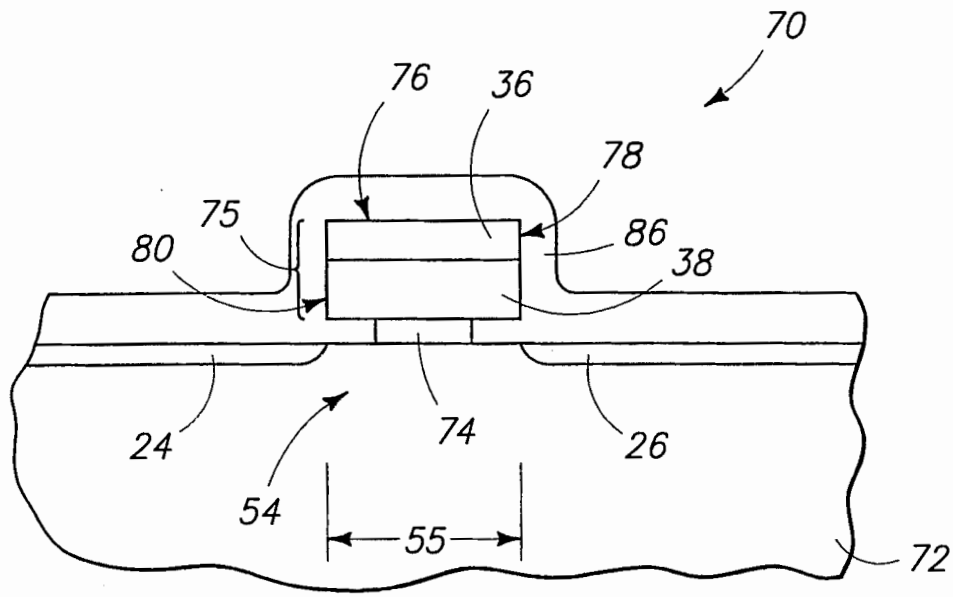

70
76
36
78
75
80
86
38
24
74
26
54
55
72

Fig 15

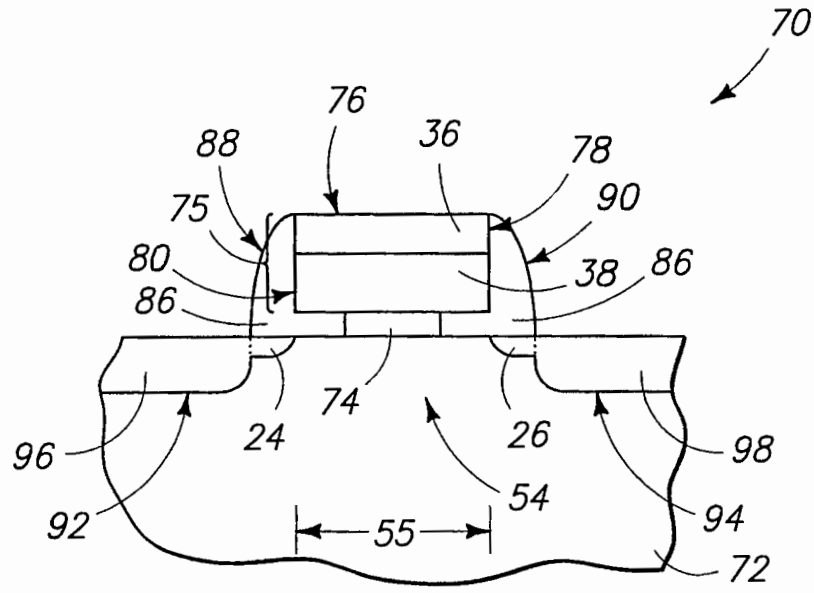

70
76
88
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38
86
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74
24
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96
98
92
54
94
55
72

Fig 16

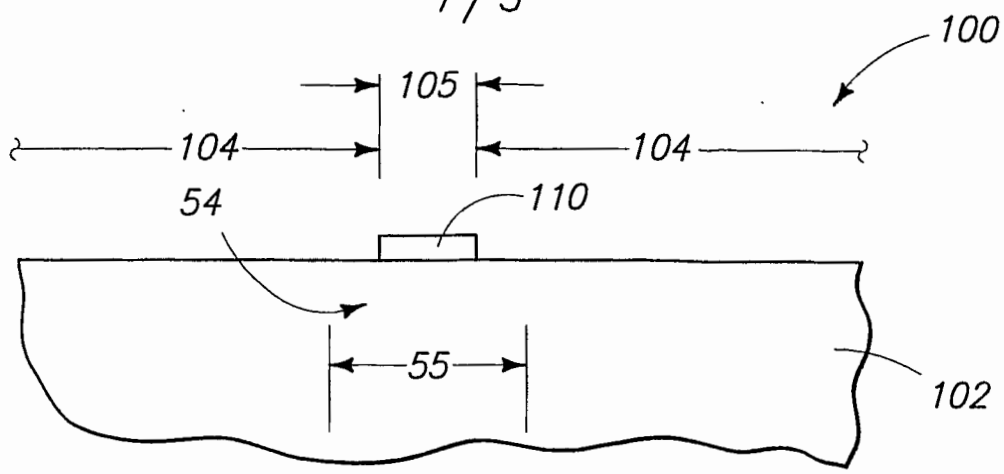


Fig 11

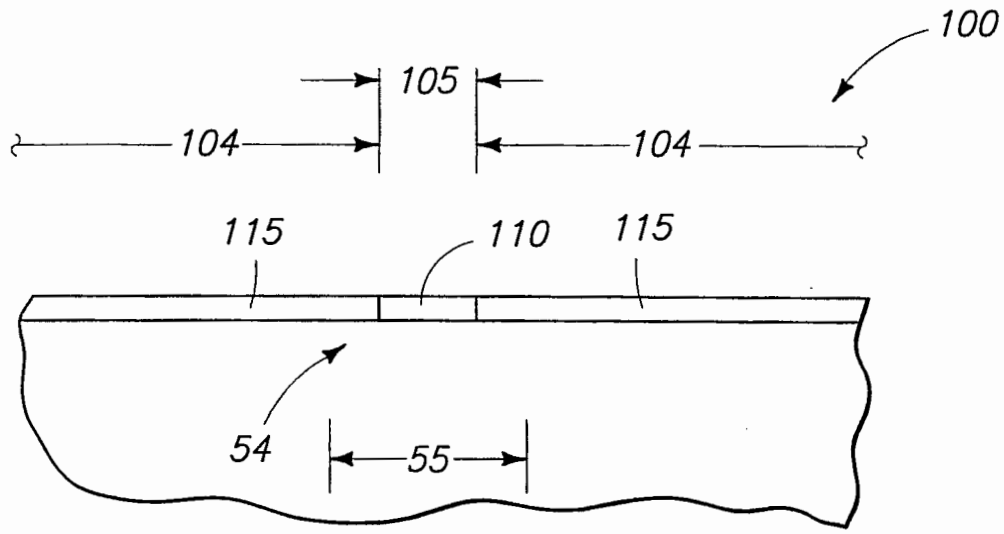


Fig 11B

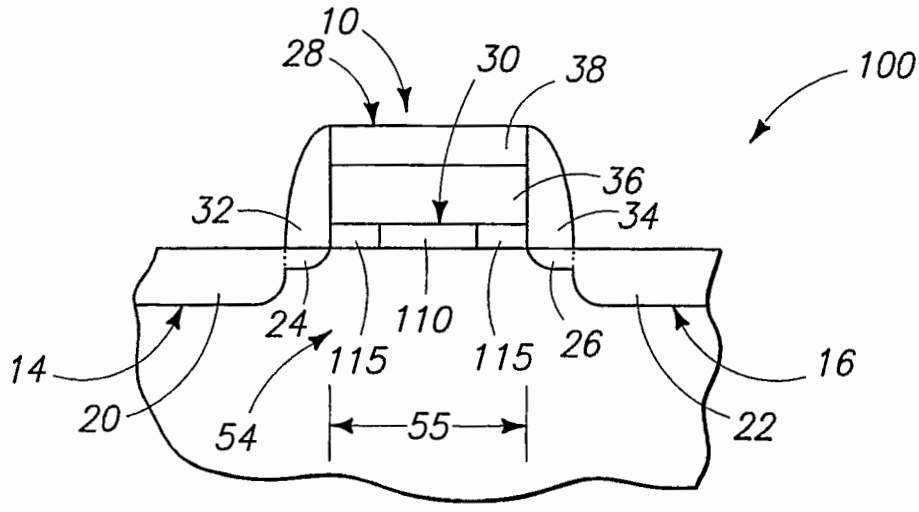


Fig 19

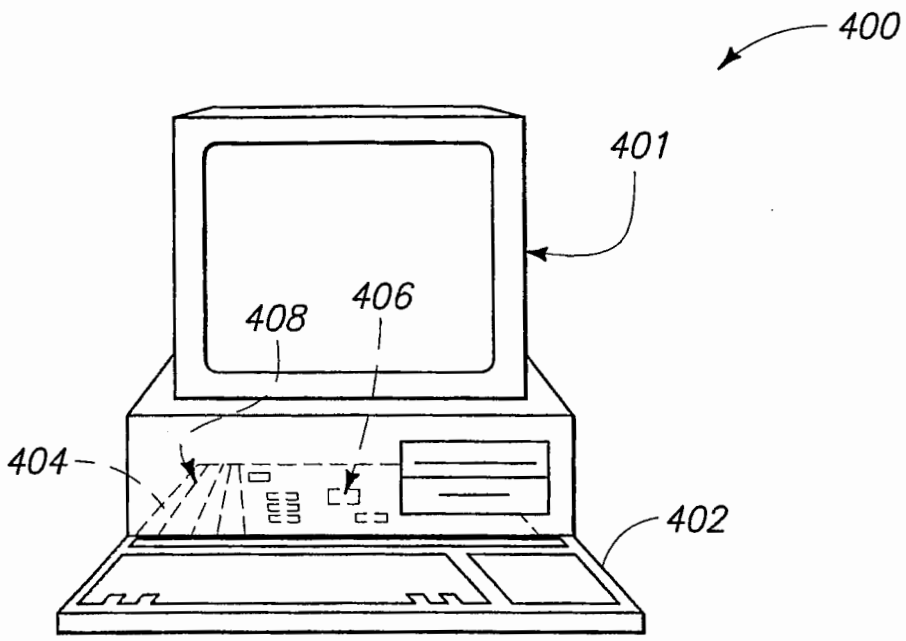


Fig 20

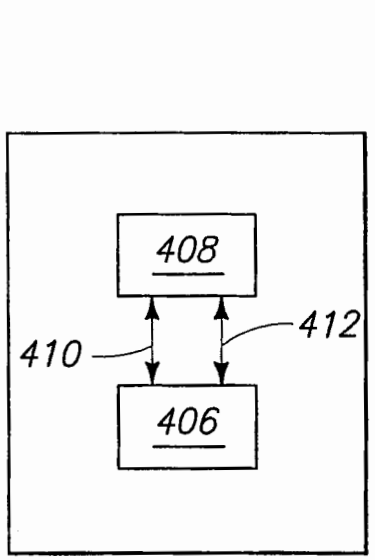


Fig 21

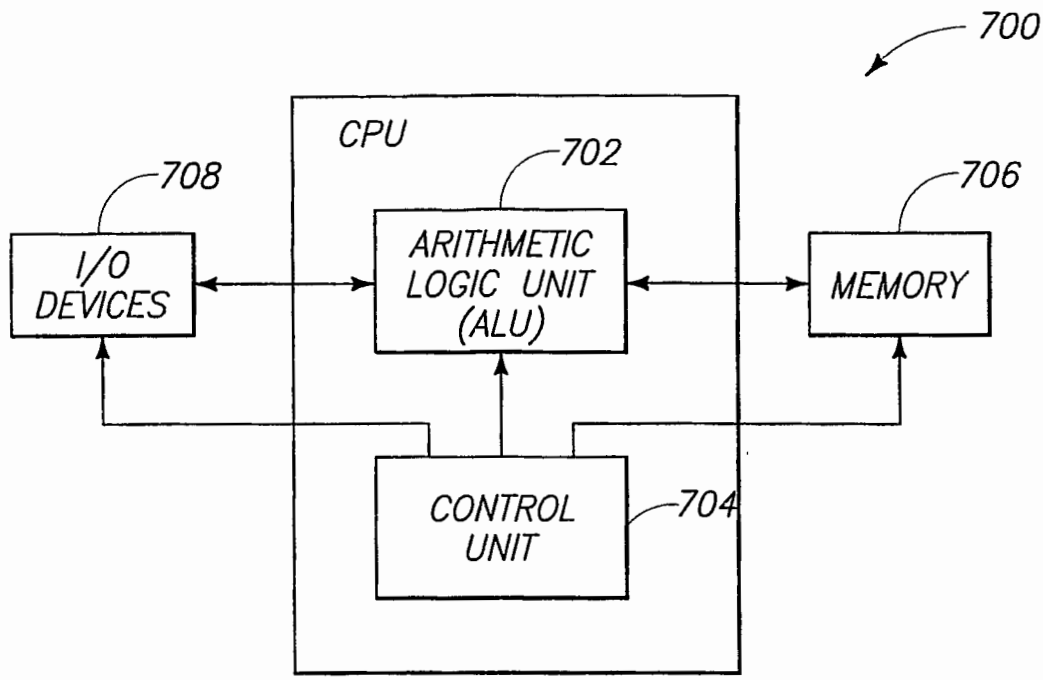

700
CPU
708
I/O DEVICES
702
ARITHMETIC LOGIC UNIT (ALU)
706
MEMORY
CONTROL UNIT
704

Fig 2

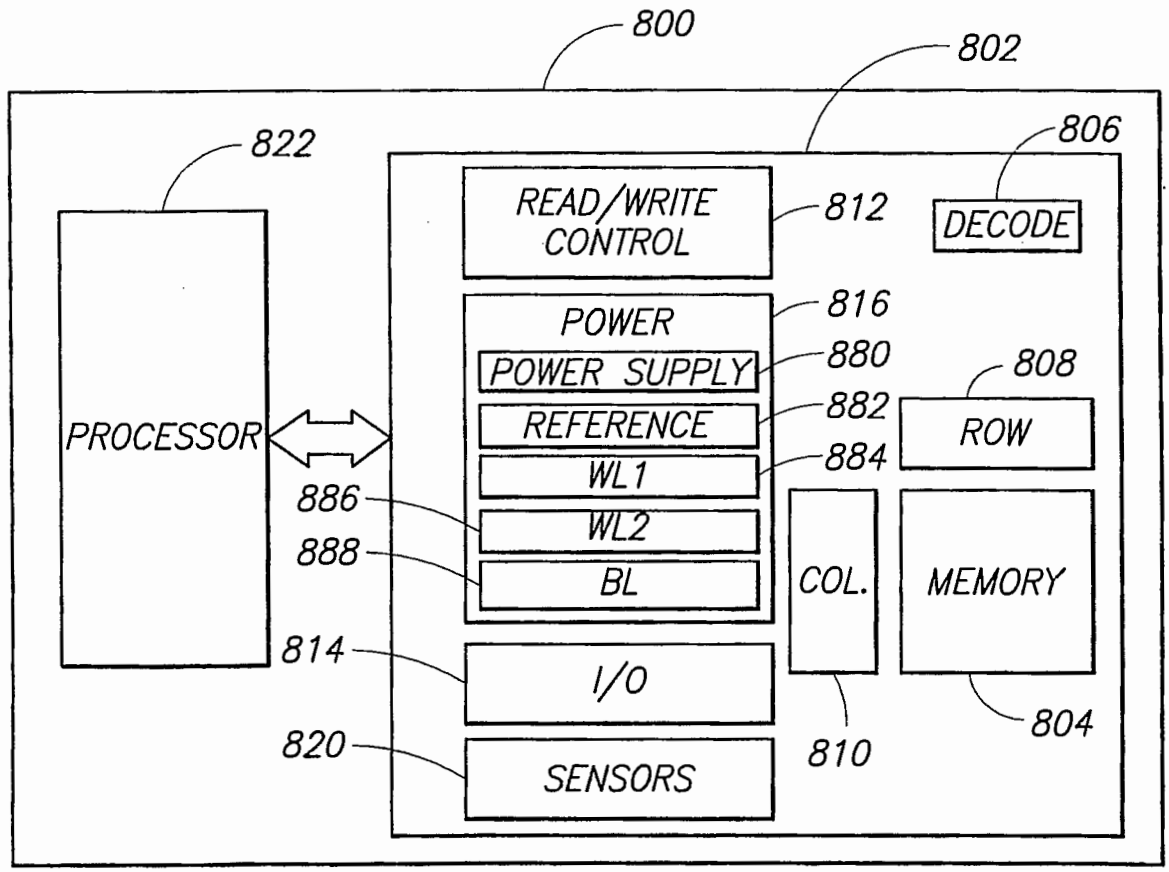

800
802
822
PROCESSOR
READ/WRITE CONTROL
812
DECODE
806
POWER
816
POWER SUPPLY
880
REFERENCE
882
WL1
884
WL2
886
BL
888
808
ROW
COL.
MEMORY
804
I/O
814
SENSORS
820
810

Fig 3