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TIMEPIECE HAVING A POWER RESERVE INDICATOR

The present invention relates to a device for indicating the power reserve of an electronic timepiece.

More specifically, the invention concerns such a device for an electronic timepiece in which the electric energy is generated by a generator driven by a winding barrel spring mechanism.

For a long time mechanical watches have been known which are provided with a device for indicating the power reserve which functions generally in a differential manner and which is coupled to the winding device as well as the barrel containing the spring of the watch, which is the energy source for running such a watch. The indicating organ of such a device is then driven in a sense of direction during the winding of the barrel and in the other sense of direction when the spring unwinds thus causing the watch to move.

Such a device for indicating the power reserve could be used in an electronic timepiece comprising an electric energy generator coupled to a winding barrel spring mechanism and intended to supply an electronic time keeping circuit which is regulated by a quartz oscillator.

However, for such an application, in view of the fact of the presence of electronic means, the classical mechanism for indicating the reserve may seem complex and large, whereas the electronic circuit allows to simplify considerably the indication of the power reserve.

An aim of the invention is to propose a simple and efficient solution within this framework for indicating in an irreproachable manner and in a clear way the power reserve of an electronic timepiece which gets its electric energy from a generator driven by a winding spring of a barrel, this generator further serving to regulate the running of the timepiece.

The invention has thus for its object a timepiece comprising a barrel, a spring contained in this barrel,



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time display organs mechanically coupled to said barrel, an electric energy generator also mechanically coupled to said barrel and intended to provide an alternating voltage, a rectifier connected to said generator for providing a continuous voltage from said alternating voltage, and a regulating circuit fed by said continuous voltage and intended to regulate the speed of said generator in such a way as to impose to said display organs a rotational speed which corresponds to a correct indication of the current time, said regulating circuit comprising to this effect a switching organ capable of short-circuiting said generator and of braking it when said regulating circuit observes that said generator rotates at a speed larger than that at which it should turn for ensuring a correct indication of the time, said timepiece being characterized in that it further comprises a device for indicating the power reserve which comprises means for fixing successive periods of time of a predetermined duration, means for counting, during each of said periods, the number of times that said generator is braked by said switching organ, and display means for providing, as a function of the result of this counting, a visual indication of the power reserve.

Other features and advantages of the invention will appear during the description will follows given solely by way of example and made with reference to the attached drawings in which :

- Figure 1 shows a general simplified scheme of a timepiece according to the invention;

- figure 2 is a time diagram illustrating the functioning of the timepiece according to the invention, and

- figure 3a to 3f show several alternatives of displaying devices which may be used in a timepiece according to the present invention.

Reference will now be made to figure 1 which represents a general simplified scheme of a timepiece

according to the invention. It is to be noted that a part of the scheme concerning the regulating circuit intended to control the rotational speed of the generator of this timepiece will not be described in detail here, as the skilled person may readily conceive this controlling device by referring to the description of the Swiss patent application CH 686 332 in the name of the applicant of the present application. However, so as to facilitate the comprehension of the present invention, the essential elements of the scheme and the functioning of this regulating circuit will be recalled briefly.

The timepiece provided with the device for indicating the power reserve according to the invention comprises a mechanical energy source formed by a barrel 1 containing a spring 1a of the type which is usual in watch technology, and which may be wound mechanically or automatically.

Barrel 1 is mechanically coupled to rotor 2a of an electric generator 2 by the intermediate of a gear train 3 symbolised by dotted lines.

Generator 2 comprises a coil 2b which generates at its terminals B1 and B2 an alternating voltage U_g , when rotor 2a is rotationally driven, this rotor being the carrier of a permanent magnet generating a magnetic field symbolised by an arrow in figure 1 and which is coupled to coil 2b.

Terminals B1 and B2 of coil 2b are connected to a rectifier 4 the output terminals 4a and 4b of which provide a continuous voltage U_a obtained from the alternating voltage U_g and intended to supply the several electronic circuits of the timepiece.

Hands 5 or any other classical mechanical displaying means of the time, are coupled to the gear train 3 so as to allow the display of the current time and possibly also the date and the day or another time information.

The rotational speed of hands 5 is kept to a constant average value thanks to a regulating circuit 6 which regulates this value to a reference speed V_c .

As is described in the patent application cited herebefore, the components of regulating circuit 6 are realised so as to regulate the rotational speed of rotor 2a in such a way that hands 5 rotate at the speed which is required to correctly indicate the time when the rotor rotates at the reference speed V_c . This speed being for example four rotations per second.

Regulating circuit 6 comprises an oscillator 7 stabilised by a quartz of the horologic type and a frequency divider 8 for bringing the frequency of this quartz to a value which is usable by a functional block 9 which controls the grid of a semiconductor component 10, for example a n-type MOS transistor.

The latter is connected by way of its principal circuit to the terminals B1 and B2 of coil 2b of generator 2. Consequently, when this semiconductor component is conducting, it allows to short-circuit this coil and thus to obtain a braking effect of the rotational movement of generator 2.

The constructional features as well as the functional features of the several elements described hereabove are realised in such a way that :

a) the average rotational speed of rotor 2a is larger than the reference speed V_c , as long as barrel spring 1a is not almost completely unwound under the condition that coil 2b is not short-circuited by the semiconductor component 10, and that

b) this average rotational speed is less than the reference speed V_c , if coil 2b is short-circuited and this even when the barrel spring 1a is completely wound and the torque it provides has a maximum value.

It should further be noted that within the framework of the present invention, the elements and the functioning described briefly hereabove may possibly be obtained in other ways than that described in the patent application cited hereabove, provided that the rotational speed of the generator is correctly regulated as indicated hereabove.

This regulation should thus be made as a function of the required reference speed V_c (which is determined by the correct indication of the time by hands 5) by the intermediate of successive braking operations of the generator 2 due to a repetitive short-circuiting of coil 2b of this generator.

The control braking signal SF which flows through a conductor line 11 between the functional block 9 and the grid of the semiconductor component 10 is of the logical type and in the example represented, it will be assumed that this signal has the logical state "0" when the timepiece is slow, i.e. when the average speed of rotor 2a is less than the reference speed V_c . Under these conditions, component or transistor 10 remains in the off-state and rotor 2a will not be braked.

On the contrary, when the timepiece runs too fast or when the average speed of rotor 2a is larger than the reference speed V_c , the control braking signal SF is formed of pulses having a fixed length which start at the beginning of each alternance, for example the positive one, of the voltage U_g at terminals B1 and B2 of coil 2b. During each of these pulses of control signal SF, this signal is in the state "1" thus rendering transistor 10 conducting and braking rotor 2a.

The information concerning the alternating voltage U_g provided at terminals B1 and B2 of coil 2b is led to the functional block 9 by the intermediate of a conductor line 12.

The invention is based on the observation that the braking signal SF flowing through conductor 11, otherwise said the frequency with which rotor 2a is braked, constitutes a measure of the winding state of spring 1. In fact, when the barrel spring is complete wound, that is when the power reserve is at its maximum, the torque it produces is maximum. Under these conditions, the rotor will have a permanent tendency to rotate at a speed which is larger than the reference speed V_c and it will thus

have to be braked at the beginning of each period of voltage U_g .

During the unwinding of barrel spring 1a, the motor torque which it provides will diminish progressively so that the rotor will have to be braked less frequently and that gradually its speed will be less often larger than the reference speed V_c , until the moment that it will not have to be braked any more. This thus indicates that the power reserve has been used up and the timepiece will stop if the user does not wind spring 1a.

According to the invention, the device for indicating the power reserve thus comprises means which count the successive braking operations during successive periods of time which are of a predetermined length and which develop a display signal from the number of braking operations which occur during these successive periods of time. An example of an embodiment of these means will be described now in more detail.

Conductor 11 of functional block 9 which transmits the control braking signal SF to transistor 10, is connected to a counter 13 which is provided to count the number of pulses of this signal. Its counting input C is thus connected to conductor 11. Its zero-reset input R is connected to the output Q of a frequency divider 14 which fixes the successive time periods of a predetermined duration. In this example, the input of this divider 14 is connected to the output of frequency divider 8 in such a way so as to further divide the output frequency of the latter.

A memory 15, formed for example by a classical latch circuit, is connected by its inputs E1 to E4 to several respective outputs, here the outputs Q9 to Q12, of counter 13. The clock input C of this memory is connected to the output Q of frequency divider 14. The outputs Q1 to Q4 of memory 15 are respectively connected to inputs E1 to E4 of a decoder 16 the outputs of which, here S1 to S9, are

respectively connected to inputs E1 to E9 of displaying means or display 17.

In the example considered, which is in no way limitative, the output signal of frequency divider 8 has a period of 250 ms. The division ratio of frequency divider 14 is 2048, which thus creates every 512 seconds a pulse at its output (see figure 2). This signal thus sets the predetermined time period during which the number of times that transistor 10 short-circuits coil 2b is counted.

When assuming that rotor 2a rotates too fast with respect to the reference speed V_c during the total duration of the counting period and is braked at the beginning of each period of voltage U_g (this means that the spring only just begins to unwind), the number of pulses of control signal SF will be superior to 2048. Due to this, counter 13 must have a greater capacity and needs an extra flip-flop which thus gives this counter a minimum necessary capacity of 4096.

Figure 2 illustrates by way of a diagram representing several important signals as a function of time, the way in which the device for indicating the power reserve described herebefore functions.

Curve a) shows the output signal of frequency divider 14. This signal presents a period of duration D, chosen here as being equal to 512 seconds.

Curve b) shows the signal SF applied to the counting input of counter 13, it being understood that this figure shows in an illustrative manner two counting periods which are relatively spaced apart and during which the number of pulses of each period are different one from the other. As such two phases of functioning are taken into consideration during which the winding of the spring is respectively strong or weak.

Curve c) represents the contents of counter 13 during the two represented respective periods, while curve d) shows the output of memory 15 which conserves each time the information of the preceding counting period and which

is reset to zero at the end of each counting period thanks to a reset signal which transits along a conductor 18 (figure 1).

Figure 3 shows several examples of practical embodiments of displaying means 17. The figure represents in a), b), c) and d) displays indicating the instantaneous value of the output of memory 15 respectively by a non-graduated indication (a) or a graduated indication having, for example, 12 hour intervals (b), c) and d)). As may be understood, the ways in which these displaying devices may be obtained are numerous and the skilled person will readily know how to conceive such. For example, dial C of the timepiece may comprise an aperture G behind which a coloured strip moves which is animated by a small actuator (non represented) receiving its control signal from decoder 16. As an alternative, a series of luminescent diodes may be used.

In e) and f) of figure 3, two other alternatives have been represented, one comprising a liquid crystal display CL visualising the power reserve in hours, and the other comprising several apertures O forming a scale which is graduated in periods of 12 hours for example. A luminescent diode may be provided behind each of said apertures and being controlled in a suitable manner by the output of decoder 16.

The resolution of display 17 is irrelative, and may be for example in periods of a predetermined number of hours, as represented in b), c), d) and f) of figure 3, in hour units as in e), or even by all or nothing requiring a sole indicator, an alternative which has not been represented in the drawings. The skilled person may readily adapt memory 15, decoder 16 and display 17 to these several different possibilities of displaying which all remain within the framework of this invention.

In particular, it is obvious that an increase of the resolution goes together with an increase of the capacity of memory 15.

However, as has been explained hereabove, it is also possible to realise the elements and the functions in a different manner than that described in the Swiss patent application CH 686 332 cited herebefore. Thus, it may be understood that in analogy it is also possible, thereby still remaining within the framework in the present invention, to measure the duration between each occurrence of a braking pulse SC so as to determine the power reserve, instead of counting this number of pulses during a fixed predetermined duration as has been explained herebefore. In this case, the power reserve is large when the duration between two successive pulses is short, i.e. when braking must be performed often, and the reserve is small when this duration is long.

It is also possible that memory 15 only memorises a certain number of the most significant bits of the output information of counter 13, for example the four most significant bits, as is represented in figure 1. Under these conditions, it is theoretically possible to display sixteen distinct informations on display 17. This is generally considered as being redundant for the envisaged aim, a number of information of at most nine should suffice for usefully informing the user of a timepiece of its power reserve state.

It should further be noted that the device according to the invention may, to a certain extent, inform the user of the mechanical state of the timepiece. For example, if display 17 cannot reach the maximum reserve graduation, although the user has completely wound the spring, this could mean for example that the timepiece requires a revision and a new greasing.

CLAIMS

1. Timepiece comprising a barrel (1), a spring (1a) contained in this barrel (1), time display organs (5) mechanically coupled to said barrel (1), an electric energy generator (2) also mechanically coupled to said barrel (1) and intended to provide an alternating voltage (U_g), a rectifier (4) connected to said generator (2) for providing a continuous voltage (U_a) from said alternating voltage (U_g), and a regulating circuit (6) fed by said continuous voltage (U_a) and intended to regulate the rotational speed of said generator to a reference speed (V_c), said regulating circuit (6) comprising means for braking said generator (2) when said regulating circuit observes that said generator rotates at a speed larger than said reference speed, this timepiece being characterized in that said means for braking said generator is adapted to apply successive braking operations on this generator and in that it comprises a device for indicating the power reserve adapted for effecting a measure representative of said successive braking operations and for providing a visual indication of the power reserve as a function of this measure.

2. Timepiece according to claim 1, characterized in that said generator comprises a coil (2b) and in that said means for braking this generator comprises a switching organ (10) formed of a semiconductor component and connected by way of its principal circuit to the terminals of said coil (2b) in such a way that it may short-circuit said coil under the control of said regulating circuit (6).

3. Timepiece according to claim 1 or 2, characterized in that said measure representative of said successive braking operations is a measure representative of the frequency of these successive braking operations.

4. Timepiece according to any of the preceding claims, characterized in that said measure representative of said successive braking operations consists in determining the mean frequency of these successive braking operations during successive periods of time of determined duration (D), said device for indicating the power reserve comprising to this effect means (8, 14) for fixing these successive periods of time and means (13) for counting, during each of said periods, the number of times that said generator is braked.

5. Timepiece according to claim 4, in which said regulating circuit (6) comprises a horological quartz oscillator (7) and a first frequency divider (8), characterized in that said means for fixing said periods of time of determined duration comprises a supplemental frequency divider (14) connected to the output of said first divider (8).

6. Timepiece according to claim 4 or 5 depending from claim 2, characterized in that said means for counting comprises a counter (13) connected to the control grid of said semiconductor component (10) for counting the number of times that this component short-circuits said coil (2b) during said determined duration
5 (D).

7. Timepiece according to claim 6, characterized in that said counter (13) is connected to a memory circuit (15) which memorizes the state of said counter during each of said successive periods of time.

8. Timepiece according to any of claims 1 to 3, characterized in that said
10 measure representative of said successive braking operations consists in determining the duration of time between these successive braking operations, said device for indicating the power reserve comprising to this effect means (8, 14) for measuring the duration between two successive braking operations.

9. Timepiece according to any of the preceding claims, characterized in that
15 display means (17) are arranged for providing an hour-graduated information or a multiple hour-graduated information on a dial (C) of said timepiece.