ARRAY SUBSTRATE, METHOD OF INSPECTING ARRAY SUBSTRATE, AND LIQUID CRYSTAL DISPLAY

ABSTRACT OF THE DISCLOSURE

An array substrate is provided for which an OS inspection can be performed without increasing the area of a signal driving circuit formed on the array substrate, and for which the manufacturing cost can be reduced. The array substrate includes a substrate, scanning lines, signal lines, pixel electrodes connected via switching elements provided at the intersections of the scanning lines and the signal lines, a plurality of input/output terminals provided at an edge portion of the substrate, a scanning line driving circuit, a signal line driving circuit for supplying picture signals sent from the outside to one ends of the signal lines via the input/output terminals, and a wiring line commonly connecting the other ends of the signal lines to at least one of the input/output terminals. A predetermined level of voltage is applied between the input/output terminal to which the wiring line is connected and another input/output terminal which at least supplies picture signals, and a current flowing at that time is measured to detect failures.

This Sheet Has Been Replaced

