Methods of Forming Metal-Comprising Materials And Capacitor Electrodes; and Capacitor Constructions

TECHNICAL FIELD

[0001] The invention pertains to methods of forming metal-comprising materials, such as, for example, capacitor electrodes. The invention also pertains to capacitor constructions.

BACKGROUND OF THE INVENTION

[0002] Capacitor constructions are utilized in numerous semiconductor structures, such as, for example, memory arrays. An exemplary memory array is a dynamic random access memory (DRAM) array, with individual DRAM cells of the array comprising a capacitor and a transistor.

[0003] Capacitor constructions comprise a first conductive capacitor electrode and a second conductive capacitor electrode, separated by a dielectric material. Among the compositions suitable for utilization as capacitor electrodes are metals, such as, for example, platinum, rhodium, iridium, ruthenium, etc. The metals can be deposited by exposing a metallo-organic precursor material to an oxidizing ambient (such as, for example, an ambient O_2, O_3, and/or N_2O) to break down the precursor and release the metal. The released metal can then deposit on a substrate to form a metal film which is ultimately incorporated into a capacitor device as a capacitor electrode.

[0004] A difficulty which can occur during oxidation of the metallo-organic precursors is that materials associated with a semiconductor substrate are exposed to the oxidizing conditions, and can themselves become oxidized or otherwise degraded during the degradation of the metallo-organic precursors. Accordingly, it would be desirable to develop alternative methods for formation of metallic materials on semiconductor substrates, other than the oxidation of metallo-organic precursors.

SUMMARY OF THE INVENTION

[0005] In one aspect, the invention encompasses a method of forming a metal-comprising mass for a semiconductor construction. A semiconductor substrate is provided, and a metallo-organic precursor is provided proximate the substrate. The precursor is exposed to a reducing atmosphere to release metal from the precursor, and subsequently the released metal is deposited over the semiconductor substrate.

[0006] In another aspect, the invention encompasses an embodiment of forming a metal-comprising mass for a semiconductor construction wherein a metal-comprising

precursor is exposed to ammonia to release metal from the precursor, and subsequently the release metal is deposited over a semiconductor substrate.

[0007] In another aspect, the invention encompasses methodology for forming capacitor electrodes wherein a metal-comprising precursor is exposed to a reducing ambient to deposit a metal-comprising mass which ultimately is incorporated into a capacitor construction as a capacitor electrode.

[0008] The invention also includes capacitor constructions.

BRIEF DESCRIPTION OF THE DRAWINGS

[0009] Preferred embodiments of the invention are described below with reference to the following accompanying drawing.

[0010] The figure is a diagrammatic, cross-sectional view of a semiconductor wafer fragment illustrating an embodiment of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0011] An embodiment of the invention is described with reference to a semiconductor wafer fragment 10 in the figure. Fragment 10 includes a substrate 12, which can comprise, for example, monocrystalline silicon. To aid in interpretation of the claims that follow, the terms "semiconductive substrate" and "semiconductor substrate" are defined to mean any construction comprising semiconductive material, including, but not limited to, bulk semiconductive materials such as a semiconductive wafer (either alone or in assemblies comprising other materials thereon), and semiconductive material layers (either alone or in assemblies comprising other materials). The term "substrate" refers to any supporting structure, including, but not limited to, the semiconductive substrates described above.

[0012] An insulative material 14 is formed over substrate 12, and can comprise, for example, borophosphosilicate glass (BPSG) and/or silicon dioxide. An opening extends through insulative material 14 and to substrate 12, and a diffusion region 16 is formed within substrate 12 at a base of such opening. Diffusion region 16 can be either n-type conductivity doped or p-type conductivity doped. Diffusion region 16 can be considered as an exemplary embodiment of an electrical node supported by substrate 12.

[0013] A series of conductive materials are formed within the opening in mass 14, and extending above diffusion region 16. Such conductive materials include a mass 18 of conductively-doped silicon, such as, for example, n-type or p-type doped polycrystalline silicon. The conductive materials also include a layer 20 of metal silicide, and a layer 22 comprising metal or metal nitride. It is noted that other conductive

materials can be used either alternatively, or in addition to the materials 18, 20 and 22 illustrated in the figure. For instance, material 18 can be replaced by a conductive metal plug, such as, for example, a titanium plug or a tungsten plug. In other embodiments, a layer of metal-silicide can be provided between diffusion region 16 and silicon-containing layer 18, with exemplary metal silicides comprising titanium silicide or tungsten silicide. Layers 18, 20 and 22 can be formed by conventional methods.

[0014] In particular embodiments, layer 22 can comprise, consist of, or consist essentially of, one or more of titanium nitride, tungsten nitride, tantalum nitride, elemental titanium, elemental tantalum or elemental tungsten; and layer 20 can comprise, consist of, or consist essentially of, titanium silicide or tungsten silicide. Layers 20 and 22 function as diffusion and/or oxidation barriers.

[0015] A metal-containing mass 24 is formed over conductive layer 22, and in the shown embodiment is in physical contact with layer 22. In accordance with methodology of the present invention, mass 24 is formed by exposing a metallo-organic precursor to a reducing atmosphere to release metal from the precursor, and subsequently the released metal is deposited to form mass 24. In the shown embodiment, mass 24 is patterned into a rectangular block. Such can be accomplished by, for example, photolithographic processing and an appropriate etch after deposition of the released metal. Appropriate photolithographic processing and etching conditions will be recognized by persons of ordinary skill in the art.

[0016] Mass 24 can comprise, consist essentially of, or consist of one or more of ruthenium, rhodium, iridium, cobalt, palladium, nickel or platinum. In a particular embodiment, mass 24 will consist of, or consist essentially of, ruthenium, and will be formed by exposing tricarbonyl-cyclohexadiene ruthenium precursor to a reducing ambient comprising one or more of ammonia (NH_3), diatomic hydrogen (H_2), or plasma-activated hydrogen species. The reducing atmosphere can, in particular embodiments, consist of, or consist essentially of, one or more of ammonia, diatomic hydrogen or plasma-activated hydrogen species. In an exemplary embodiment, tricarbonyl-cyclohexadiene ruthenium precursor is exposed to ammonia at a temperature of 210°C, and a pressure of 4 torr for a duration of 120 seconds, to deposit mass 24 to a thickness of about 450Å.

[0017] Prior art methodologies have existed wherein a metal-containing mass is formed over a layer identical to the above-described layer 22 by exposing a metallo-organic material to oxidizing conditions. However, a problem with such prior art processes is that the oxidizing conditions can oxidize various components of layer 22 to reduce the conductivity of such layer. For instance, if layer 22 comprises titanium, tantalum or tungsten, the exposure of such layer to oxidizing conditions can form oxides

of titanium, tungsten or tantalum. Such oxides are electrically insulative, and accordingly the desired conductive characteristics of layer 22 are compromised, or in some cases even entirely lost, which can render devices subsequently formed from layer 22 to be inoperable. In contrast, the utilization of reducing conditions in embodiments of the present invention can avoid oxidation of the materials of layer 22, and accordingly maintain the desired conductive characteristics of layer 22 during formation of mass 24. A further advantage of utilizing reducing conditions in methodology of the present invention is that many metallo-organic precursor materials contain oxygen, which can be released during chemical degradation of the precursor materials. The released oxygen can oxidize substrate materials. However, utilization of a reducing atmosphere can essentially scavenge the oxygen before it deleteriously reacts with a substrate material. For instance, in particular embodiments of the present invention, NH_3 can be utilized to essentially scavenge oxygen.

[0018] A reason that reducing conditions have not previously been utilized for forming conductive masses in semiconductor constructions is that there is a concern that carbon from a metallo-organic precursor will deposit within a conductive metal mass formed from the precursor unless the precursor is exposed to oxidizing conditions. In one aspect of the present invention, it is recognized that carbon incorporation is less problematic in particular semiconductor fabrication applications than is oxidation of materials associated with a semiconductor device. For instance, in the embodiment described with reference to the Figure, it can be highly desirable to maintain the conductive characteristics of layer 22, and of less concern is elimination of carbon from mass 24. Accordingly, utilization of reducing conditions to form mass 24 is preferred relative to utilization of oxidizing conditions, even if such causes an increase in carbon incorporation within mass 24 relative to that which would occur with utilization of oxidizing conditions. However, an analysis of a mass 24 formed by exposing tricarbonyl-cyclohexadiene ruthenium precursor to ammonia has shown that there is very little carbon incorporation within such mass. Accordingly, in particular embodiments of the present invention, oxidation of layer 22 can be avoided, and also carbon incorporation within mass 24 can be avoided. Such can be preferred embodiments of the present invention, but it is to be understood that the invention can also encompass embodiments wherein carbon incorporation occurs within mass 24.

[0019] Mass 24 can, particular embodiments, comprises multiple metals formed from one or more precursors. The multiple metals can include one or more of ruthenium, rhodium, iridium, cobalt, palladium, nickel or platinum. In some aspects of the invention, it can be desired that mass 24 does not consist solely of platinum. However, in such aspects, mass 24 can comprise platinum in combination with other

metals. Accordingly, it can be desired that mass 24 be formed from at least one precursor that comprises a metal other than platinum; but such at least one precursor can be utilized in combination with other precursors that do comprise platinum.

[0020] After formation of mass 24, a dielectric material 26 is provided over the mass. Dielectric material 26 can comprise silicon dioxide and/or silicon nitride. Additionally, or alternatively, dielectric material 26 can comprise a so-called high-k material, such as, for example, Ta_2O_5.

[0021] After formation of dielectric material 26, a second capacitor electrode 28 is formed over dielectric material 26. Capacitor electrode 28 can comprise metal and/or conductively-doped silicon. If electrode 28 comprises metal, such can be formed utilizing a reducing atmosphere as was described previously for formation of mass 24. Alternatively, electrode 28 can be formed by exposing a metallo-organic precursor to an oxidizing atmosphere. In preferred embodiments of the invention, dielectric material 26 can comprise a high-k material, and electrode 28 is formed by exposing a metallo-organic precursor to oxidizing conditions. The oxidizing conditions can cure oxide deficiencies that can otherwise exist in a high-k material. For instance, if the high-k material comprises Ta_2O_5, there can be regions in the material which have an excess of tantalum relative to the amount of oxygen so that the material is tantalum-rich (i.e., so that there is more tantalum than should be present in the stoichiometric relationship Ta_2O_5). Such tantalum-rich regions will lack the desired dielectric characteristics of the high-k material. Exposure of the tantalum-rich regions to oxidizing conditions can convert such regions to Ta_2O_5, and accordingly improve the dielectric properties associated with the regions.

[0022] Second capacitor electrode 28 can comprise, consist of, or consist essentially of ruthenium, rhodium, iridium, cobalt, palladium, nickel or platinum; and can comprise an identical metal to that utilized in mass 24, or a different metal than that utilized in mass 24. In particular preferred embodiments, mass 24 will be formed by exposing a metallo-organic precursor to a reducing environment, and will constitute a first capacitor electrode; and mass 28 will be formed by exposing the same metallo-organic precursor, or a different metallo-organic precursor, to an oxidizing atmosphere, and will constitute a second capacitor electrode.

[0023] The masses 24, 26 and 28 of the Figure together define at least a portion of a capacitor construction. Such capacitor construction can be incorporated into various semiconductor devices, such as, for example, a DRAM cell. In embodiments in which the capacitor construction is incorporated into a DRAM cell, there will typically be a transistor gate (not shown) utilizing diffusion region 16 as a source/drain region, and

accordingly electrically connected to mass 24 though diffusion region 16. In such embodiments, mass 24 can be considered a storage node of a capacitor construction.

CLAIMS

1. A method of forming a metal-comprising mass for a semiconductor construction, comprising:

providing a semiconductor substrate;

providing one or more metallo-organic precursors proximate the substrate, at least one of the one or more precursors not comprising platinum;

exposing at least one of said precursors to a reducing atmosphere to release metal from said at least one of the precursors;

depositing the released metal over the semiconductor substrate to form a metal-comprising mass on the semiconductor substrate; wherein the substrate comprises an upper surface consisting of one or more of TiN, elemental Ti, WN, elemental W, TaN and elemental Ta; and the upper surface is exposed to the reducing atmosphere during formation of the metal-comprising mass; and

patterning the metal-containing metal-comprising mass into a rectangular block.

2. The method of claim 1 wherein the metal-comprising mass is formed physically against the upper surface of the substrate.

3. The method of claim 1 wherein the upper surface consists of TiN.

4. The method of claim 1 wherein the upper surface consists of elemental Ti.

5. The method of claim 1 wherein the upper surface consists of WN.

6. The method of claim 1 wherein the upper surface consists of elemental W.

7. The method of claim 1 wherein the upper surface consists of TaN.

8. The method of claim 1 wherein the upper surface consists of elemental Ta.

9. The method of claim 1 wherein the reducing atmosphere comprises NH_3.

10. The method of claim 1 wherein the reducing atmosphere comprises plasma-activated hydrogen.

11. The method of claim 1 wherein the reducing atmosphere comprises H_2.

12. A method of forming a capacitor, comprising:

providing a semiconductor substrate having an electrical node supported thereby;

forming an electrical interconnect in electrical contact with the node, the electrical interconnect comprising conductively-doped silicon;

forming a conductive material over the electrical interconnect, the conductive material comprising one or more of TiN, WN, TaN, elemental Ta, elemental Ti and elemental W;

providing a metallo-organic precursor proximate the conductive material, the metallo-organic precursor comprising one or more of ruthenium, rhodium, iridium, cobalt, palladium, platinum and nickel;

exposing the precursor to a reducing atmosphere to release metal from the precursor, the released metal consisting essentially of one or more of ruthenium, rhodium, iridium, cobalt, palladium, platinum and nickel;

depositing the released metal over the conductive material to form a first capacitor electrode;

forming a dielectric material over the first capacitor electrode; and

forming a second capacitor electrode over the dielectric material; wherein the second capacitor electrode comprises metal; and wherein the forming the second capacitor electrode comprises exposing a metal-comprising precursor to an oxidizing atmosphere.

13. The method of claim 12 wherein the precursor comprises ruthenium, and wherein the released metal consists essentially of ruthenium.

14. The method of claim 12 wherein the precursor comprises rhodium, and wherein the released metal consists essentially of rhodium.

15. The method of claim 12 wherein the precursor comprises iridium, and wherein the released metal consists essentially of iridium.

16. The method of claim 12 wherein the precursor comprises cobalt, and wherein the released metal consists essentially of cobalt.

17. The method of claim 12 wherein the precursor comprises palladium, and wherein the released metal consists essentially of palladium.

18. The method of claim 12 wherein the precursor comprises platinum, and wherein the released metal consists essentially of platinum.

19. The method of claim 12 wherein the precursor comprises nickel, and wherein the released metal consists essentially of nickel.

20. The method of claim 12 wherein the precursor comprises tricarbonyl-cyclohexadiene ruthenium.

21. The method of claim 12 wherein the reducing atmosphere comprises NH_3.

22. The method of claim 12 wherein the reducing atmosphere comprises plasma activated hydrogen.

23. The method of claim 12 wherein the reducing atmosphere comprises H_2.

24. A method of forming a capacitor, comprising:

providing a semiconductor substrate having an electrical node supported thereby;

forming an electrical interconnect in electrical contact with the node, the electrical interconnect comprising conductively-doped silicon;

forming a conductive material over the electrical interconnect, the conductive material comprising one or more of TiN, WN, TaN, elemental Ta, elemental Ti and elemental W;

providing a metallo-organic precursor proximate the conductive material;

exposing the precursor to a reducing atmosphere to release metal from the precursor;

depositing the released metal over the conductive material to form a first capacitor electrode;

forming a dielectric material over the first capacitor electrode; and

forming a second capacitor electrode over the dielectric material; wherein the second capacitor electrode comprises metal; wherein the dielectric material comprises an oxide; and wherein the forming the second capacitor electrode comprises exposing a metal-comprising precursor to an oxidizing atmosphere.

25. The method of claim 24 wherein the dielectric material comprises Ta_2O_5; and wherein the forming the second capacitor electrode comprises exposing the metal-comprising precursor to one or more of N_2O, O_2 and O_3.

26. The method of claim 24 wherein the conductive material consists of TiN, and wherein the deposited released metal is in physical contact with the conductive material.

27. The method of claim 24 wherein the conductive material consists of elemental Ti, and wherein the deposited released metal is in physical contact with the conductive material.

28. The method of claim 24 wherein the conductive material consists of WN, and wherein the deposited released metal is in physical contact with the conductive material.

29. The method of claim 24 wherein the conductive material consists of elemental W, and wherein the deposited released metal is in physical contact with the conductive material.

30. The method of claim 24 wherein the conductive material consists of TaN, and wherein the deposited released metal is in physical contact with the conductive material.

31. The method of claim 24 wherein the conductive material consists of elemental Ta, and wherein the deposited released metal is in physical contact with the conductive material.

32. The method of claim 24 wherein the dielectric material comprises a high-k material.

33. The method of claim 24 wherein the dielectric material comprises Ta_2O_5.

34. A method of forming a capacitor, comprising:

providing a semiconductor substrate supporting a conductive material;

providing a metallo-organic precursor proximate the conductive material;

exposing the precursor to a reducing atmosphere to release metal from the precursor;

depositing the released metal over the conductive material to form a first capacitor electrode;

forming a dielectric material over the first capacitor electrode;

forming a second capacitor electrode over the dielectric material; and

wherein the second capacitor electrode comprises metal; wherein the dielectric material comprises an oxide; and wherein the forming the second capacitor electrode comprises exposing a metal-comprising precursor to an oxidizing atmosphere.