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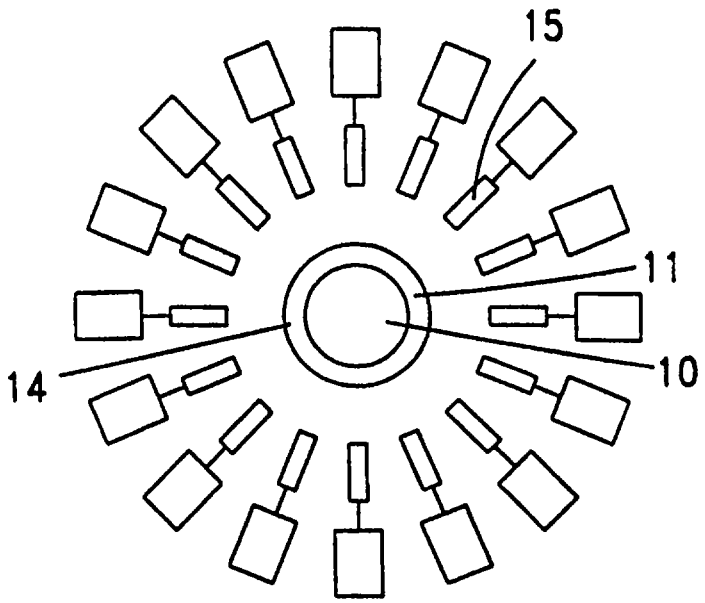


Fig. 1

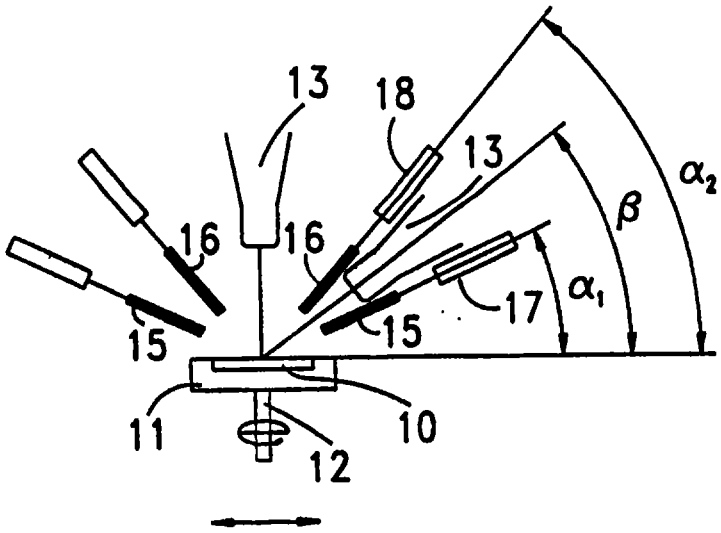


Fig. 2

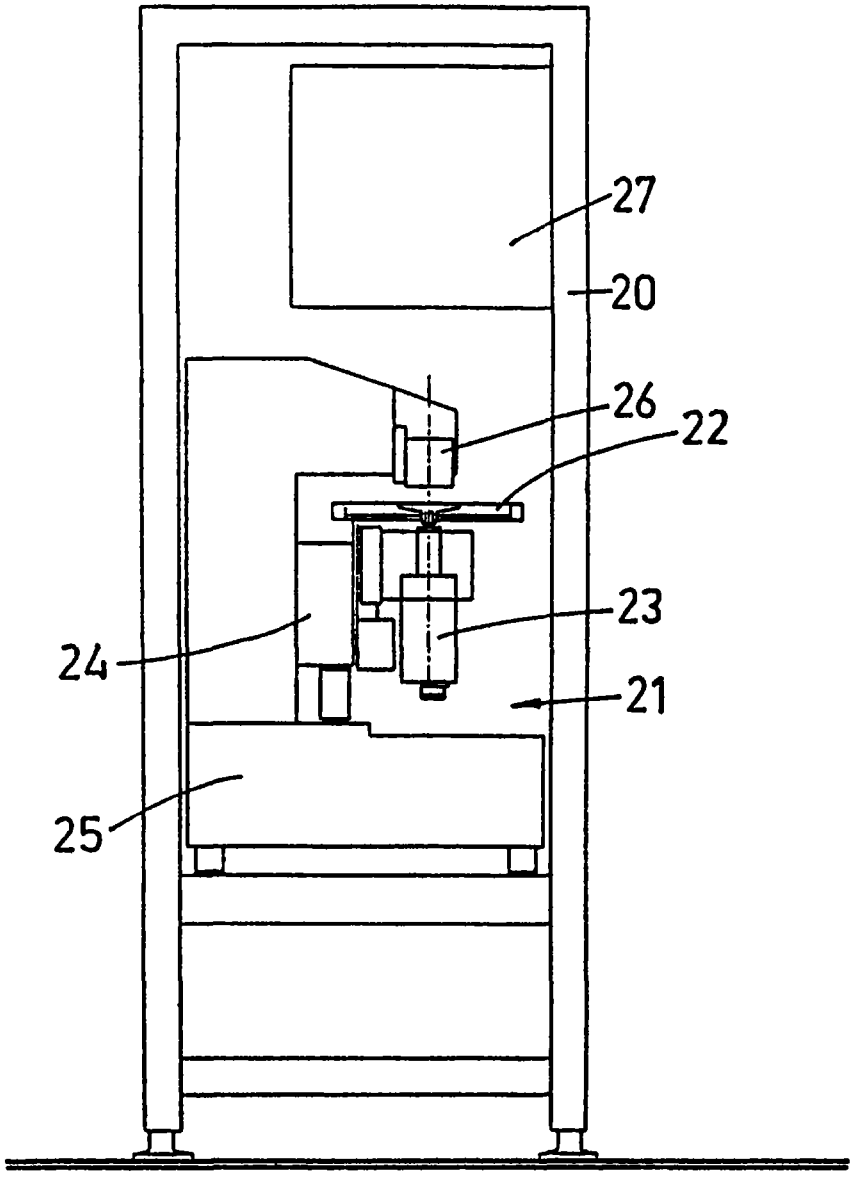


Fig. 3

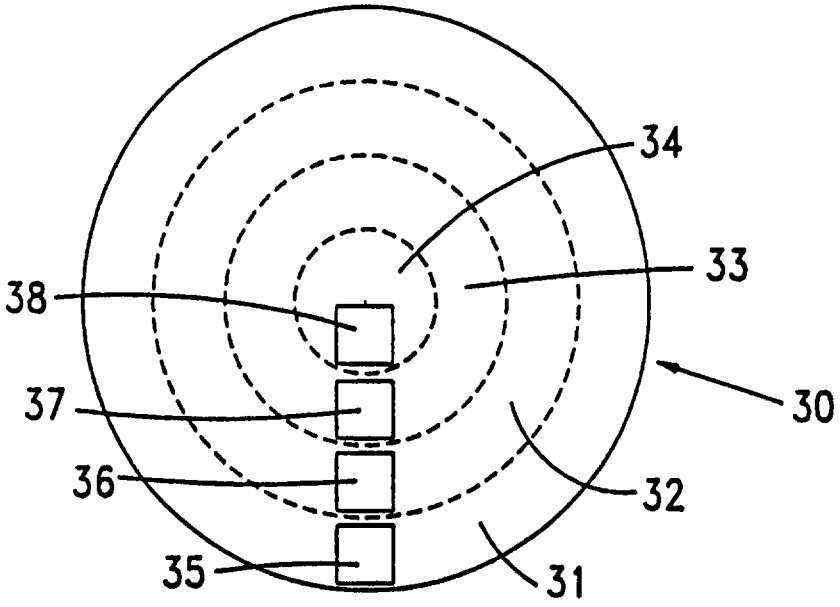


Fig. 4

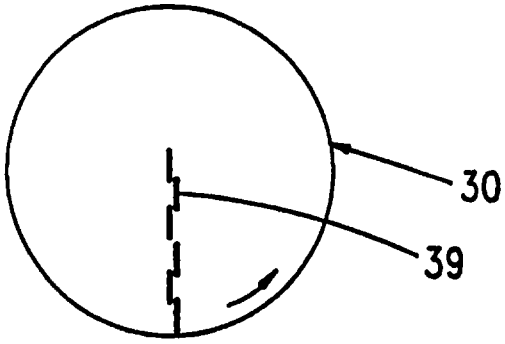


Fig. 5

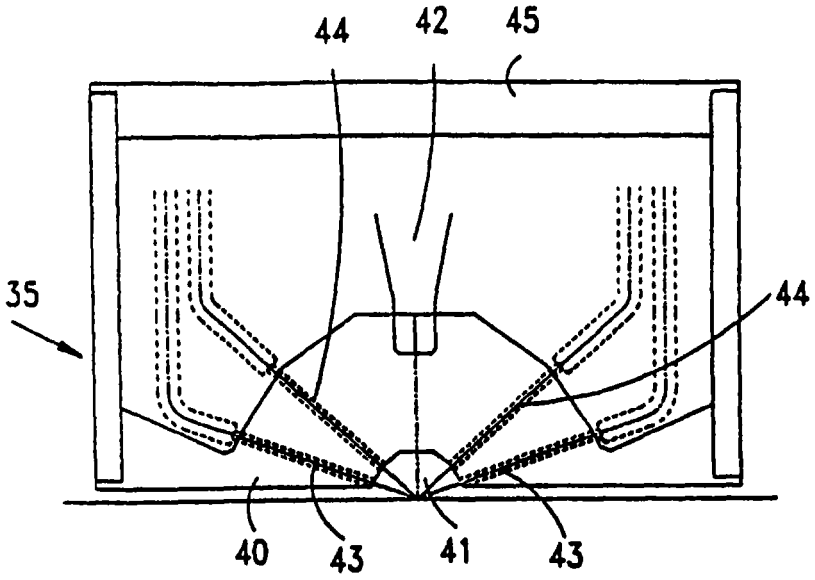


Fig. 6a

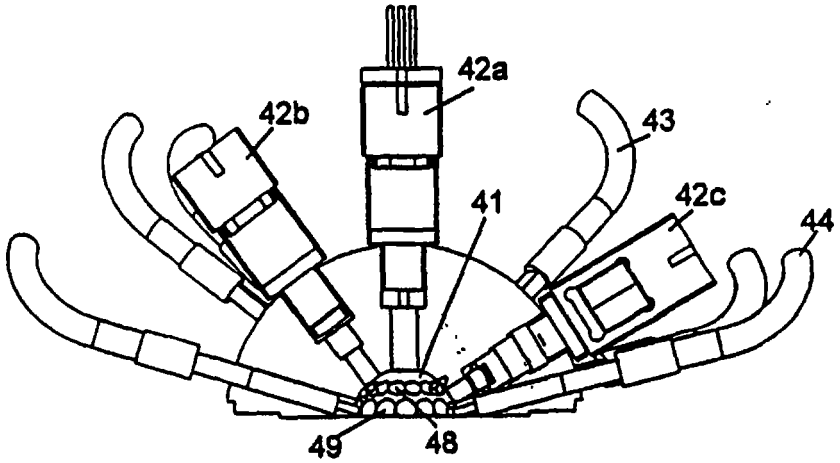


Fig. 6b

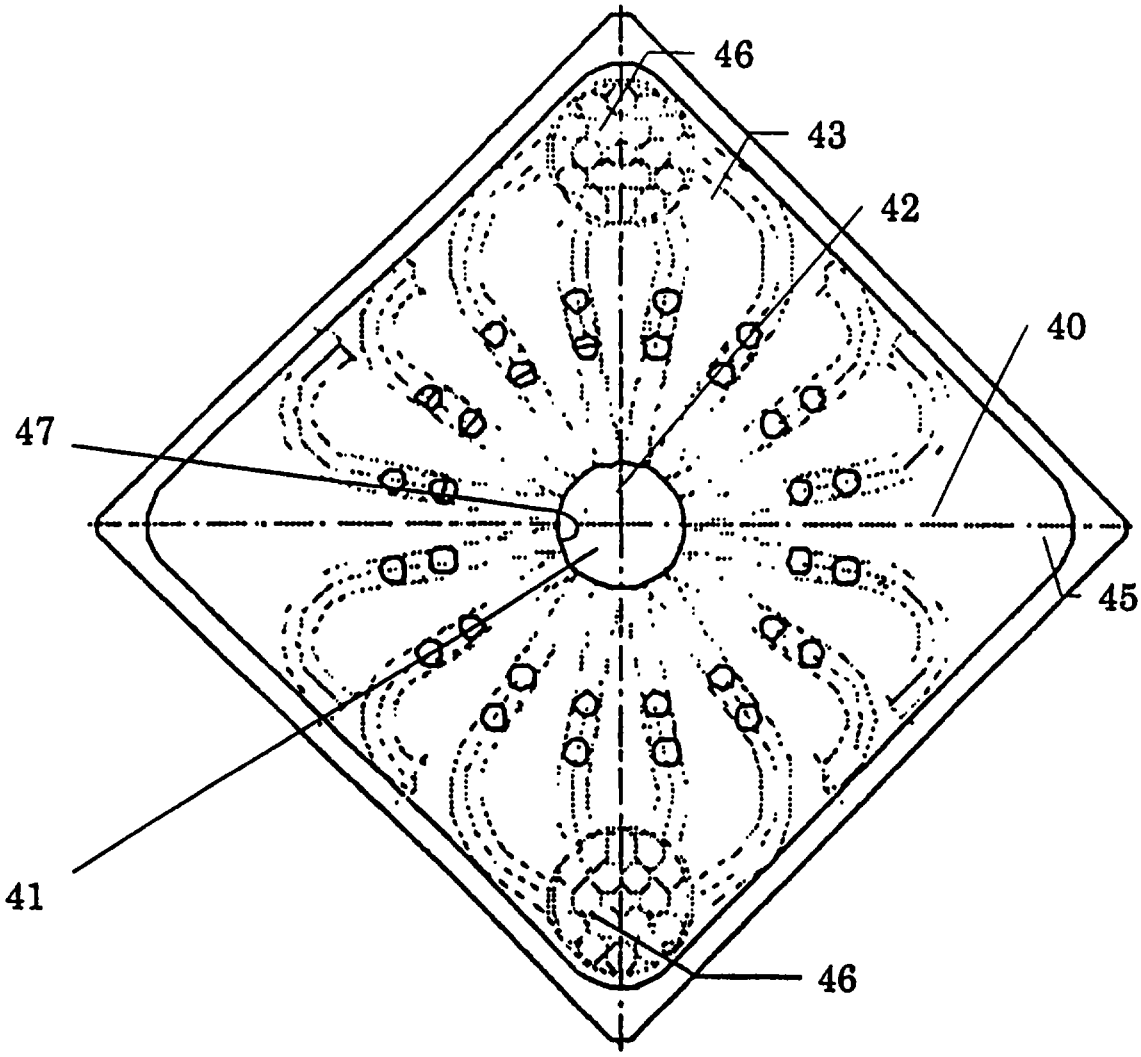


Fig. 7

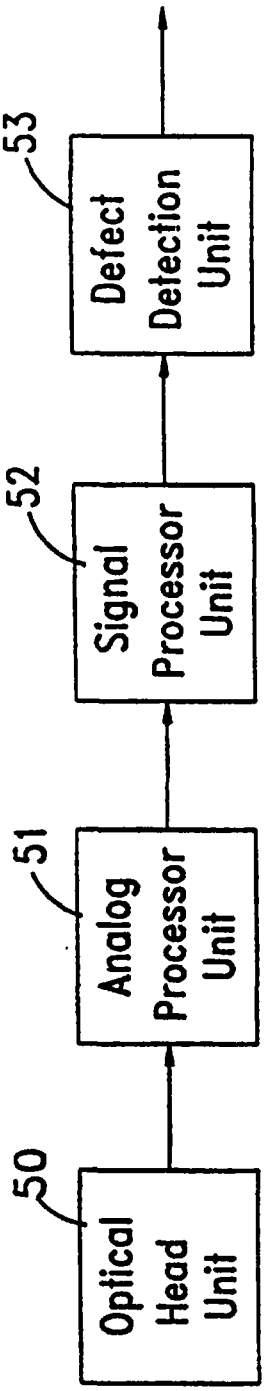

50
Optical Head Unit
51
Analog Processor Unit
52
Signal Processor Unit
53
Defect Detection Unit

Fig. 8

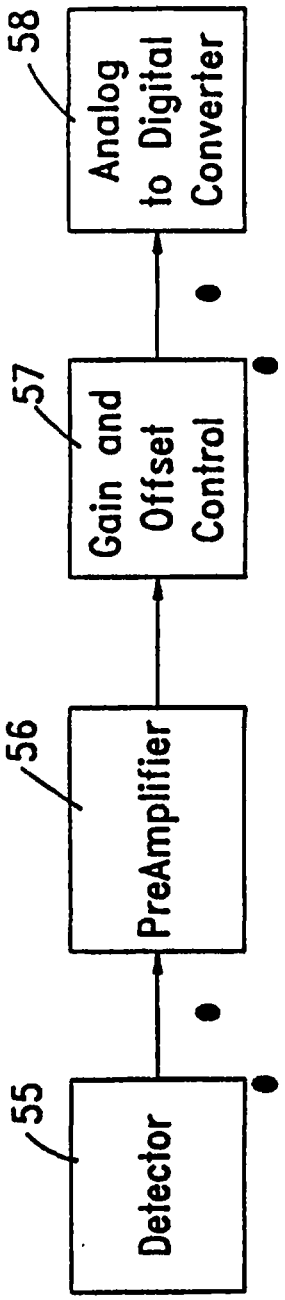

55
Detector
56
PreAmplifier
57
Gain and Offset Control
58
Analog to Digital Converter

Fig. 9

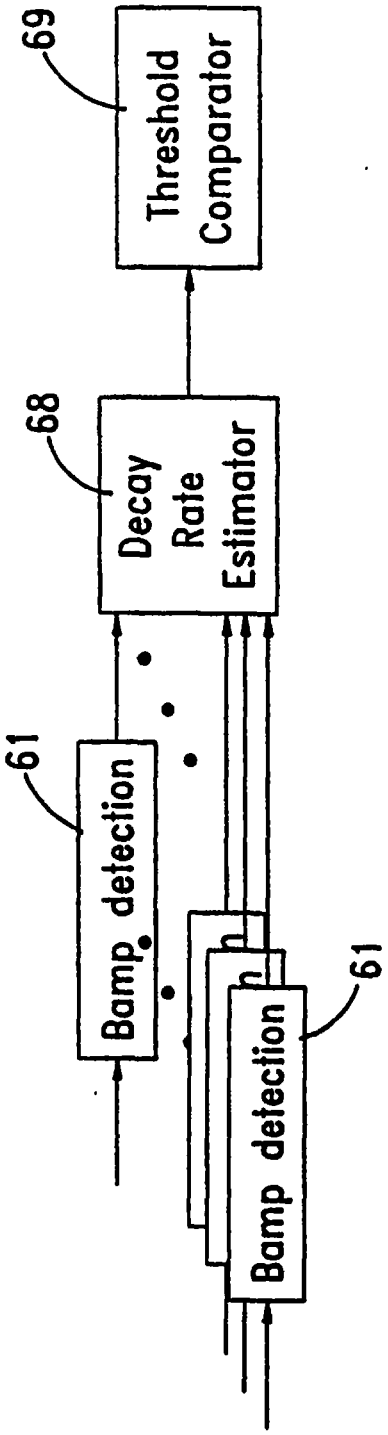

69
Threshold Comparator
68
Decay Rate Estimator
61
Bamp detection
n
61
Bamp detection

Fig. 10

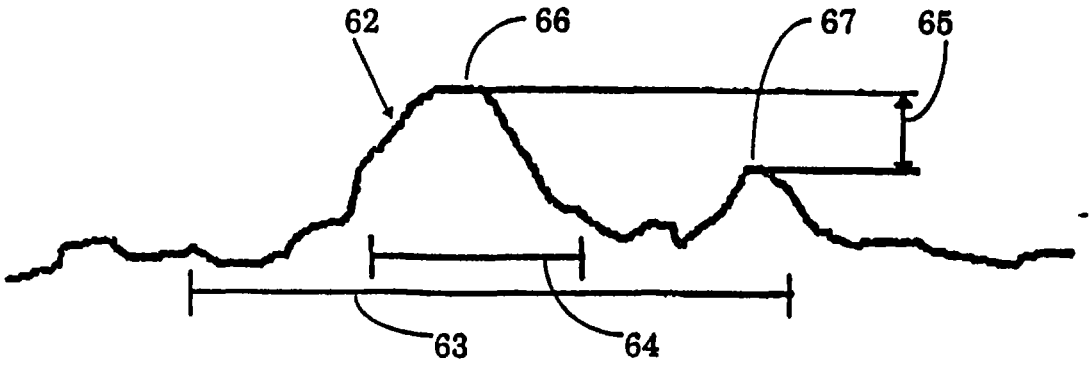


Fig. 11

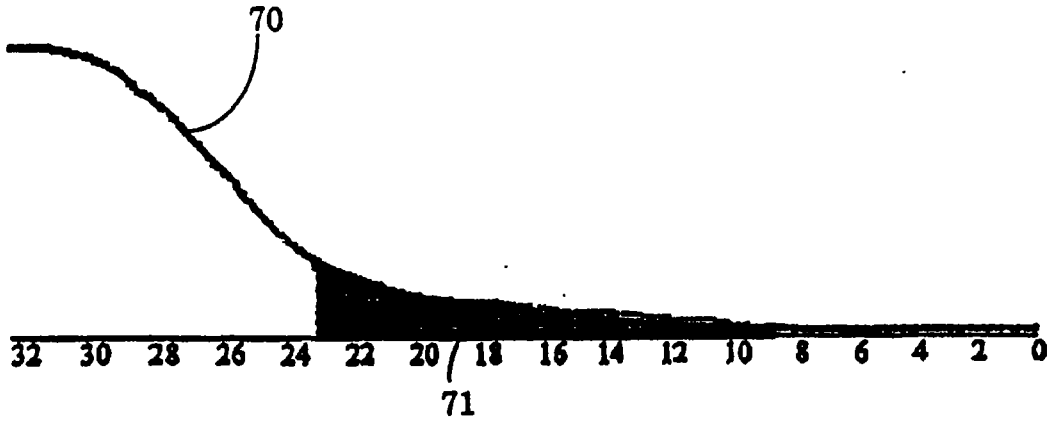


Fig. 12

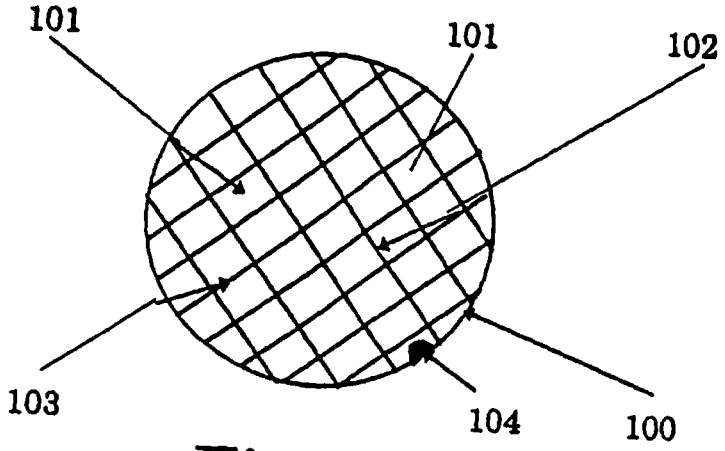


Fig. 13

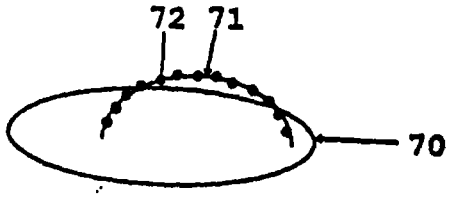


Fig. 14(a)

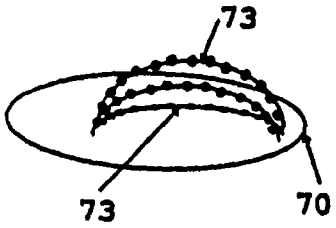


Fig. 14(b)

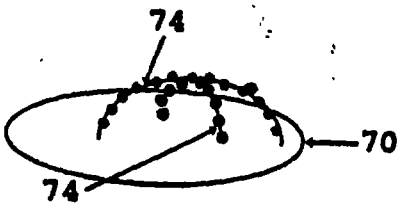


Fig. 14(c)

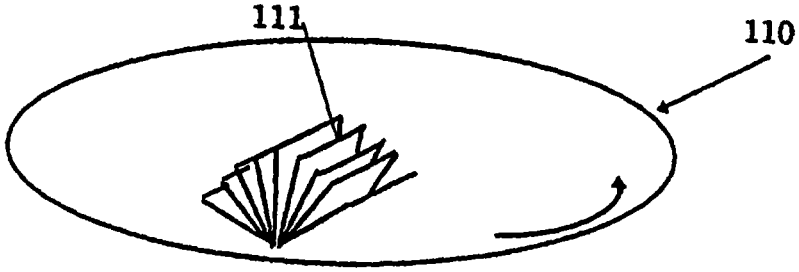


Fig. 15

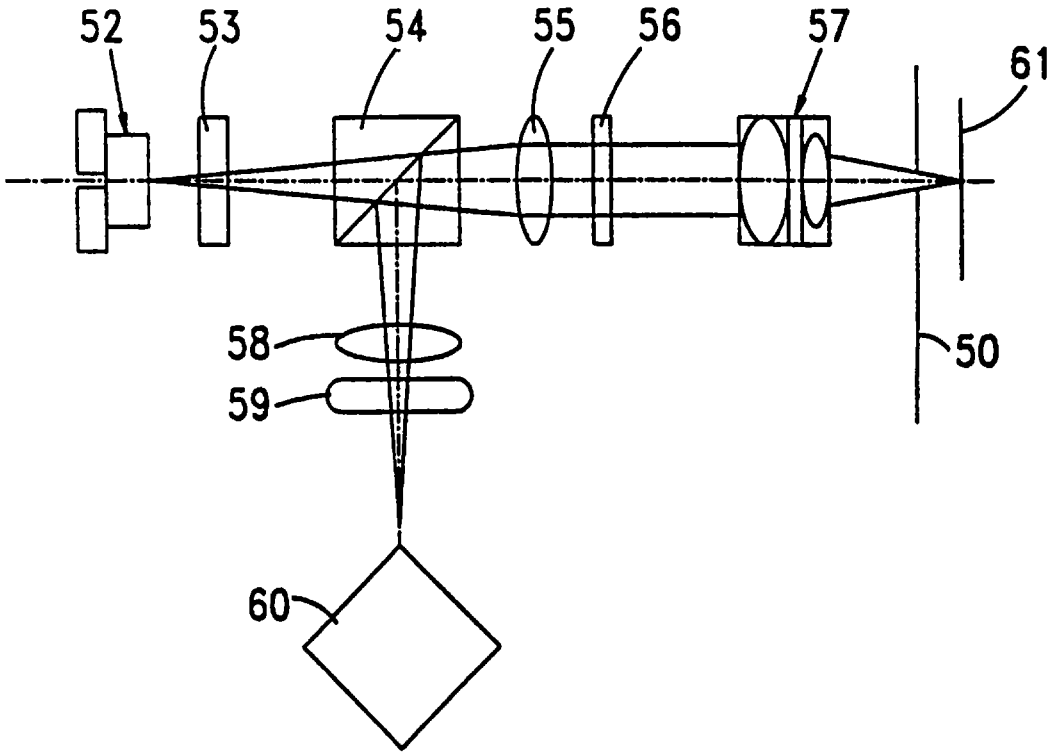


Fig. 16

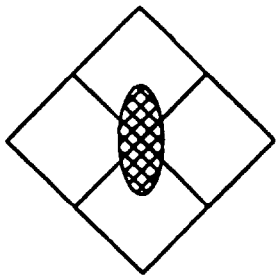


Fig. 17(a)

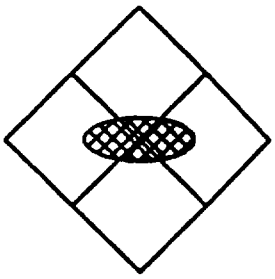


Fig. 17(b)

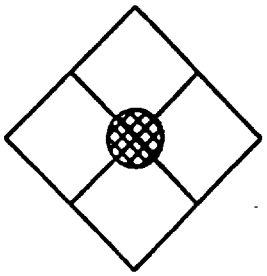


Fig. 17(c)

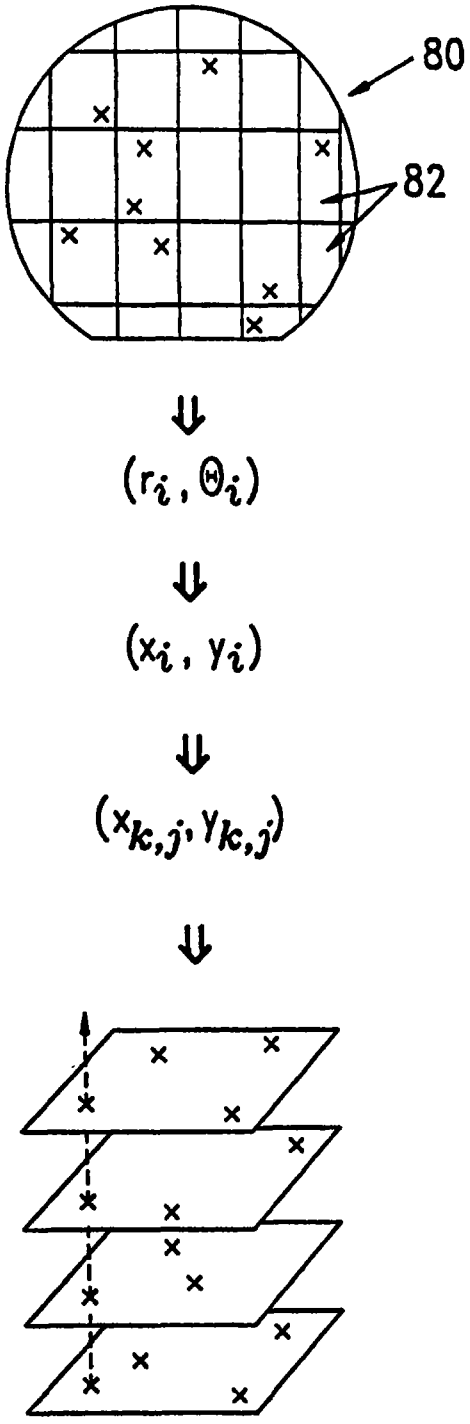


Fig. 18