

Fig. 2

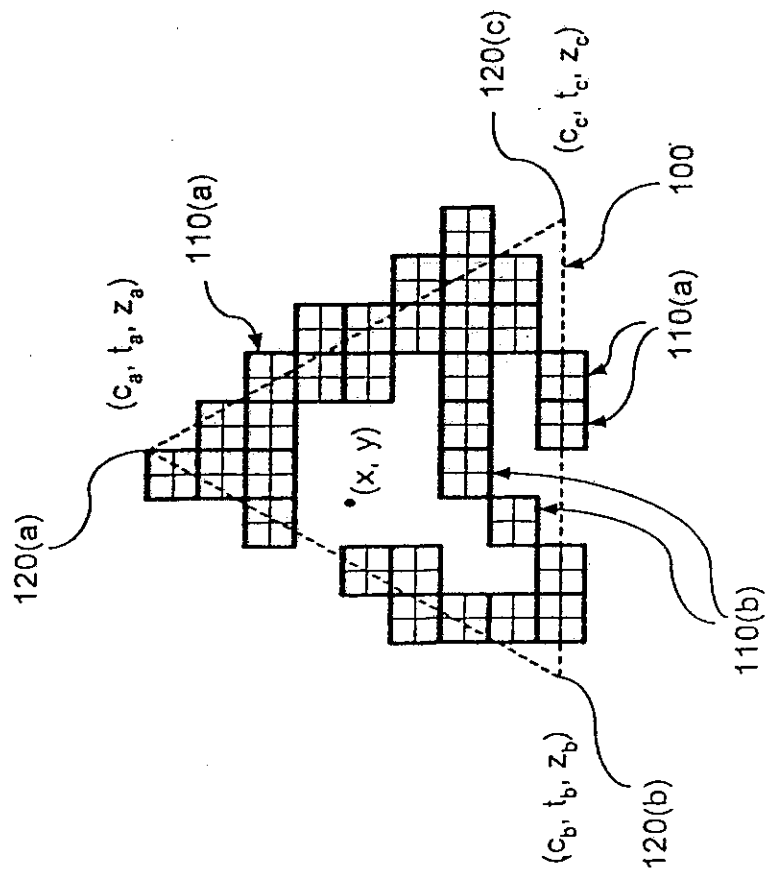


Fig. 1

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300

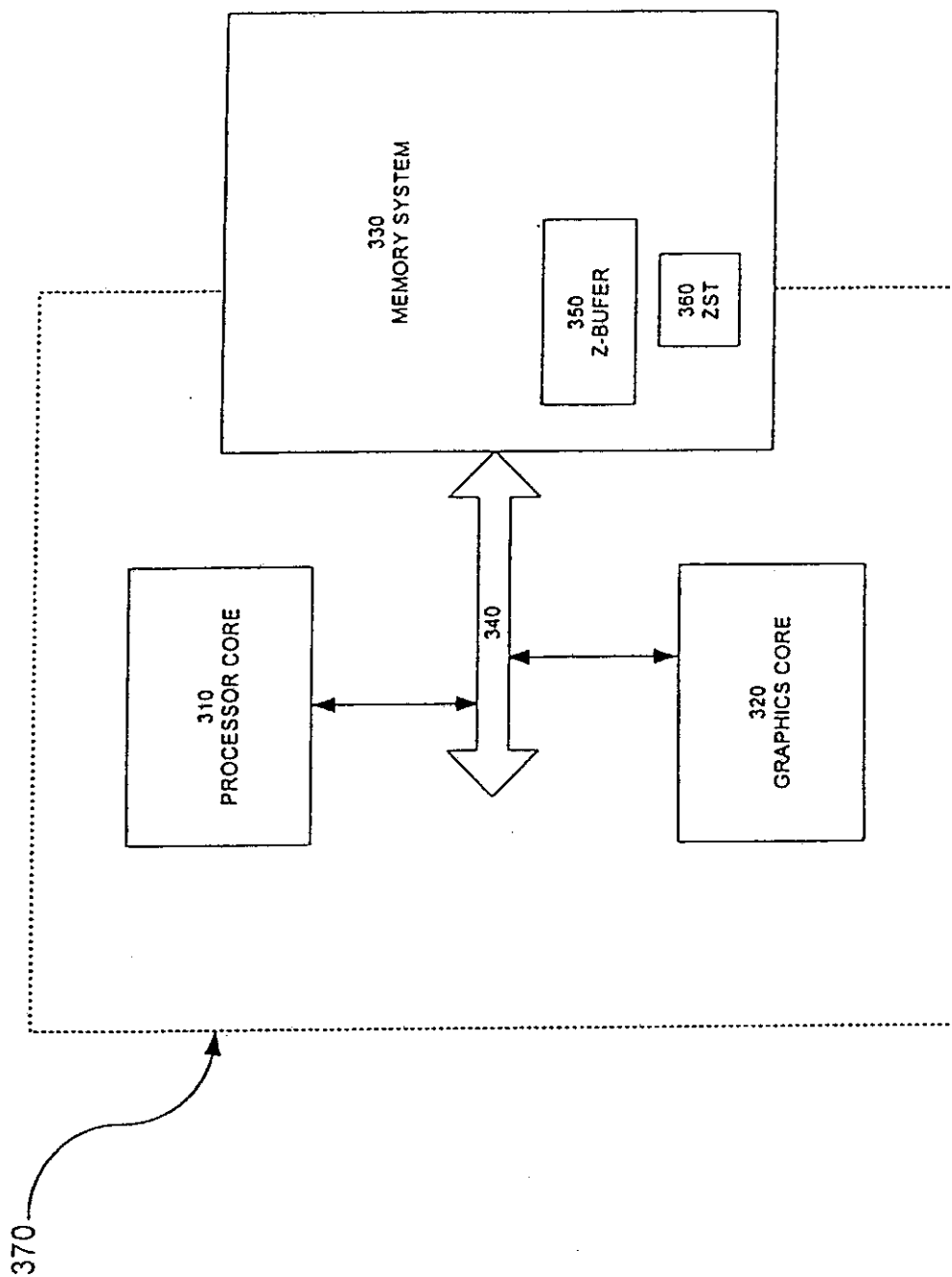


Fig. 3

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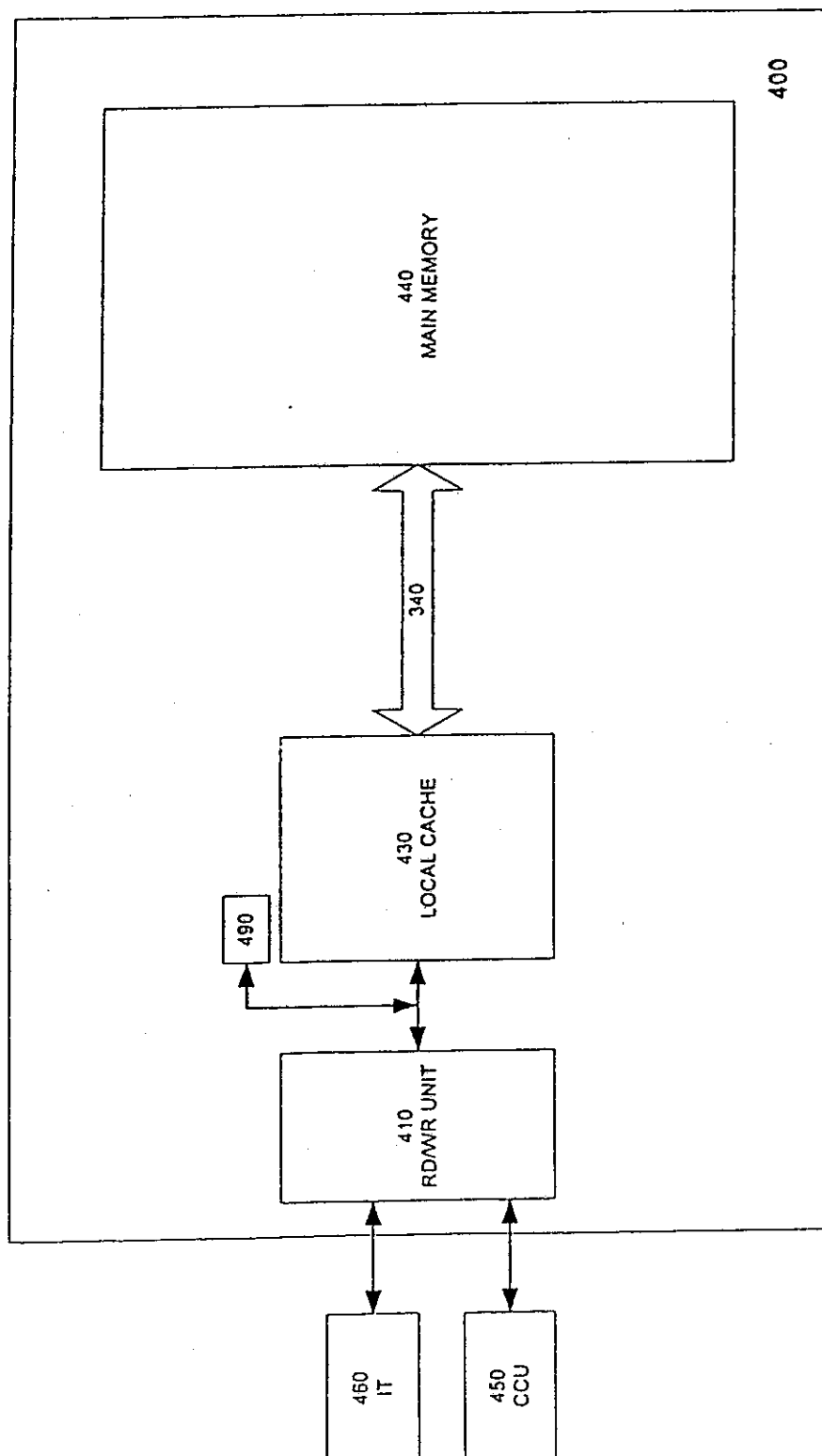


Fig. 4

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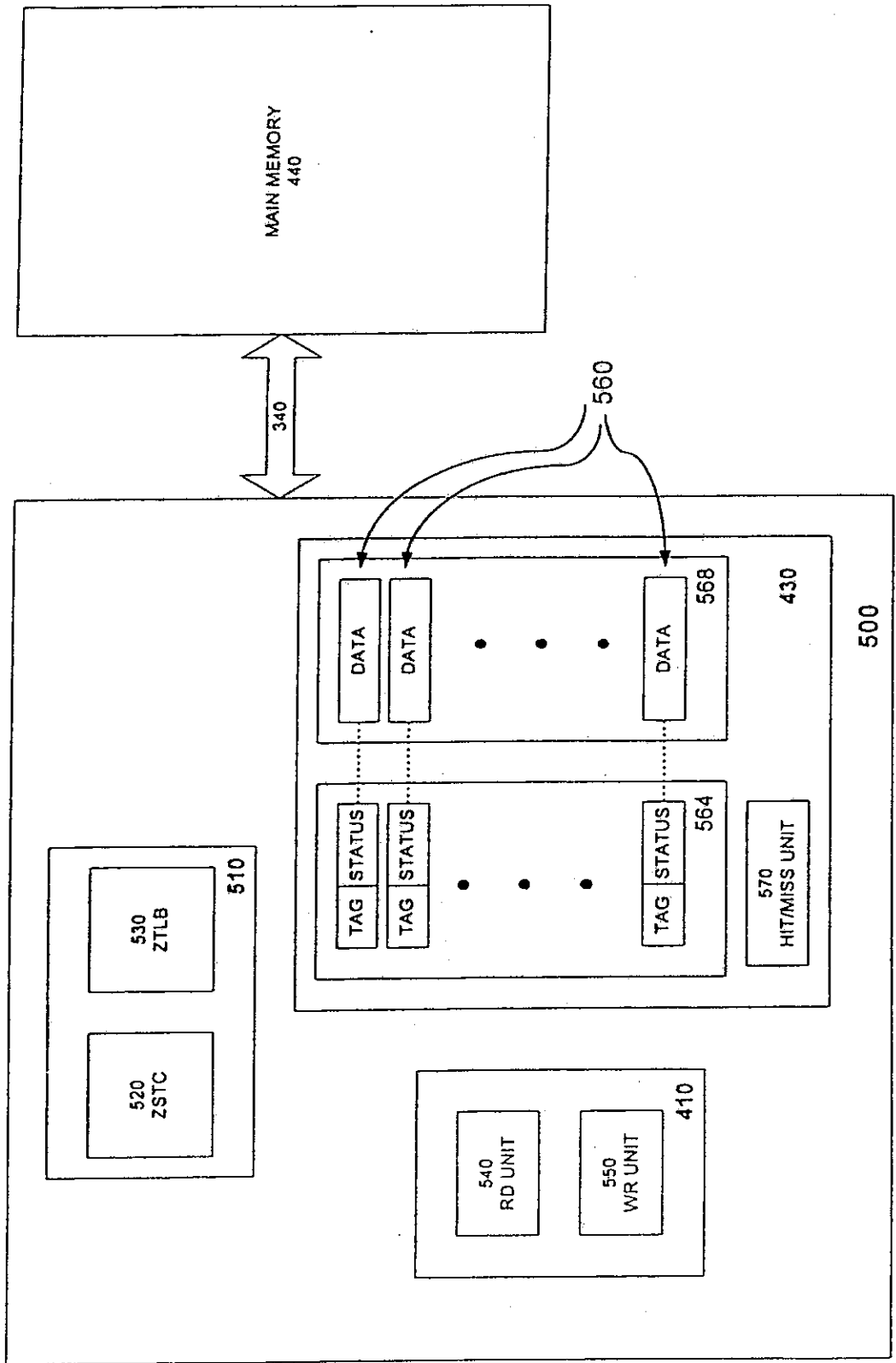


Fig. 5A

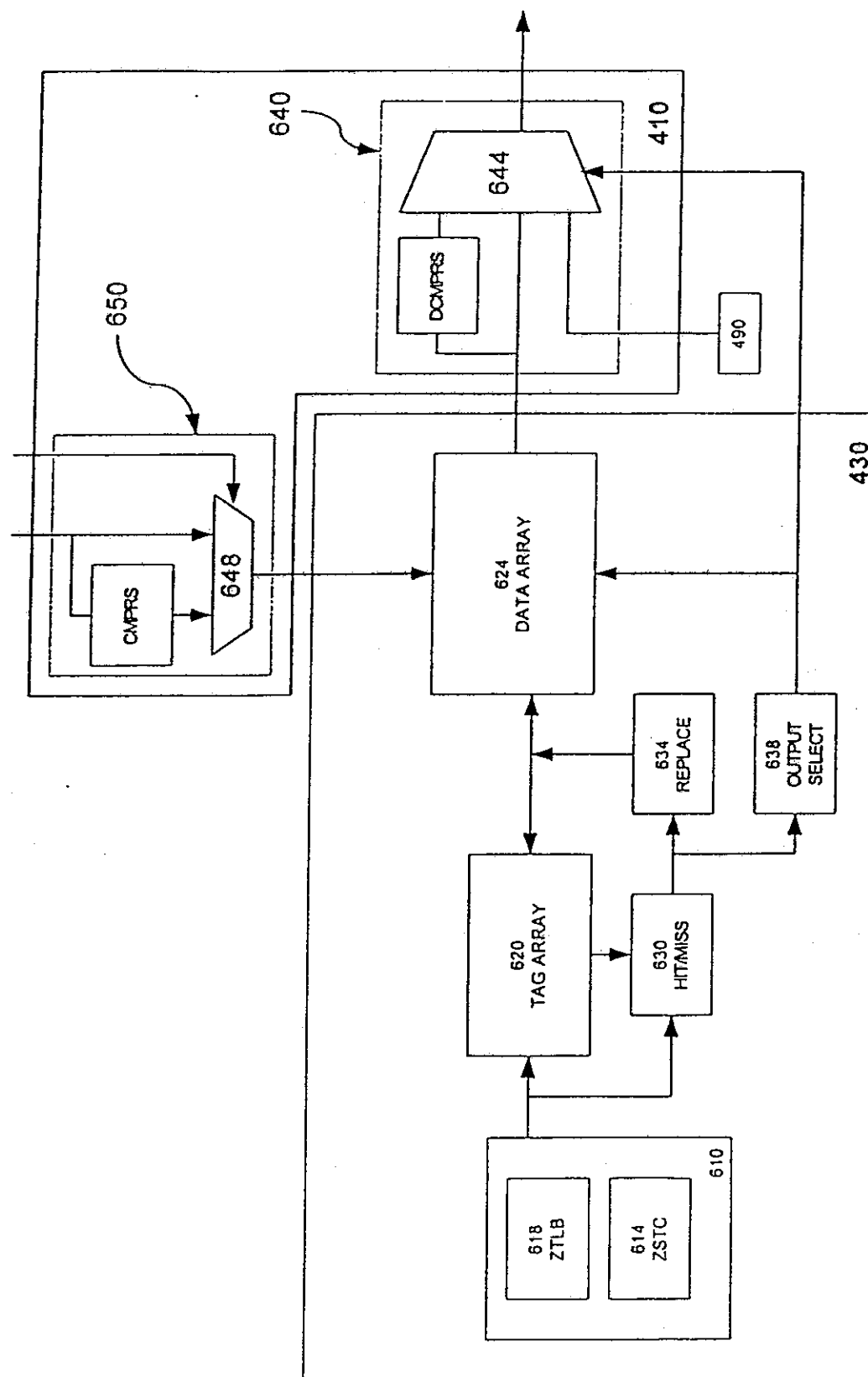


Fig. 6A

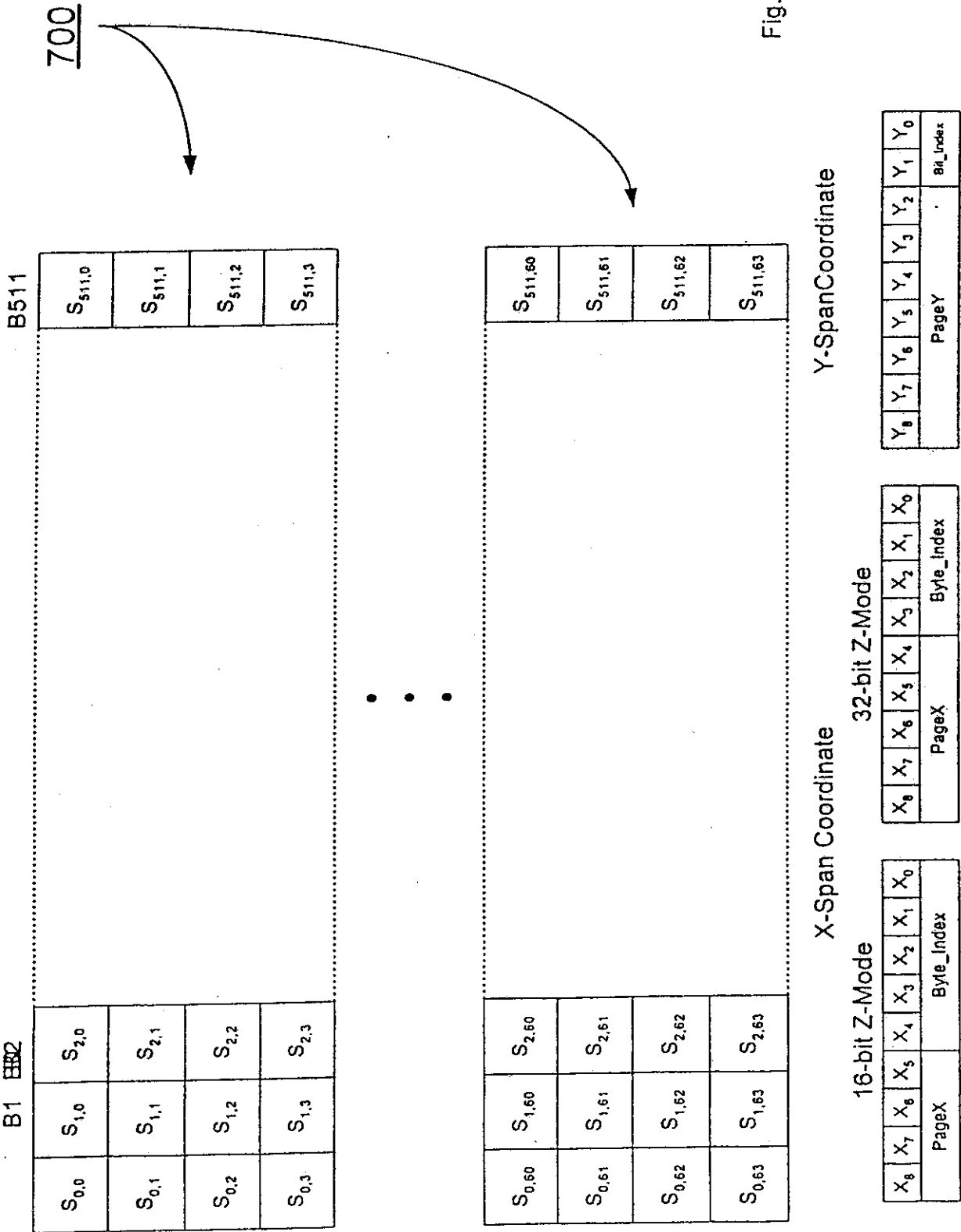


Fig. 7

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QW0	$Z_{0,0}$	$Z_{0,1}$	$Z_{0,2}$	$Z_{0,3}$
QW1	$Z_{1,0}$	$Z_{1,1}$	$Z_{1,2}$	$Z_{1,3}$
QW2	$Z_{2,0}$	$Z_{2,1}$	$Z_{2,2}$	$Z_{2,3}$
QW3	$Z_{3,0}$	$Z_{3,1}$	$Z_{3,2}$	$Z_{3,3}$



810

UNCOMPRESSED SPAN
BLOCK (16-BIT)

Fig. 8A

DQW0	$Z_{0,0}$	$Z_{0,1}$	$Z_{0,2}$	$Z_{0,3}$
DQW1	$Z_{1,0}$	$Z_{1,1}$	$Z_{1,2}$	$Z_{1,3}$
DQW2	$Z_{2,0}$	$Z_{2,1}$	$Z_{2,2}$	$Z_{2,3}$
DQW3	$Z_{3,0}$	$Z_{3,1}$	$Z_{3,2}$	$Z_{3,3}$



850

UNCOMPRESSED SPAN
BLOCK (32-BIT)

Fig. 8B

DQW	STNCL	C_y	C_x	$C_0 - C_y + Z_{bias}$
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900

COMPRESSED SPAN

Fig. 9

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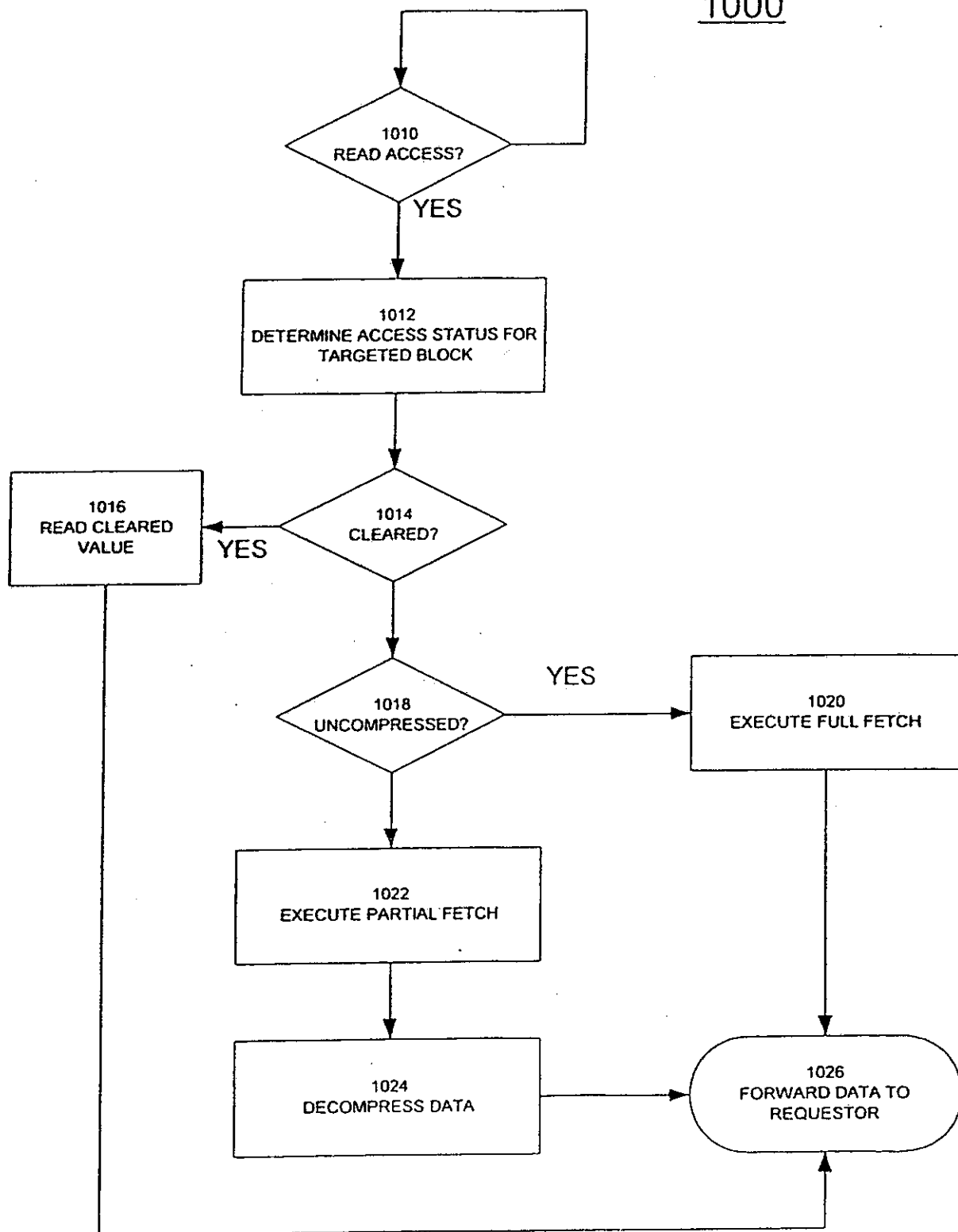
1000

Fig. 10A

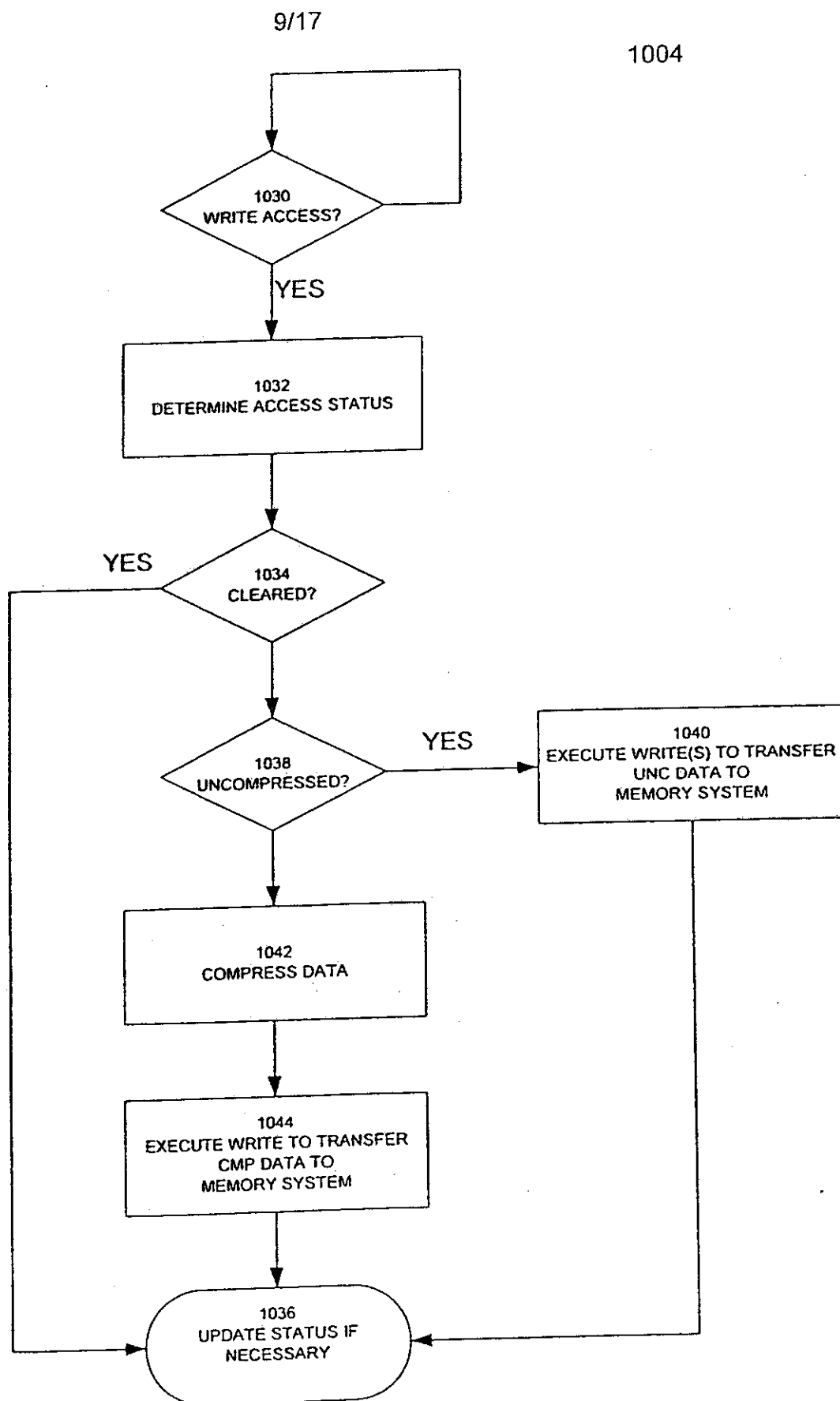


Fig. 10B

1008

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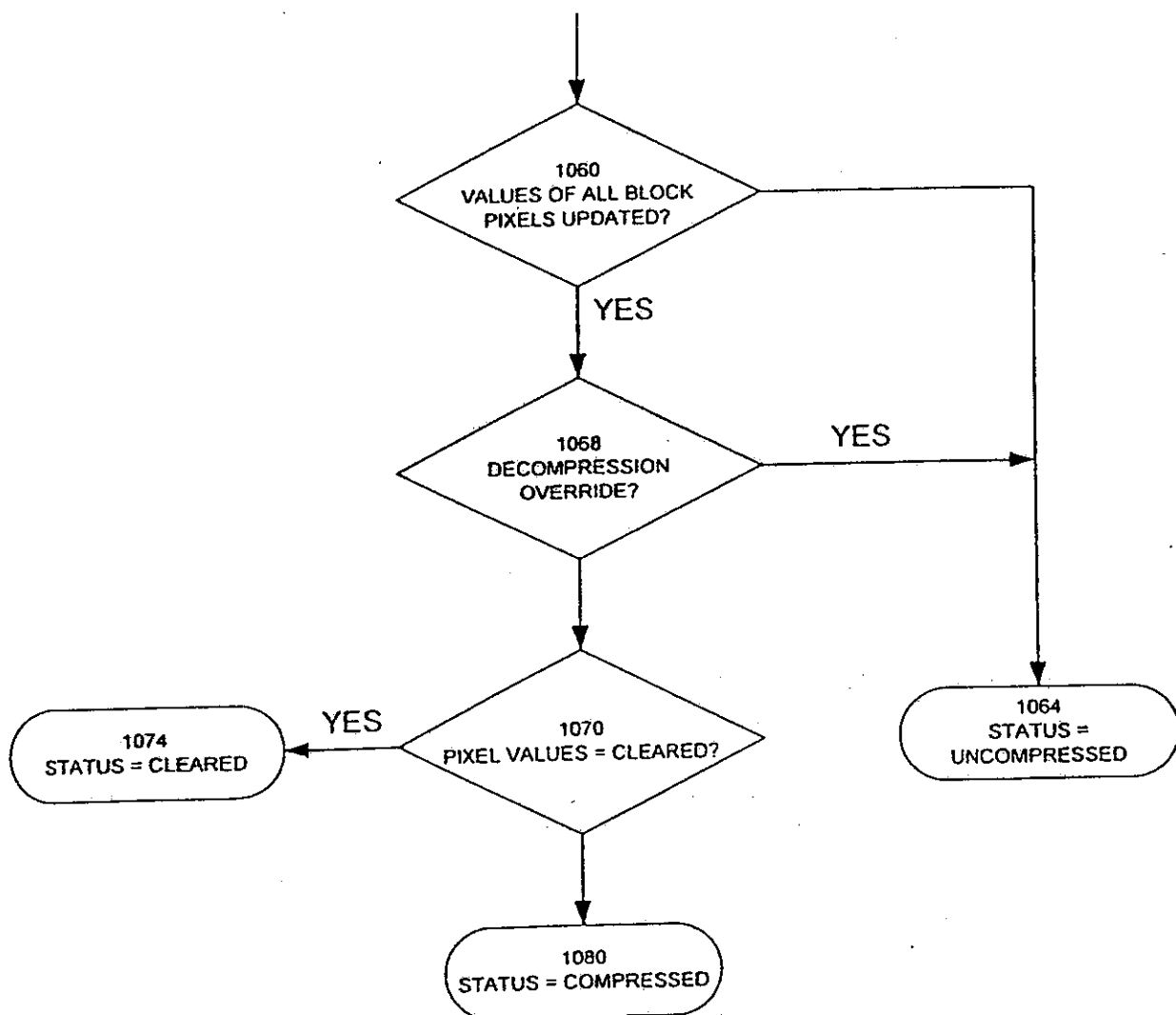
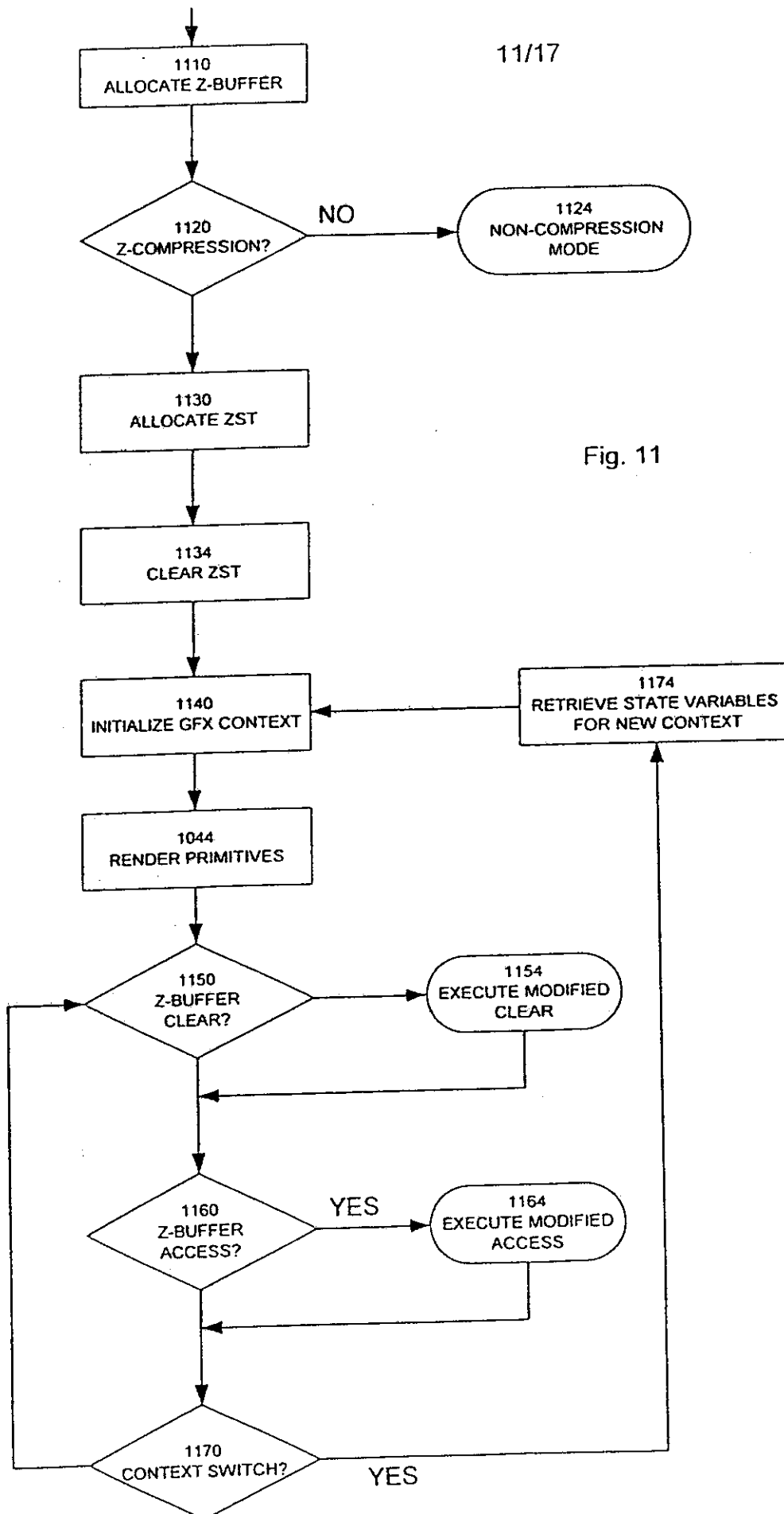


Fig. 10C



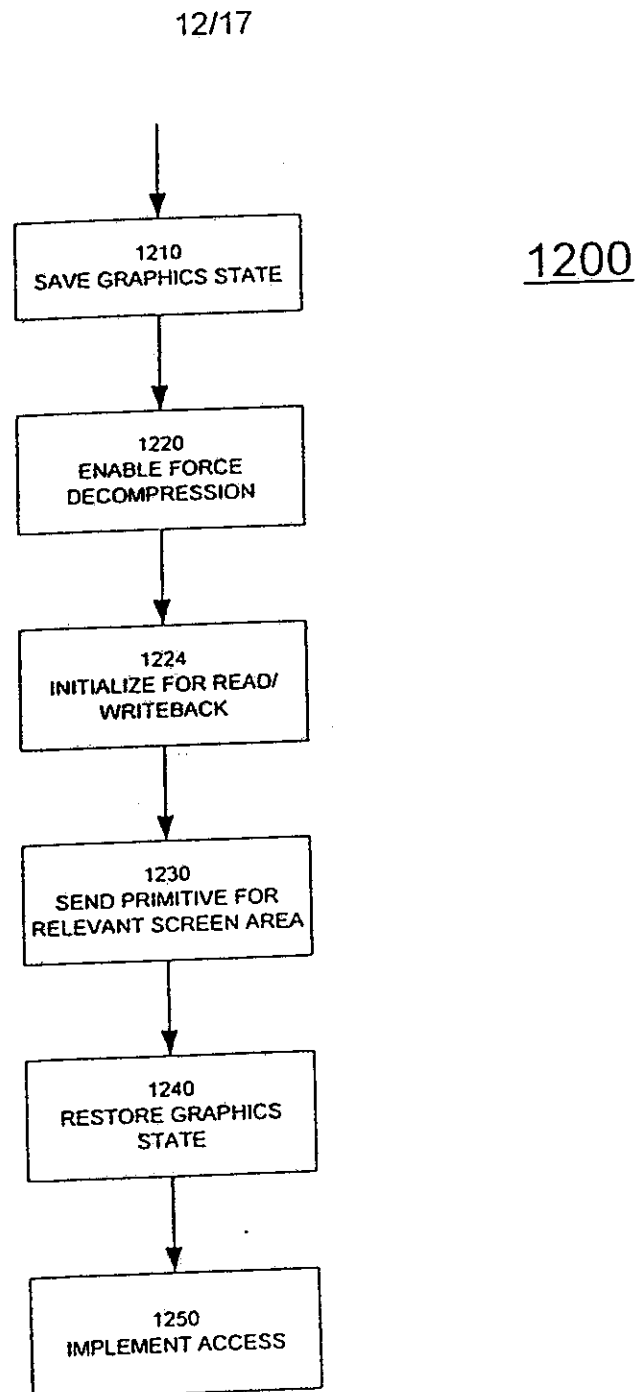


Fig. 12

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1300

CLEAR FULL Z-BUFFER
VIA BLIT

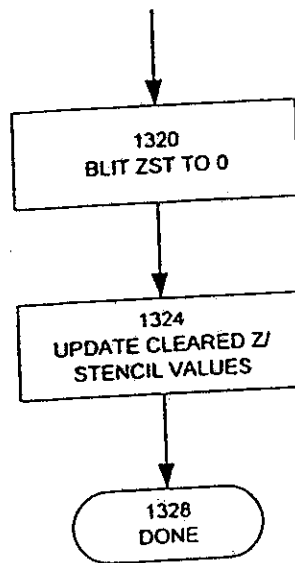


Fig. 13A

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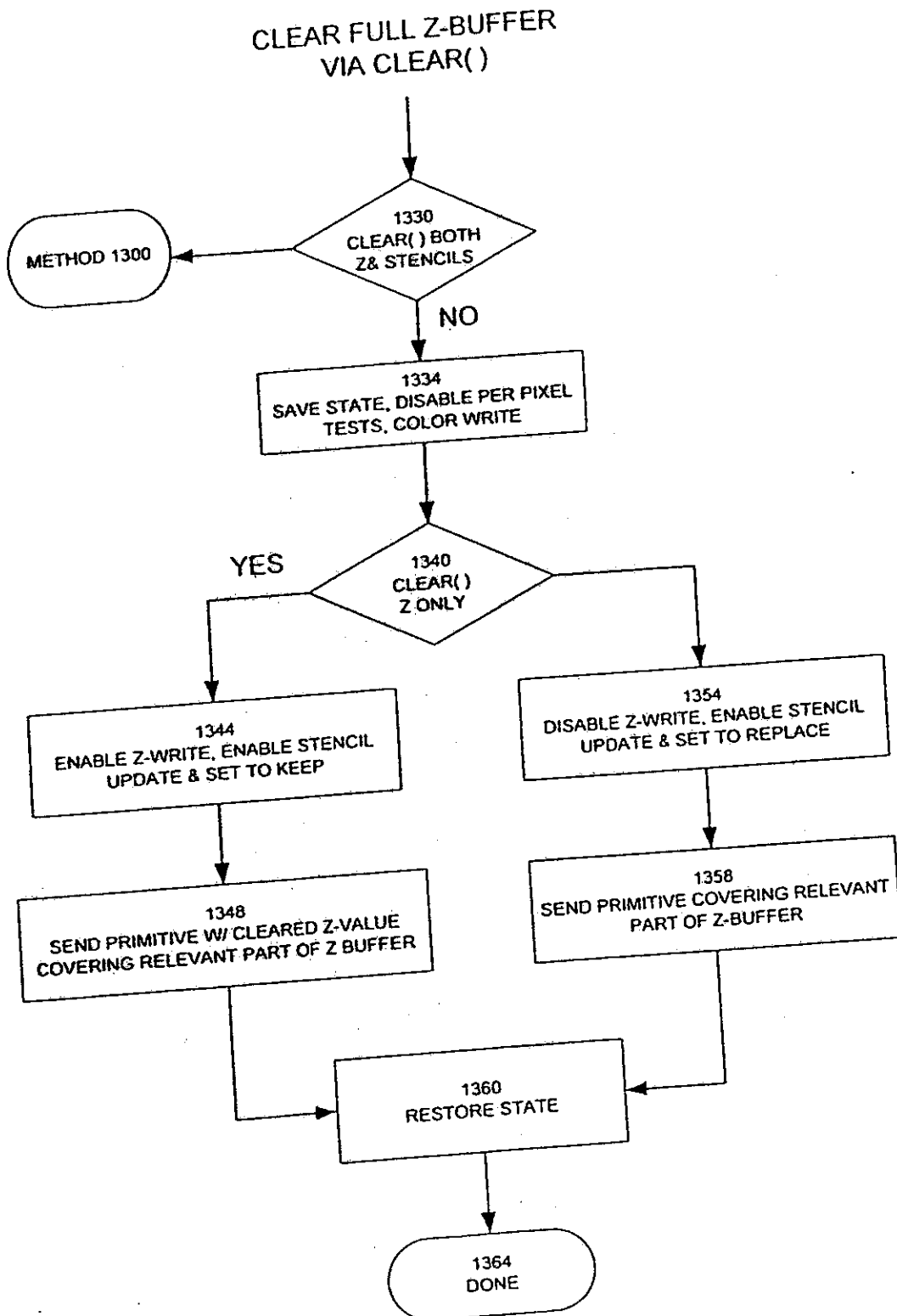
1304

Fig. 13B

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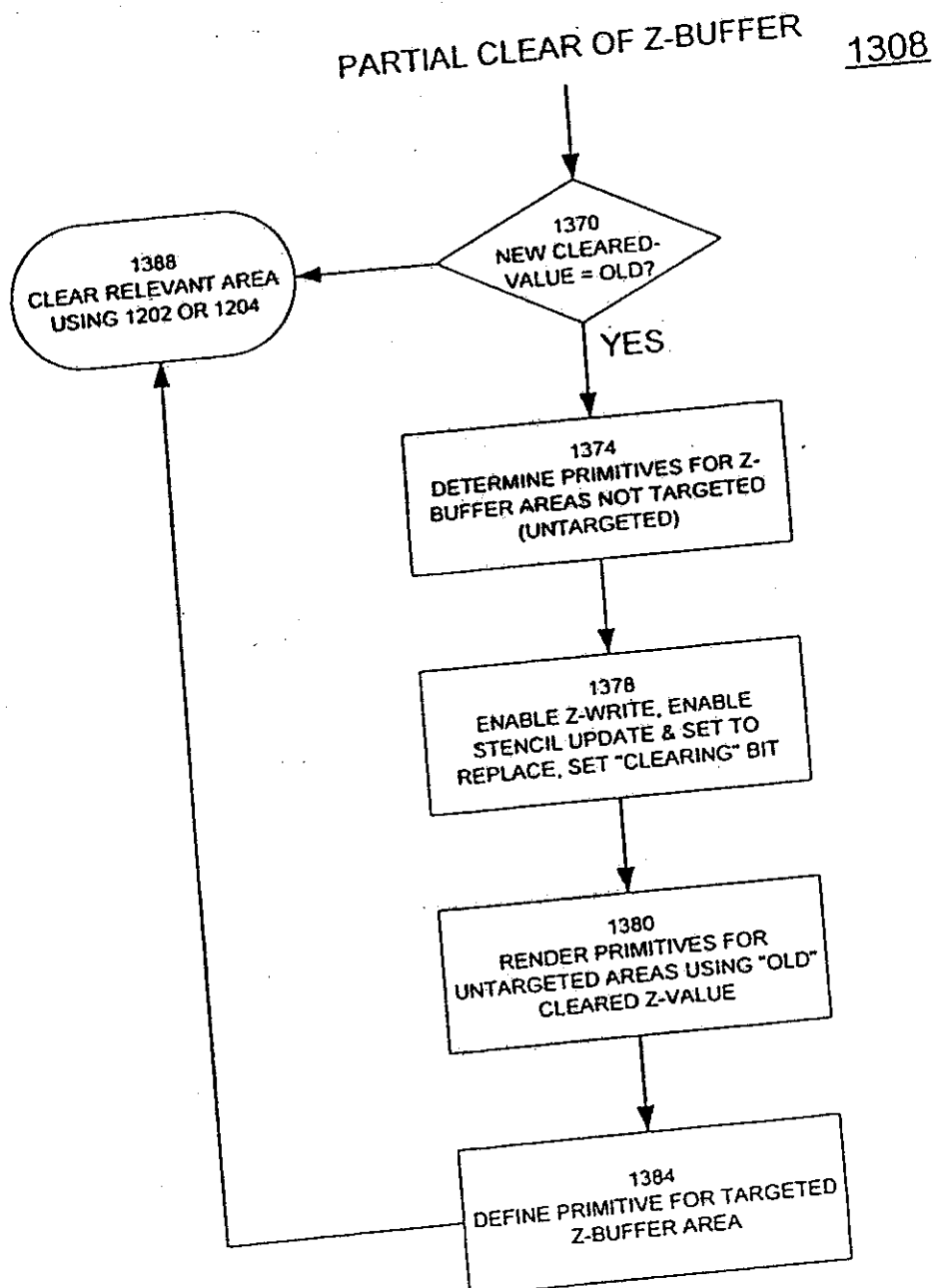


Fig. 13C

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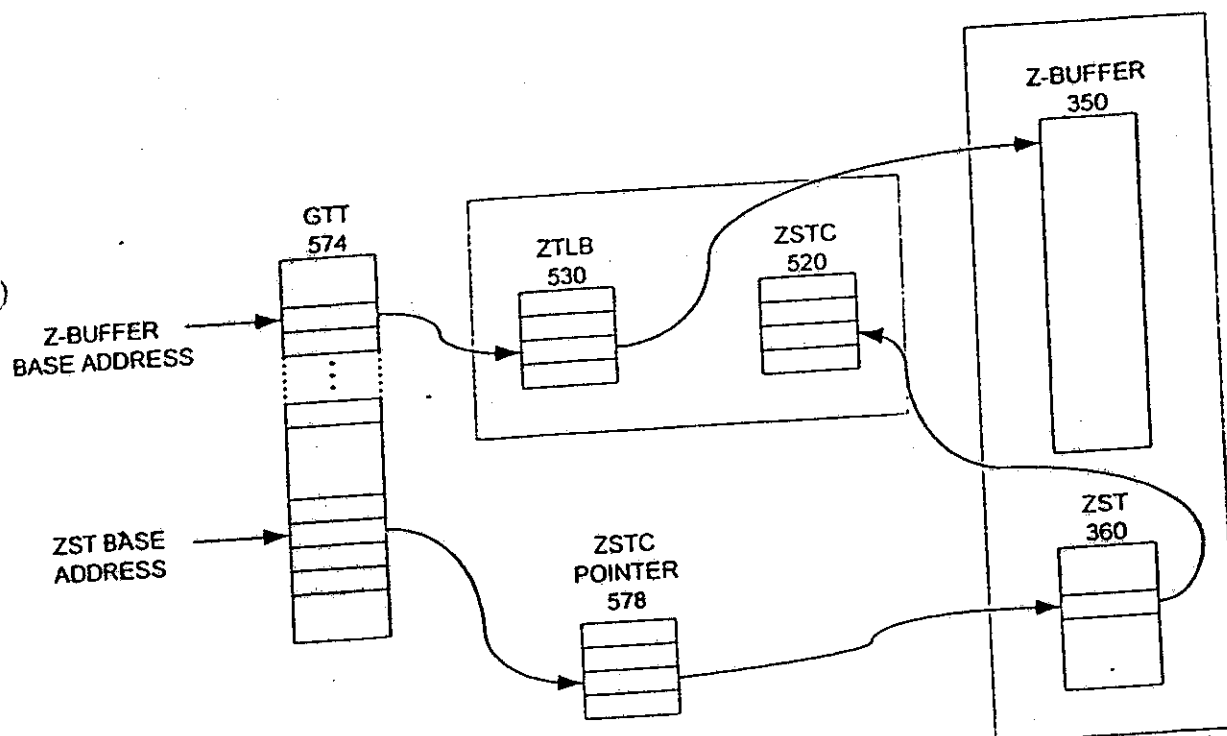


Fig. 5B

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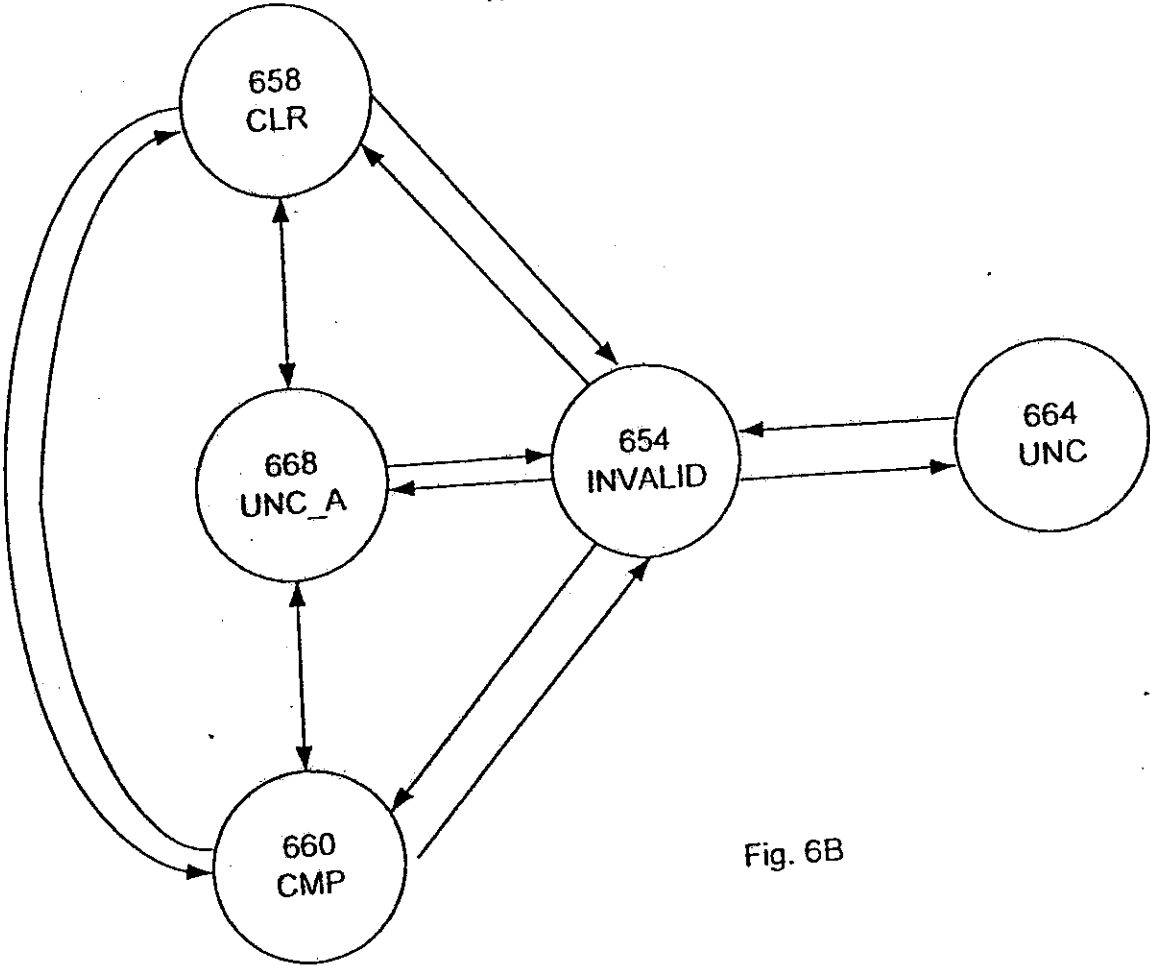


Fig. 6B

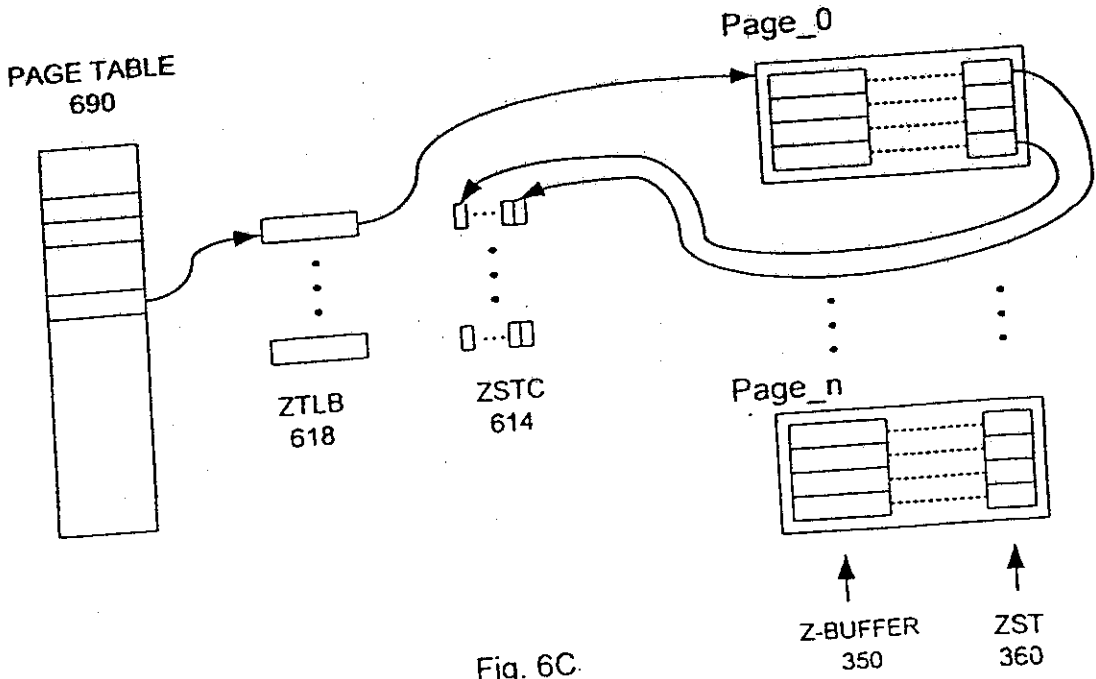


Fig. 6C