

**Abstract:**

Power amplifier stage of the voltage follower type.

The amplifier stage comprises at the output two MOS power transistors (M1, M2) in series between a positive supply line (2) and earth (1), the gate of each of these being controlled by a respective amplifier (A1, A2). An input signal (Vi) is applied to the input of these amplifiers so as to be compared with an output signal (Vo) of the amplifier stage.

According to the invention, the amplifiers (A1, A2) are each provided with an inhibiting input (20, 22) with which it is possible to connect to earth the gate of the corresponding power transistor (M2, M1), and the amplifier stage comprises two transistors which act as switches (SW1, SW2) achieving that either the one or the other of the inhibiting inputs (20, 22) is connected to earth, so that a simultaneous conduction of the power transistors (M1, M2) is excluded.

Application: The control of low-power motors.

Fig. 1

4-0017038