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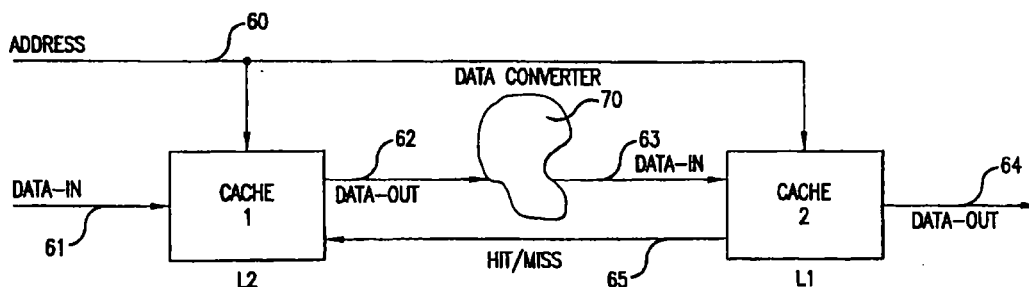
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ACTION

(54) Title: DUAL CACHE WITH MULTIPLE INTERCONNECTION OPERATION



(57) Abstract: A computer system having cache modules interconnected in series includes a first and a second cache module directly coupled to an address generating line for parallel lookup of data and data conversion logic coupled between the first cache module and said second cache module.

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