

DATA ACCESS METHOD AND COMPUTING DEVICE

ABSTRACT

This application relates to a data access method and a computing device. The method is performed by a data processing unit connected to a processor. According to the method, after
5 writing an access request into a first cache queue of the processor, the data processing unit sends, in a peripheral component interconnect express point-to-point transmission mode, a first instruction to a storage device to which an identifier of the storage device points, where the first instruction instructs the storage device to obtain the access request from the first cache queue and execute the access request. The access request includes the identifier of the storage device to be
10 accessed, and the storage device is connected to the processor and not directly connected to the data processing unit. The first instruction includes location information of the access request in the first cache queue. Therefore, the data processing unit may directly access the host-side storage device, and does not need to access the storage device via the processor of a host, so that resources of the host are not occupied. In addition, this access manner can simplify a hardware connection
15 manner and reduce hardware design complexity.

Figure 15