

INTEGRATED CIRCUIT DEVICE AND METHOD OF MANUFACTURING THEREOF

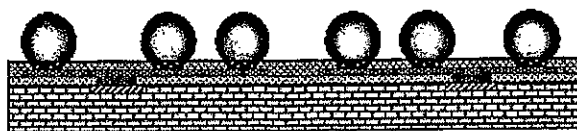


Fig. 1A – Prior art

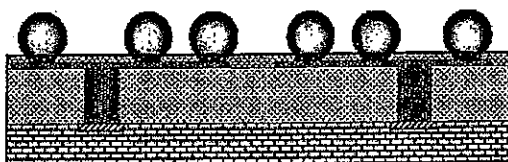


Fig. 1B – Prior art

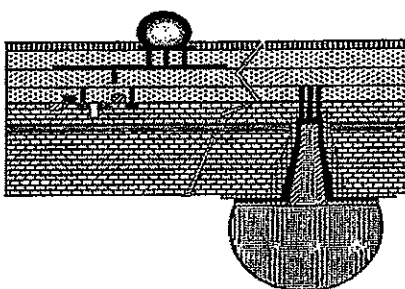


Fig. 1C – Prior art

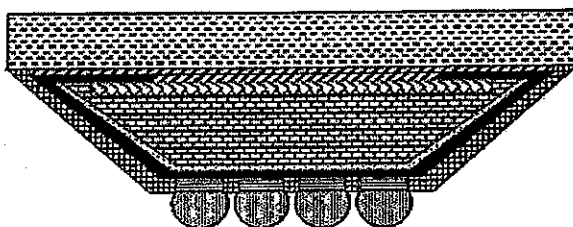


Fig. 1D – Prior art

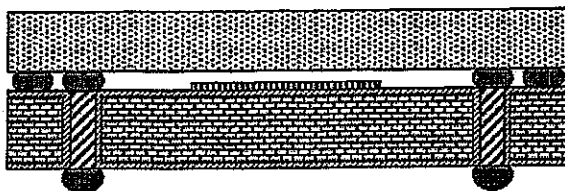


Fig. 1E – Prior art

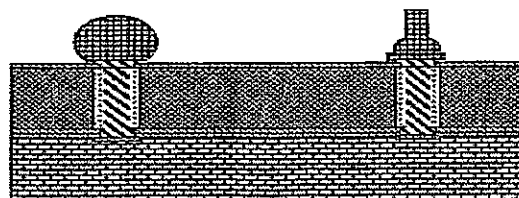


Fig. 1F – Prior art

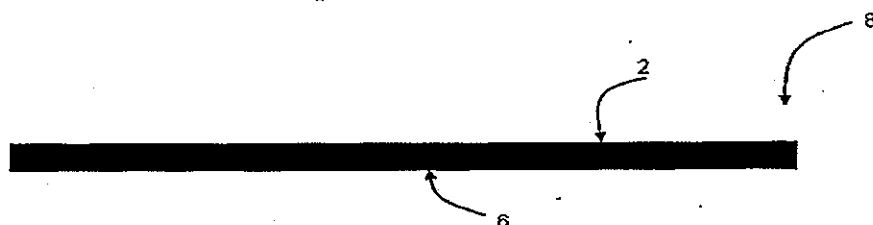


Fig. 2

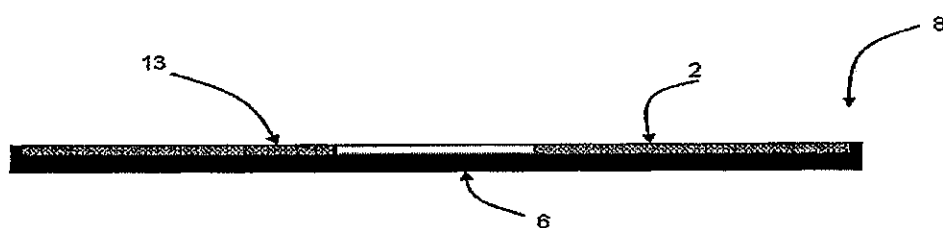


Fig. 3

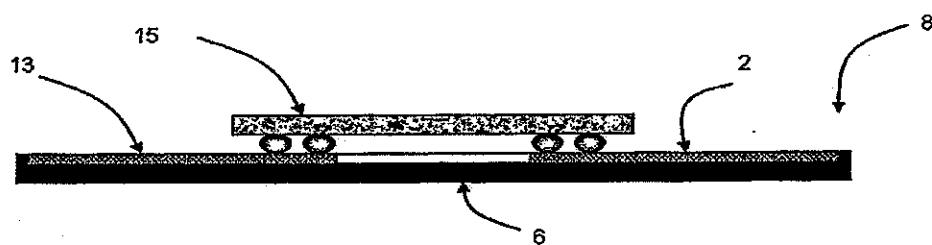


Fig. 4

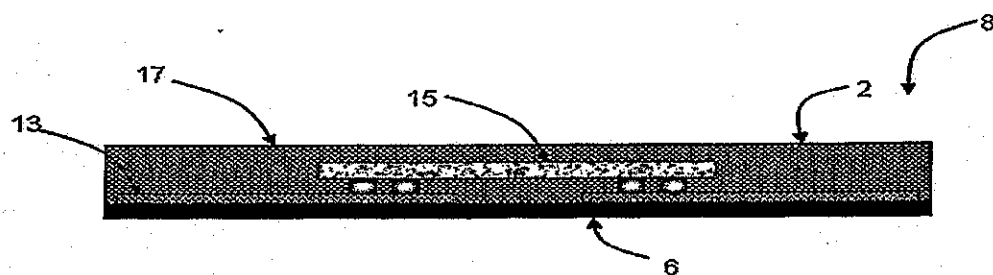


Fig. 5

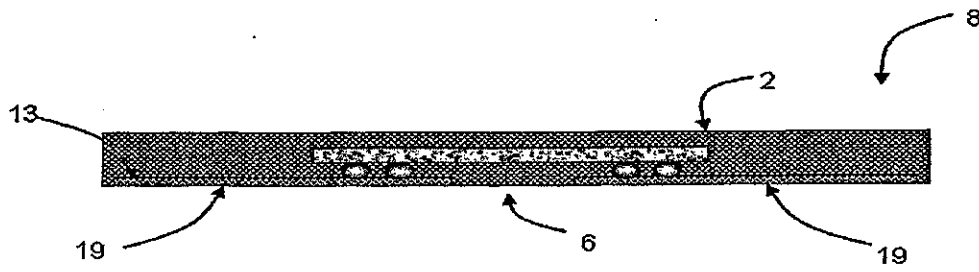


Fig. 6

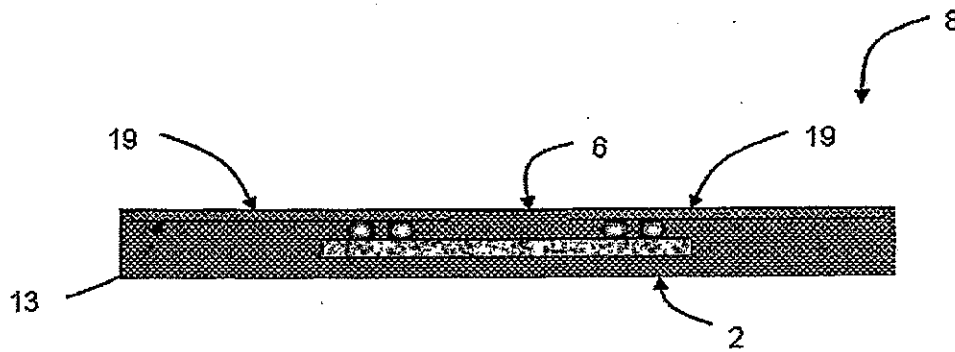


Fig. 7

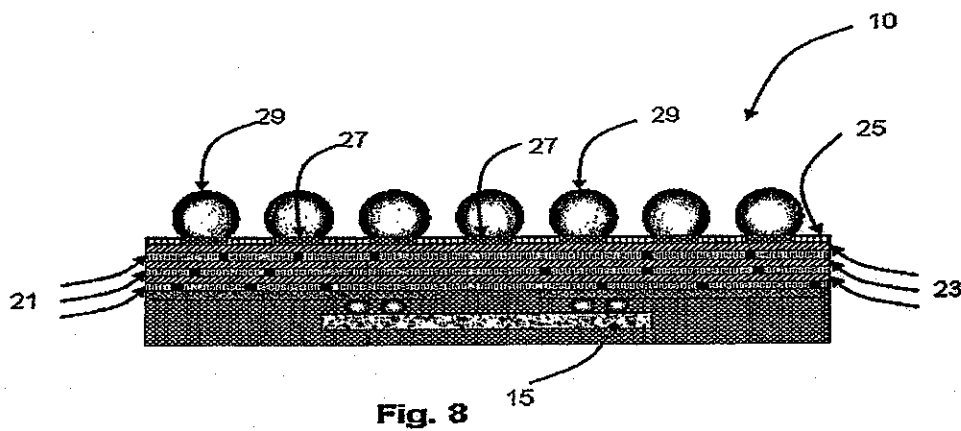


Fig. 8

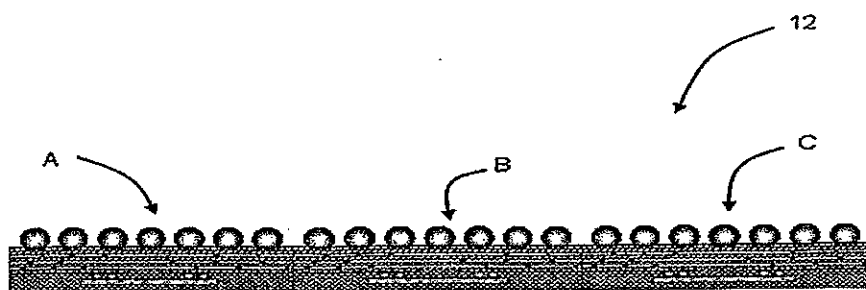


Fig. 9

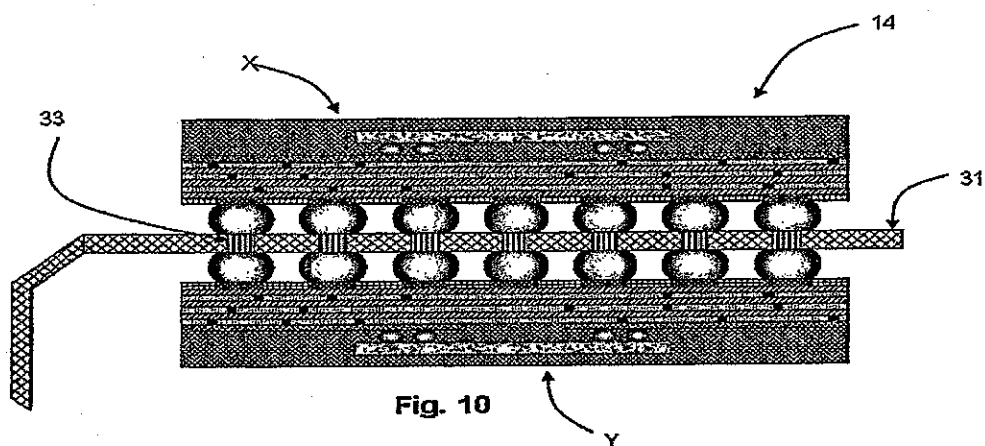


Fig. 10

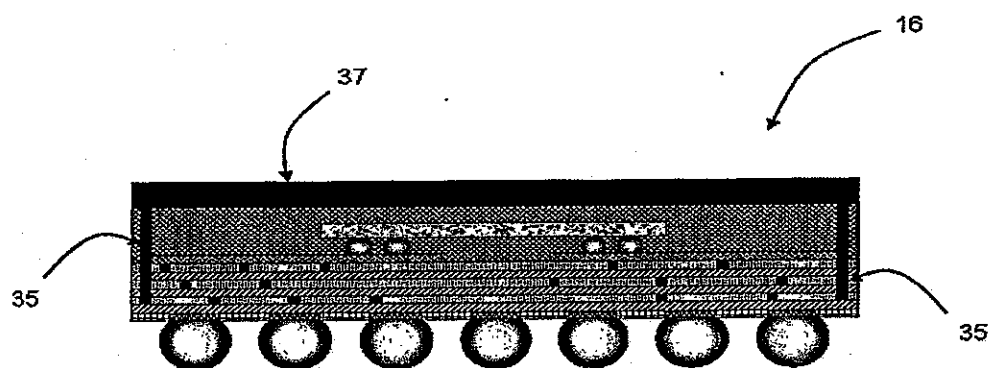


Fig. 11