

FIG. 1

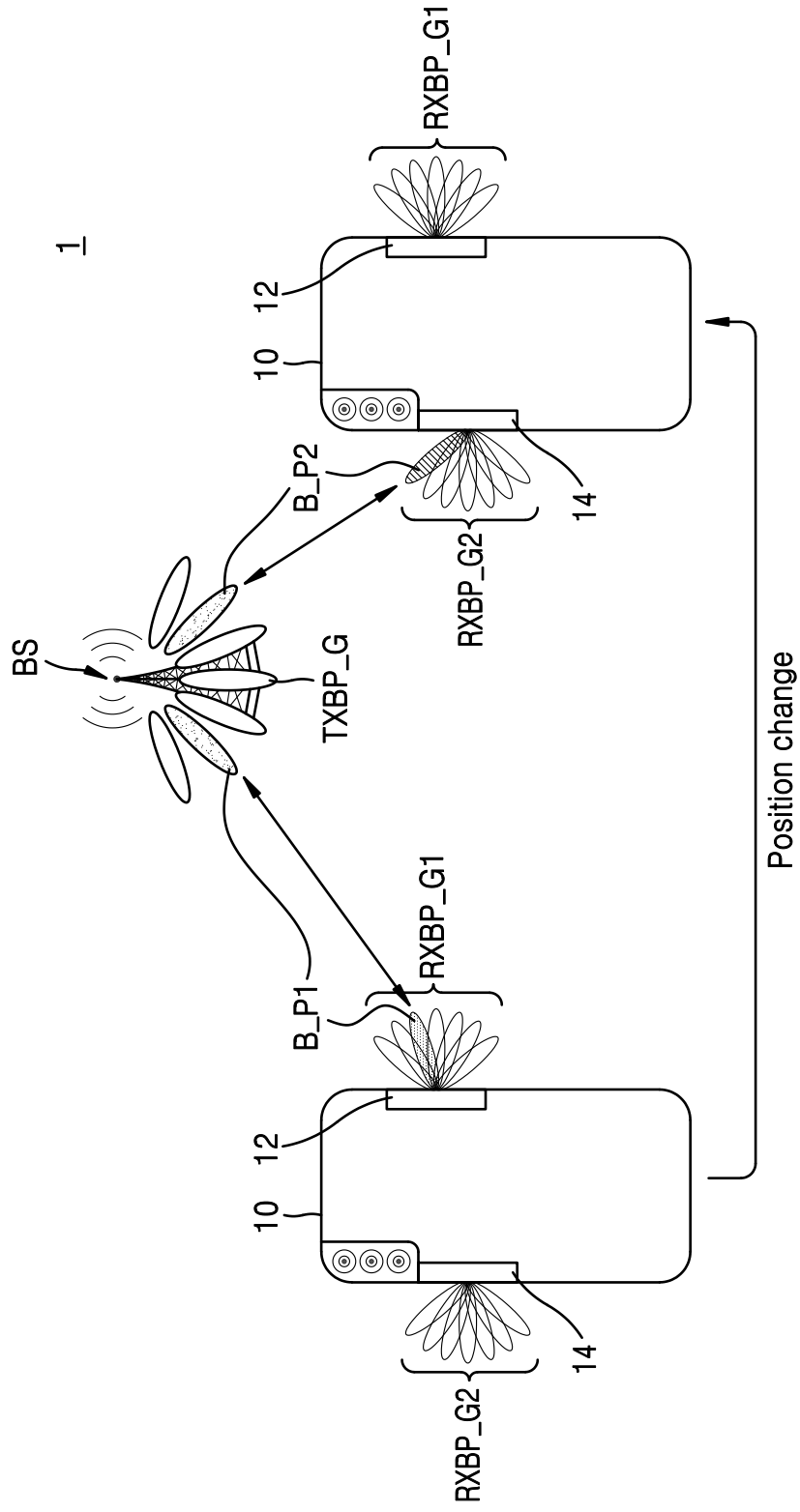


FIG. 2

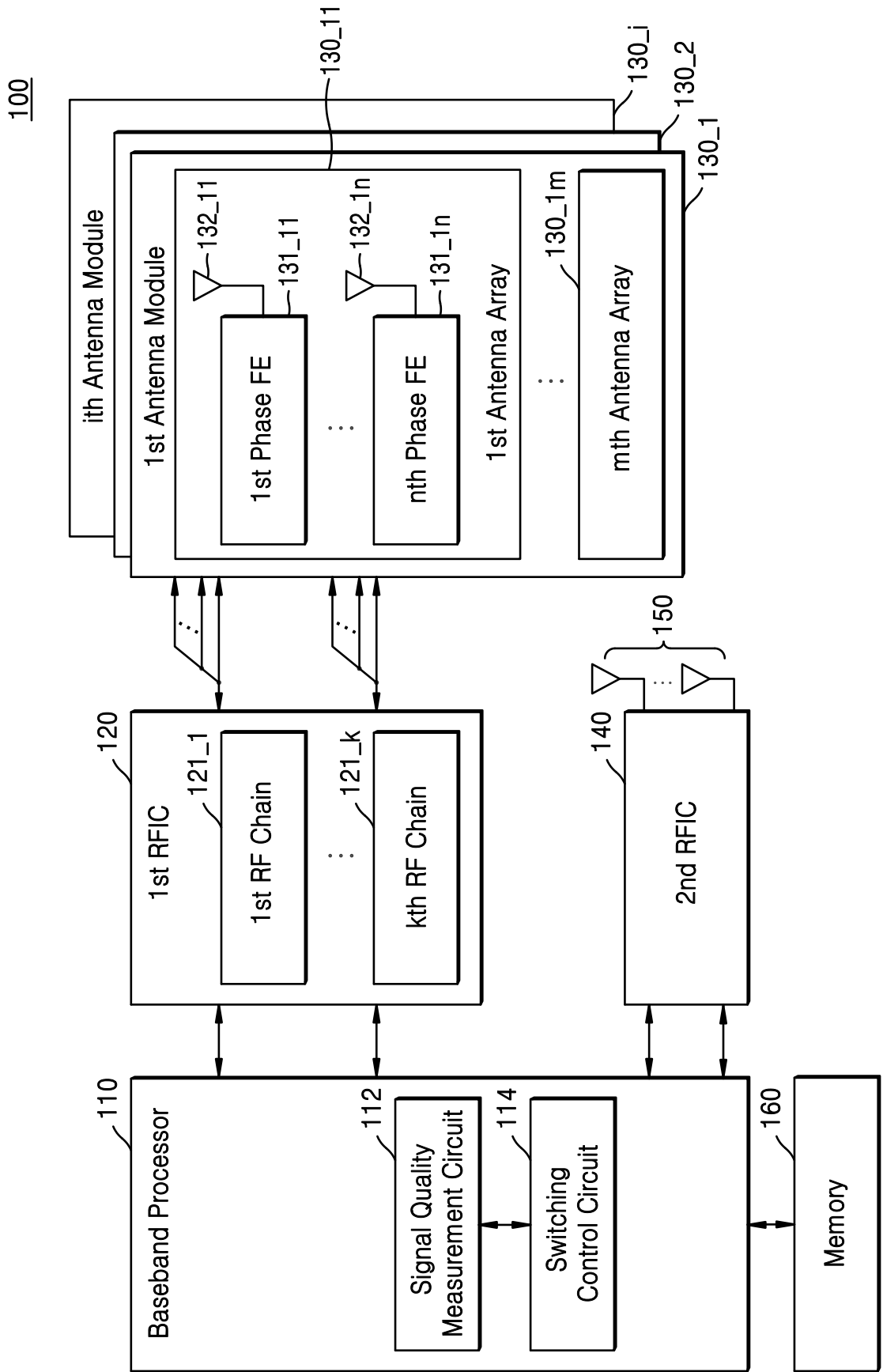


FIG. 3

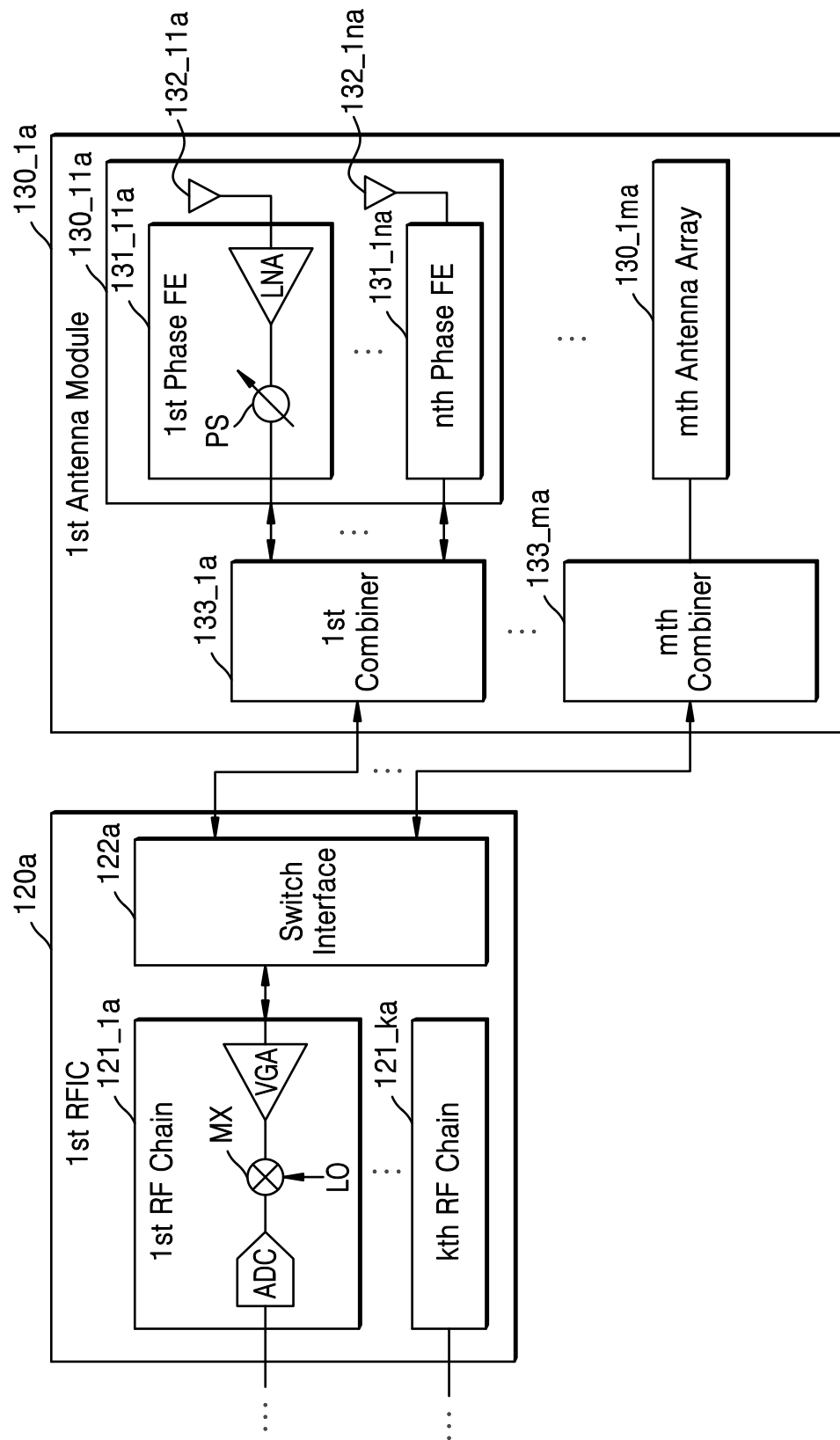


FIG. 4A

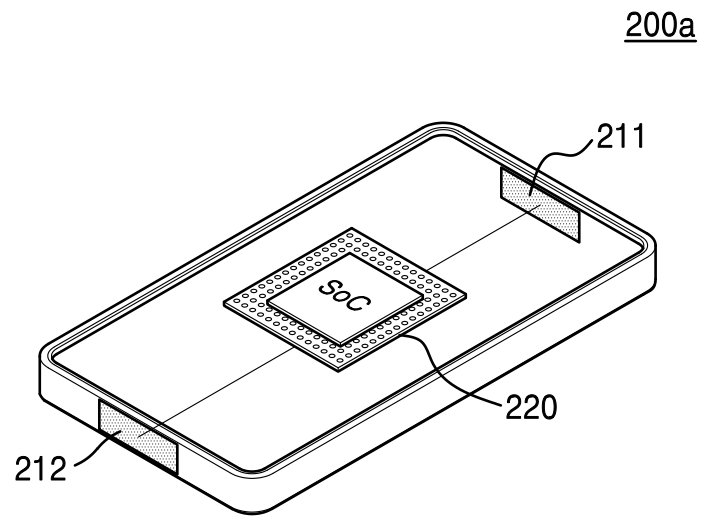


FIG. 4B

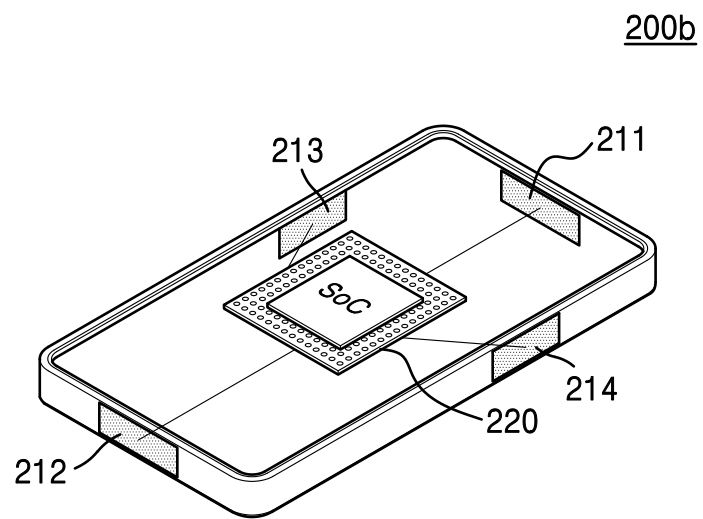


FIG. 5

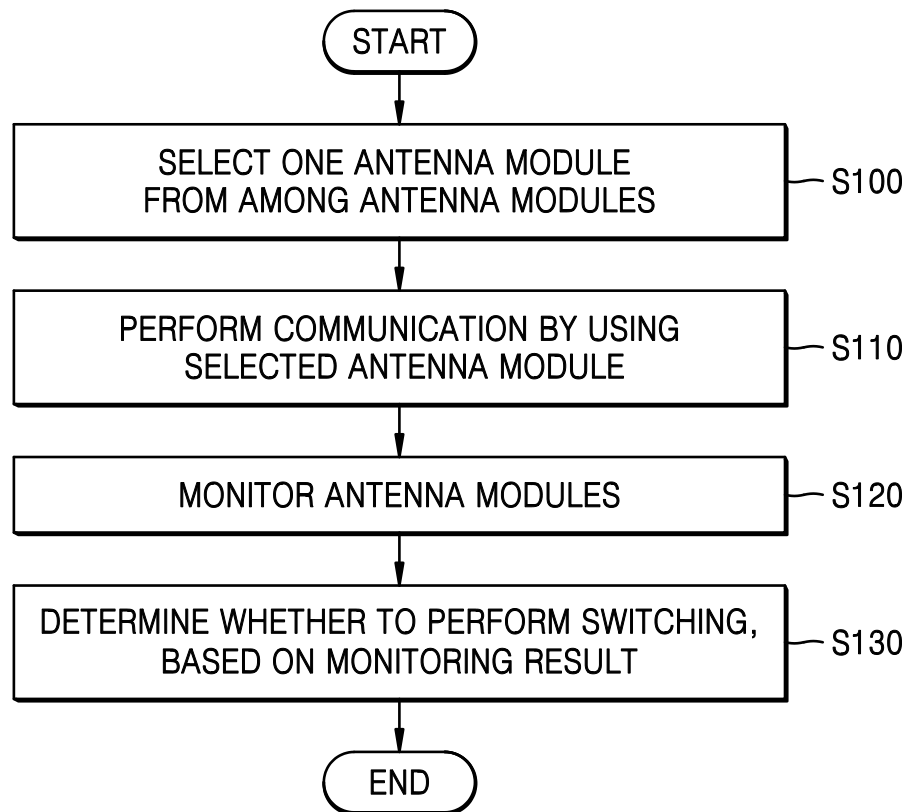


FIG. 6

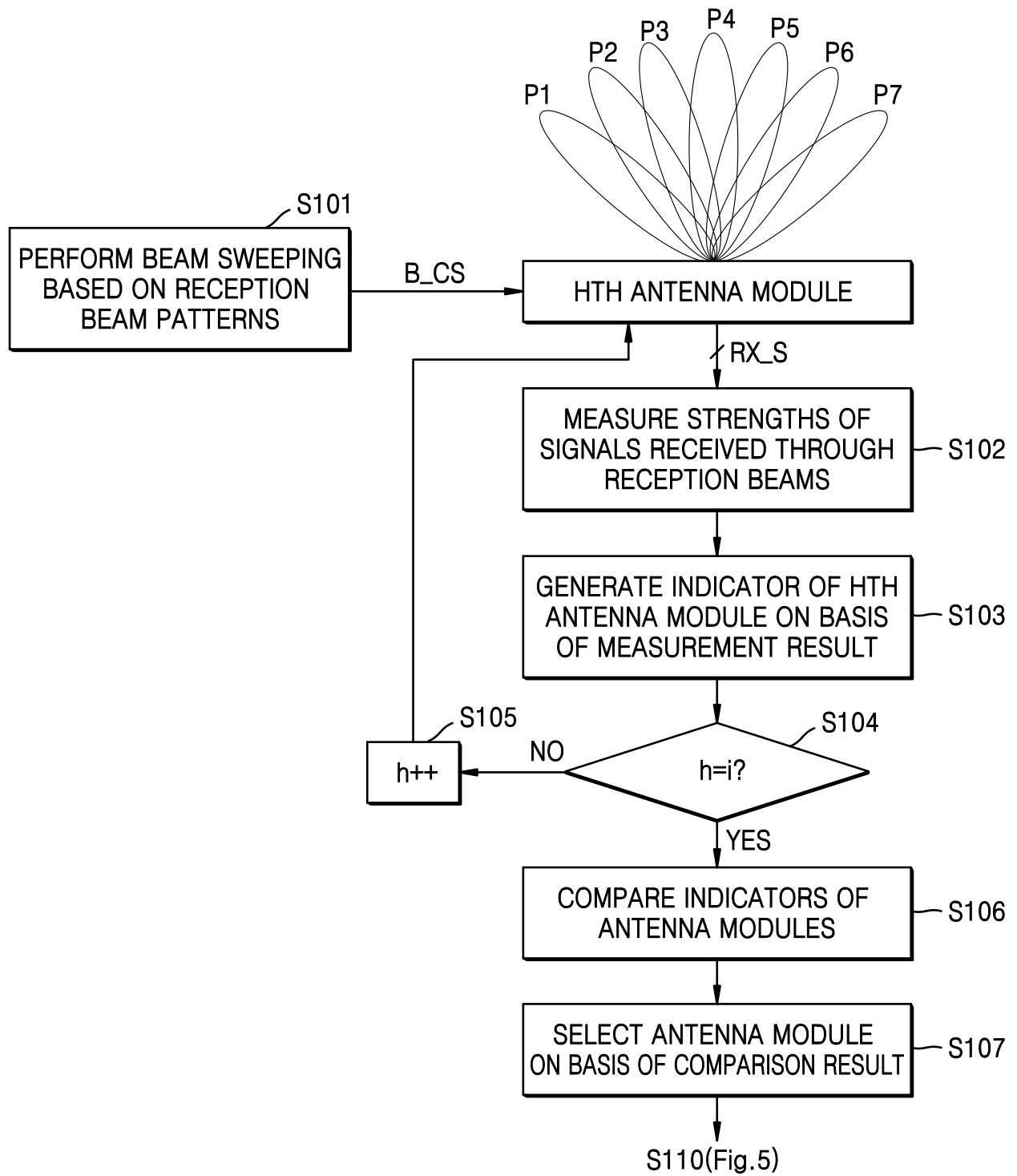


FIG. 7

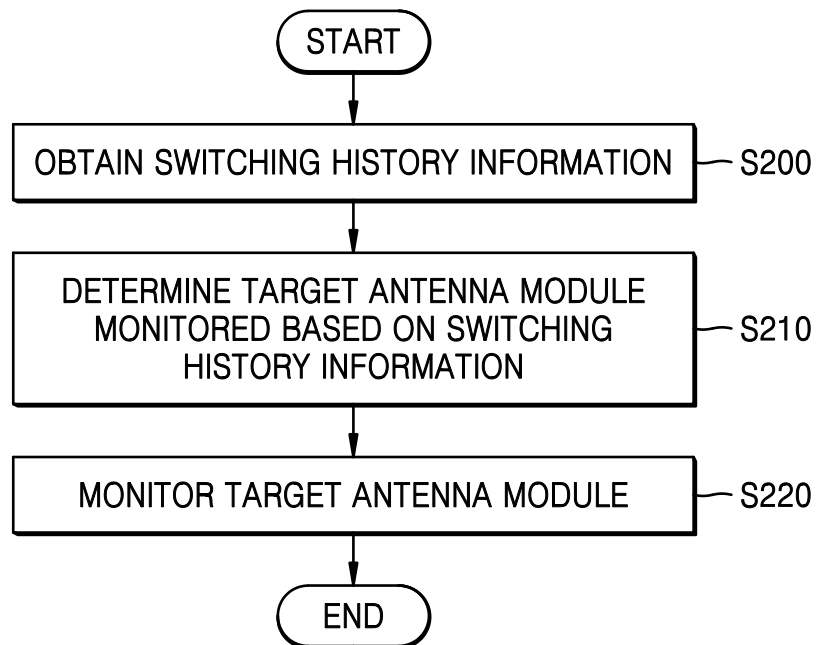


FIG. 8A

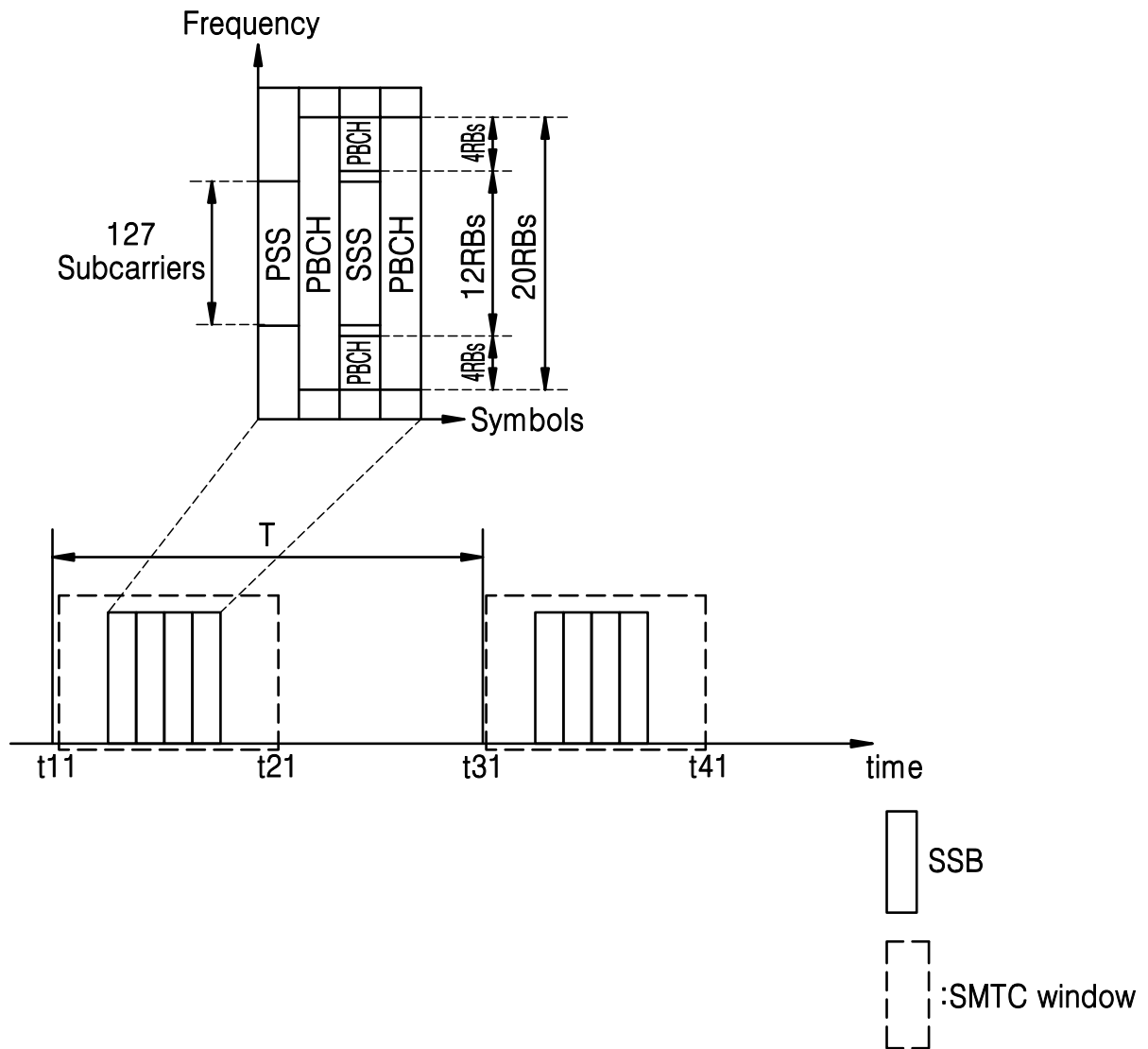


FIG. 8B

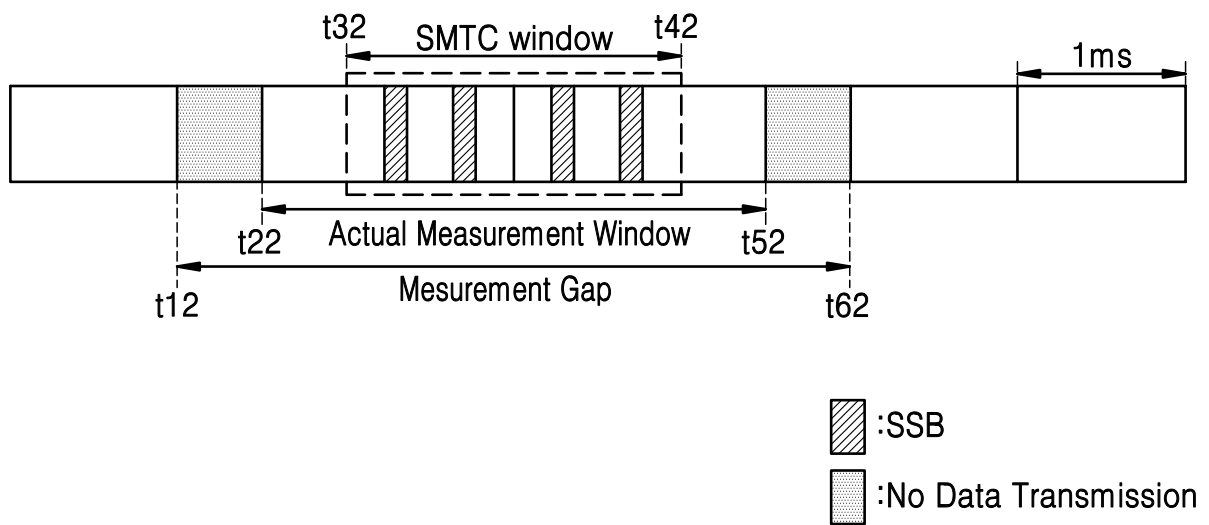


FIG. 9A

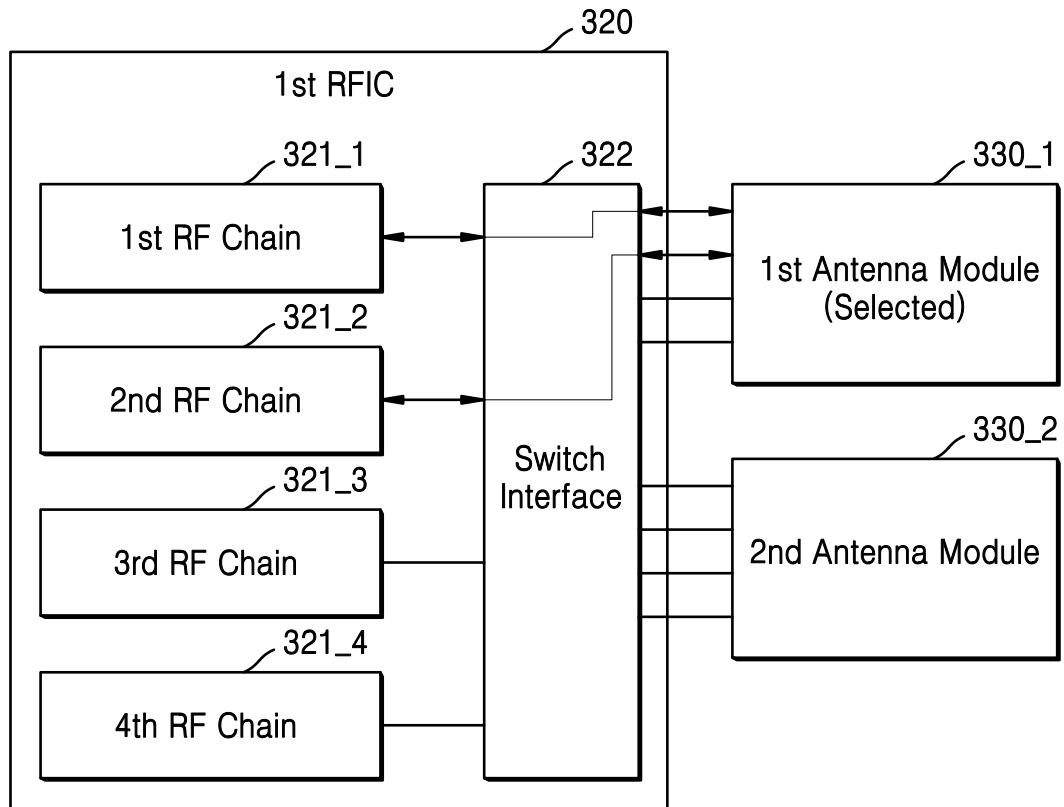


FIG. 9B

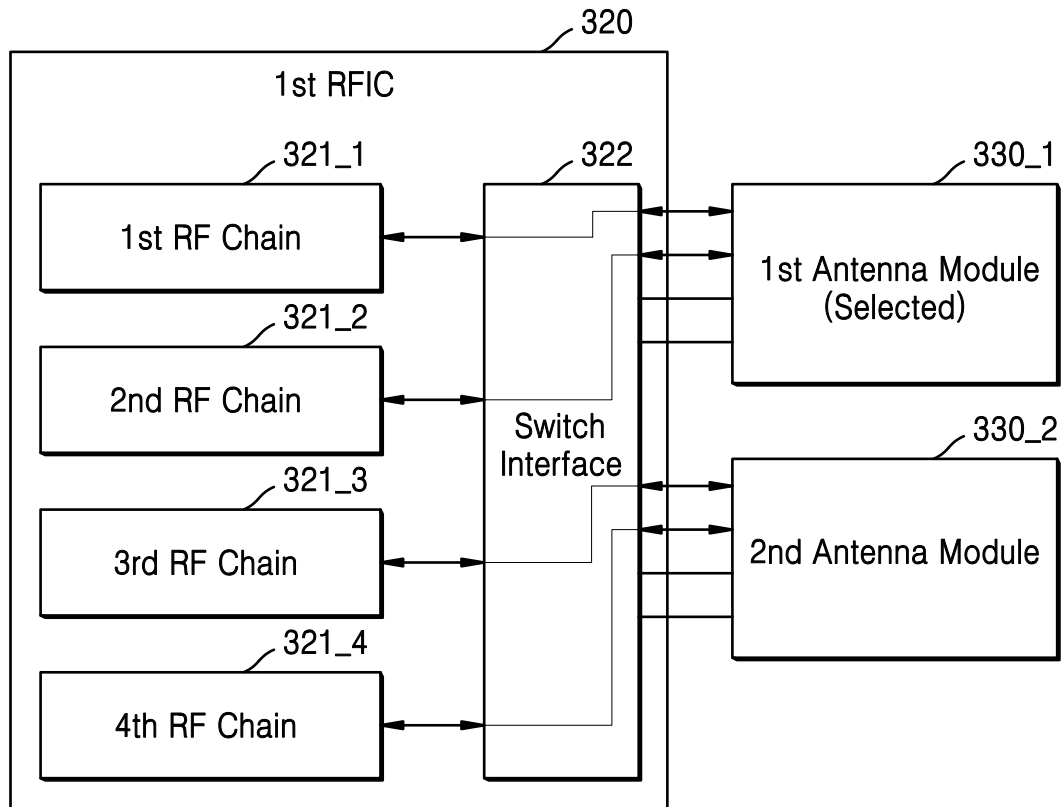


FIG. 10

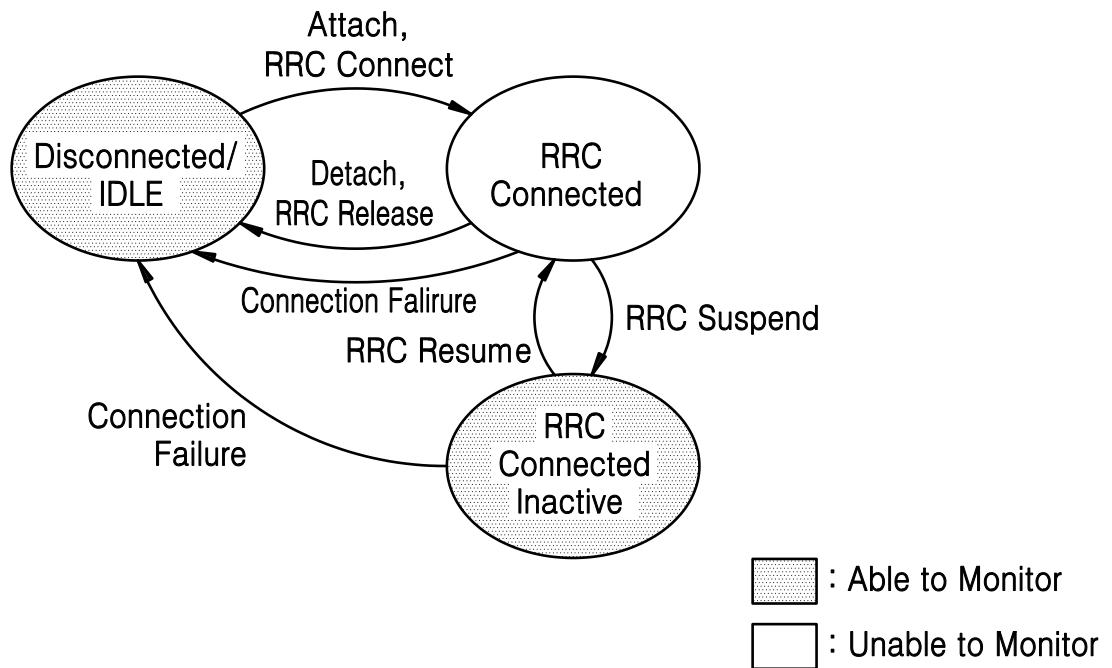


FIG. 11A

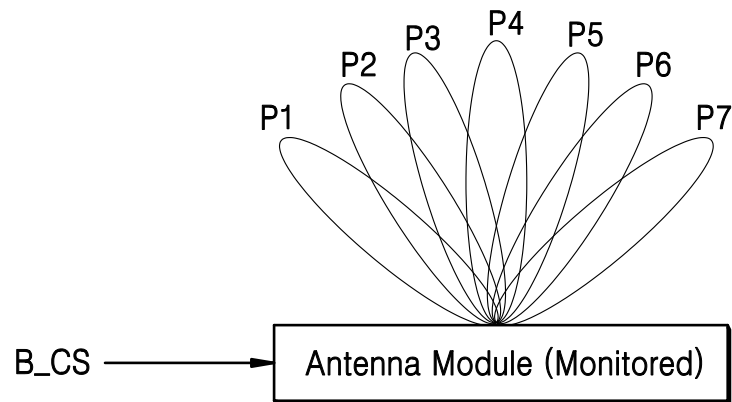


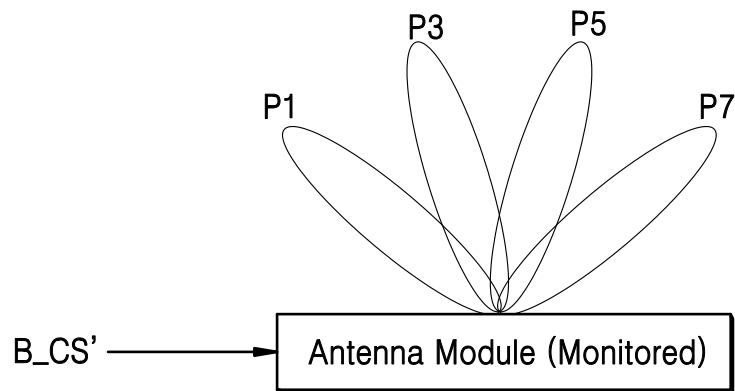
FIG. 11B

FIG. 11C

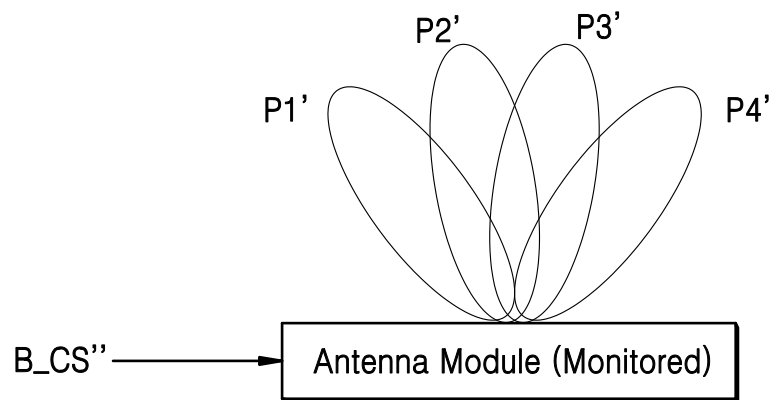


FIG. 12

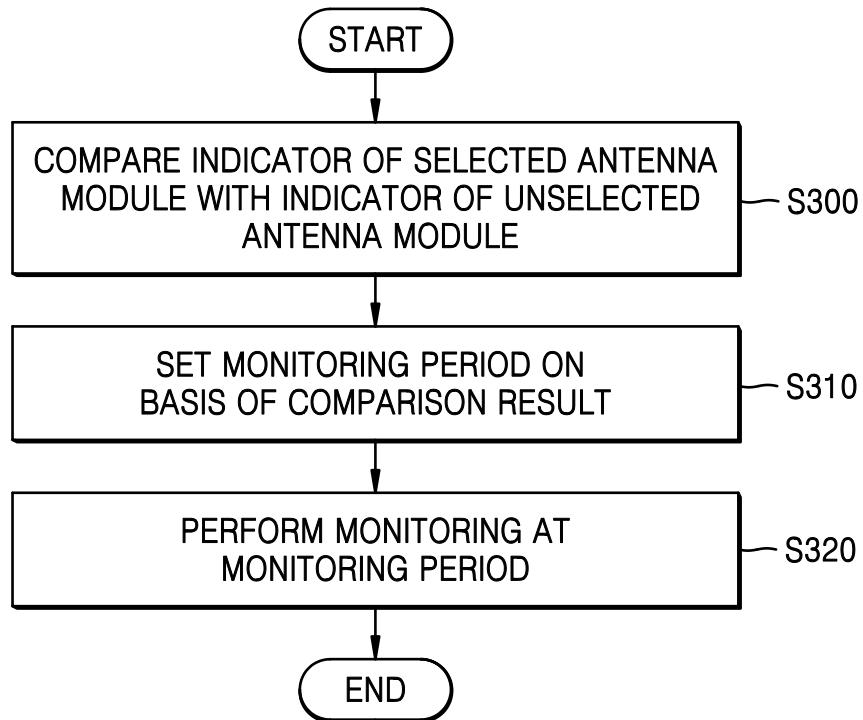


FIG. 13

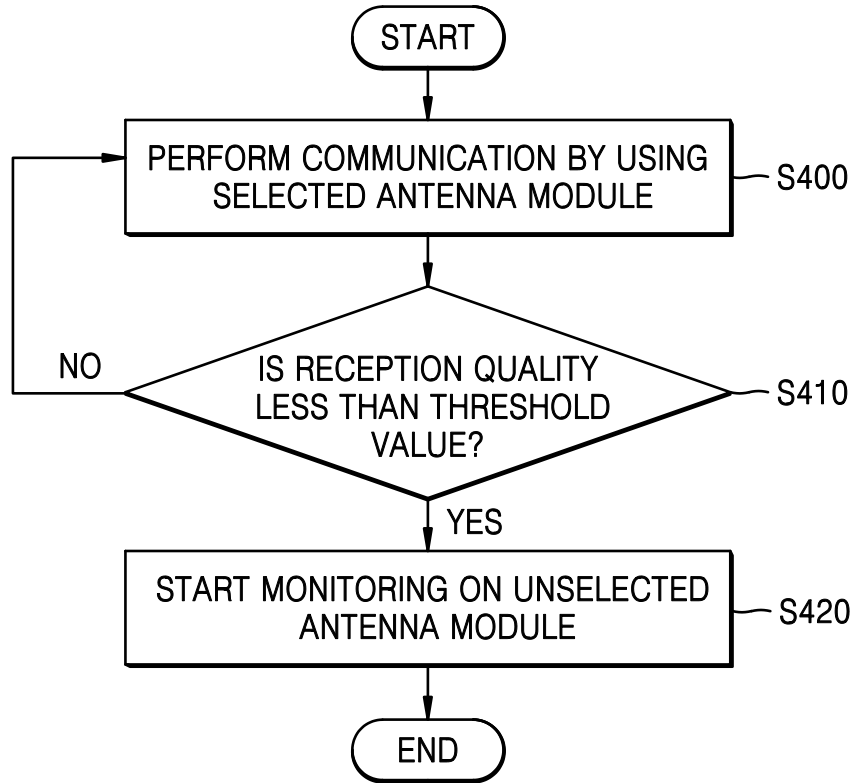


FIG. 14

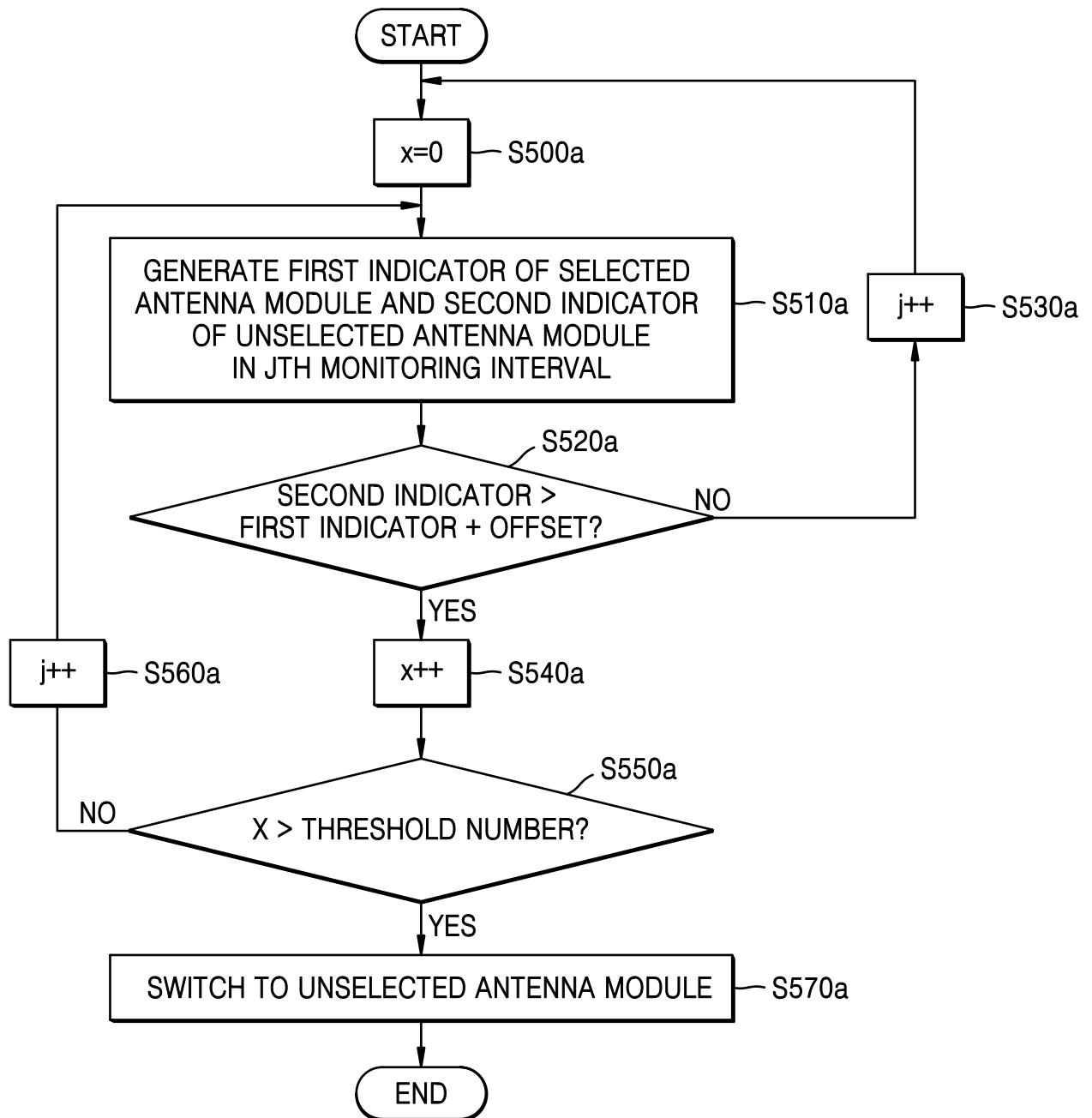


FIG. 15

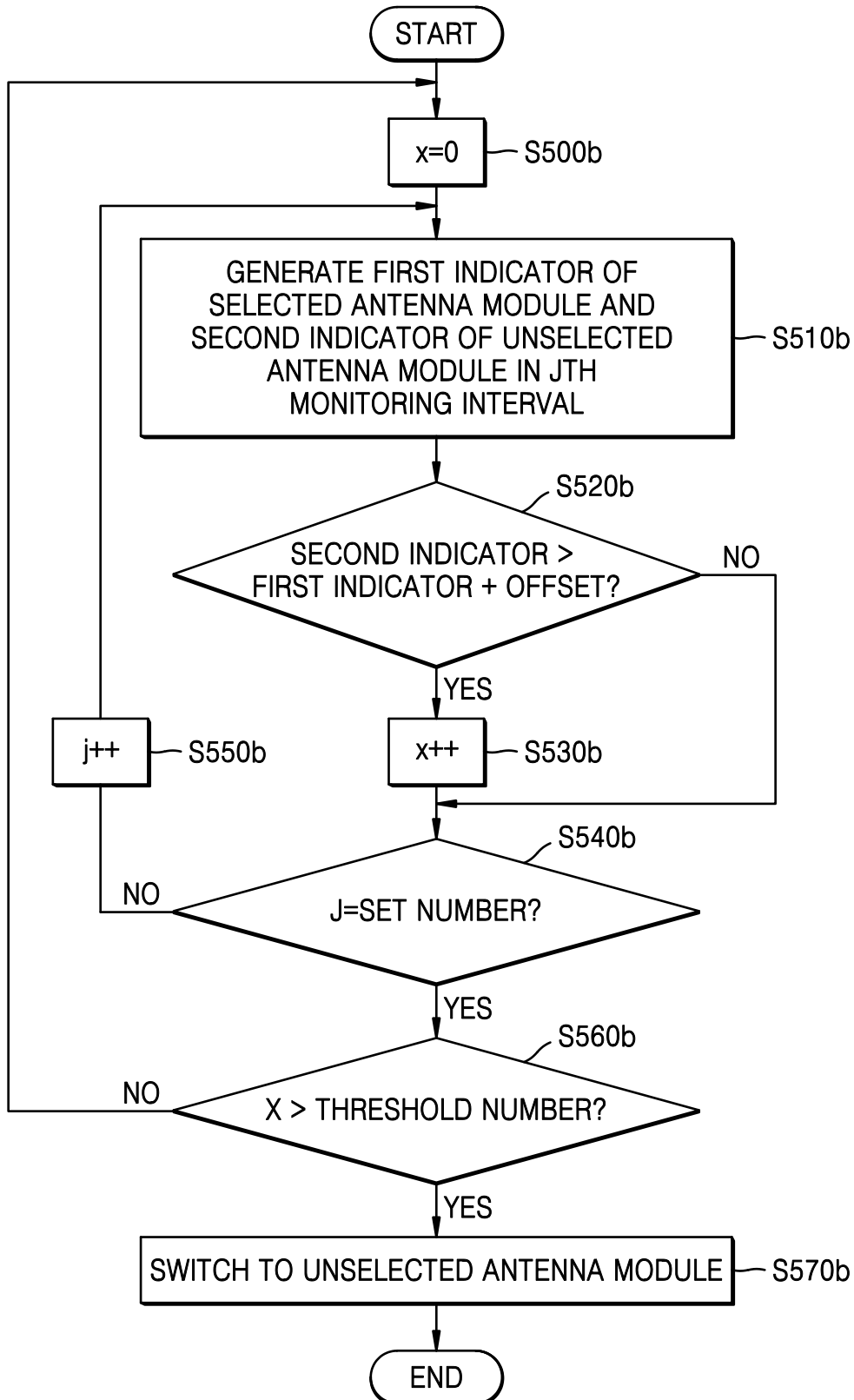


FIG. 16A

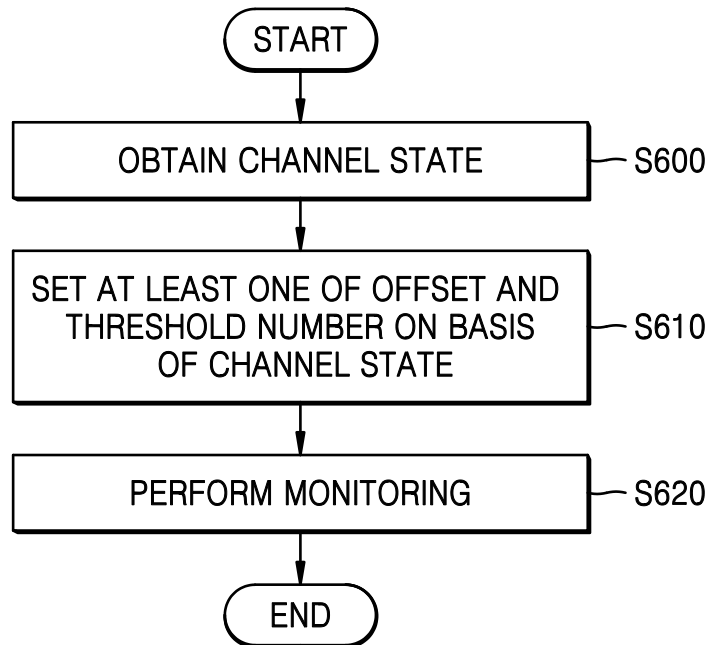


FIG. 16BTB

Channel State	Threshold Number	Offset
ST1	N1	OS1
ST2	N2	OS2
ST3	N3	OS3

FIG. 17

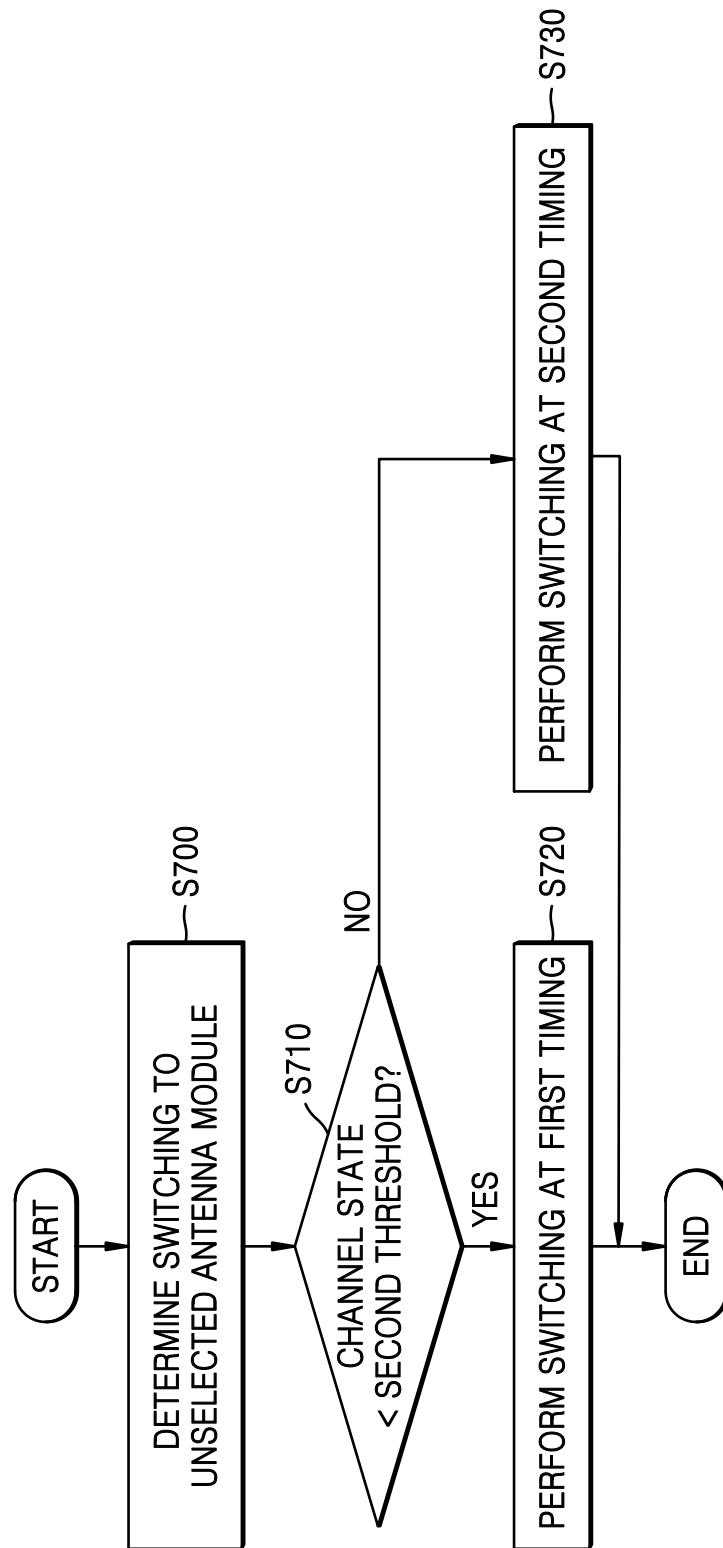


FIG. 18

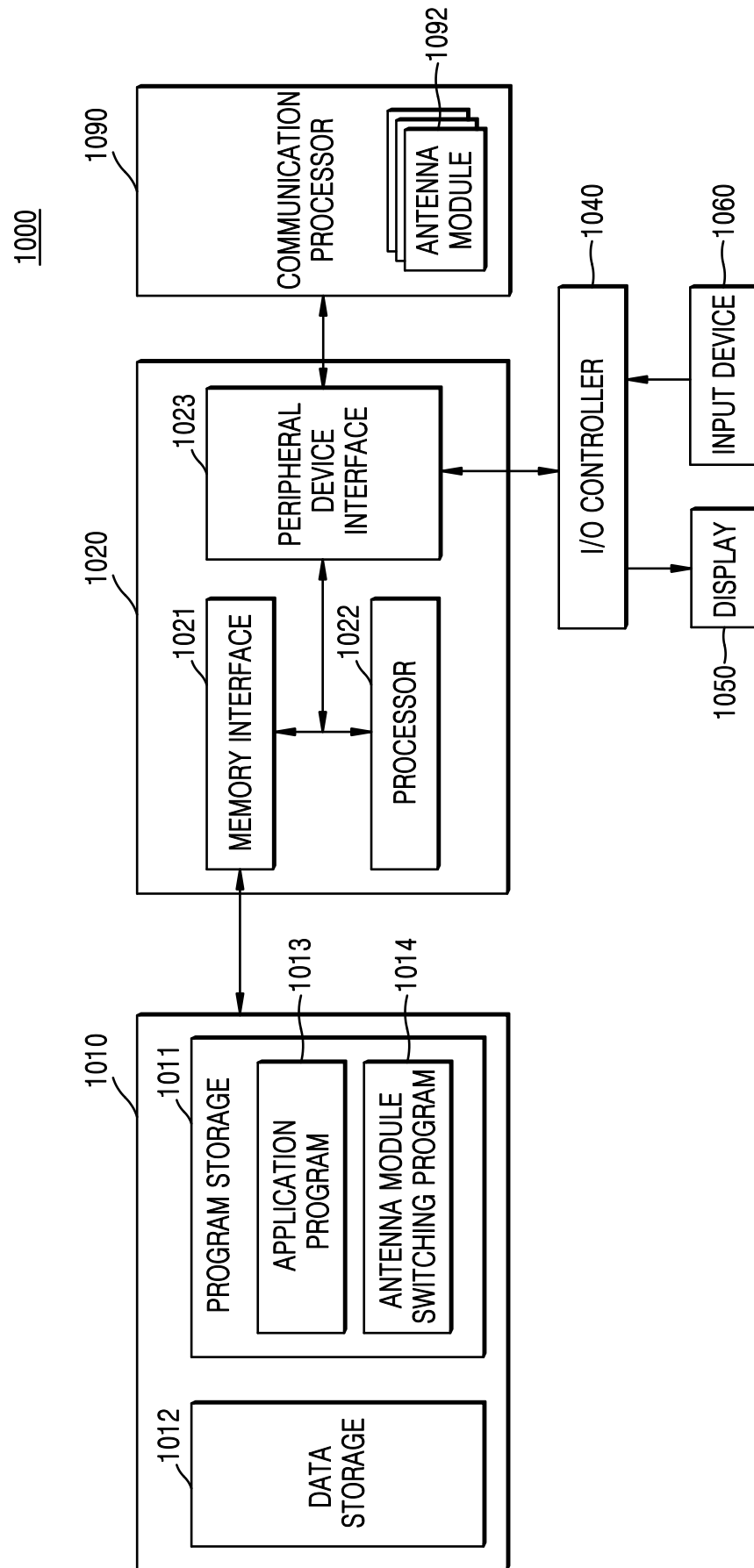


FIG. 19

