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ABSTRACT OF THE DISCLOSURE

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Image Encoding Apparatus

In an image encoding apparatus, conversion data
obtained by converting image information onto a
frequency region, and corresponding to several frames
5 are parallelly output using a one-frame period delay
circuit. The parallelly output conversion data are
quantized by a quantization circuit, and a data amount
to be obtained when the quantized data are encoded is
obtained by a variable-length coding circuit. The data
10 amount information is input to a coefficient operation
circuit for determining a quantization step of the next
stage. Conversion data quantized according to an
output from the coefficient operation circuit are
encoded, and the encoded data are output.

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