

1/6

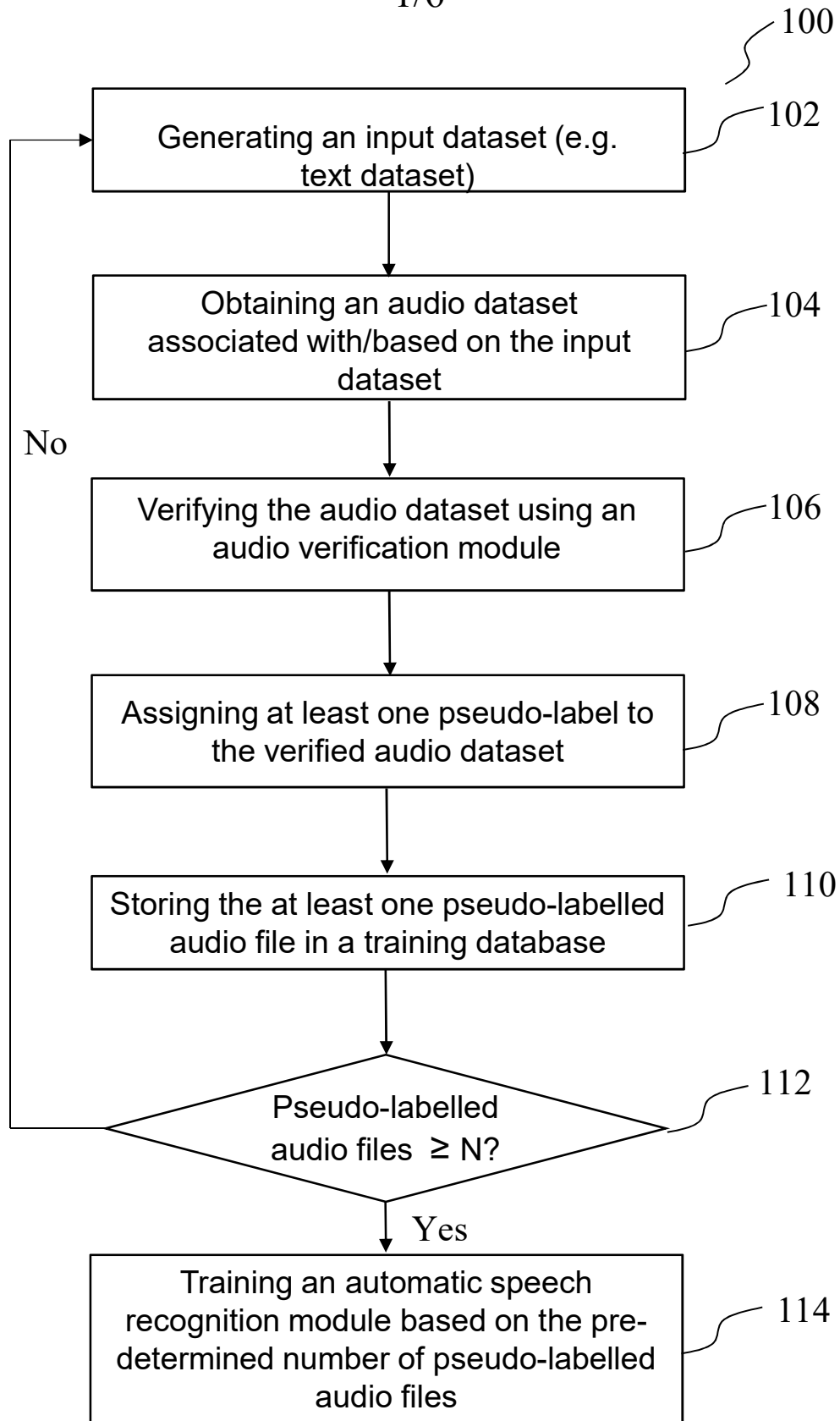
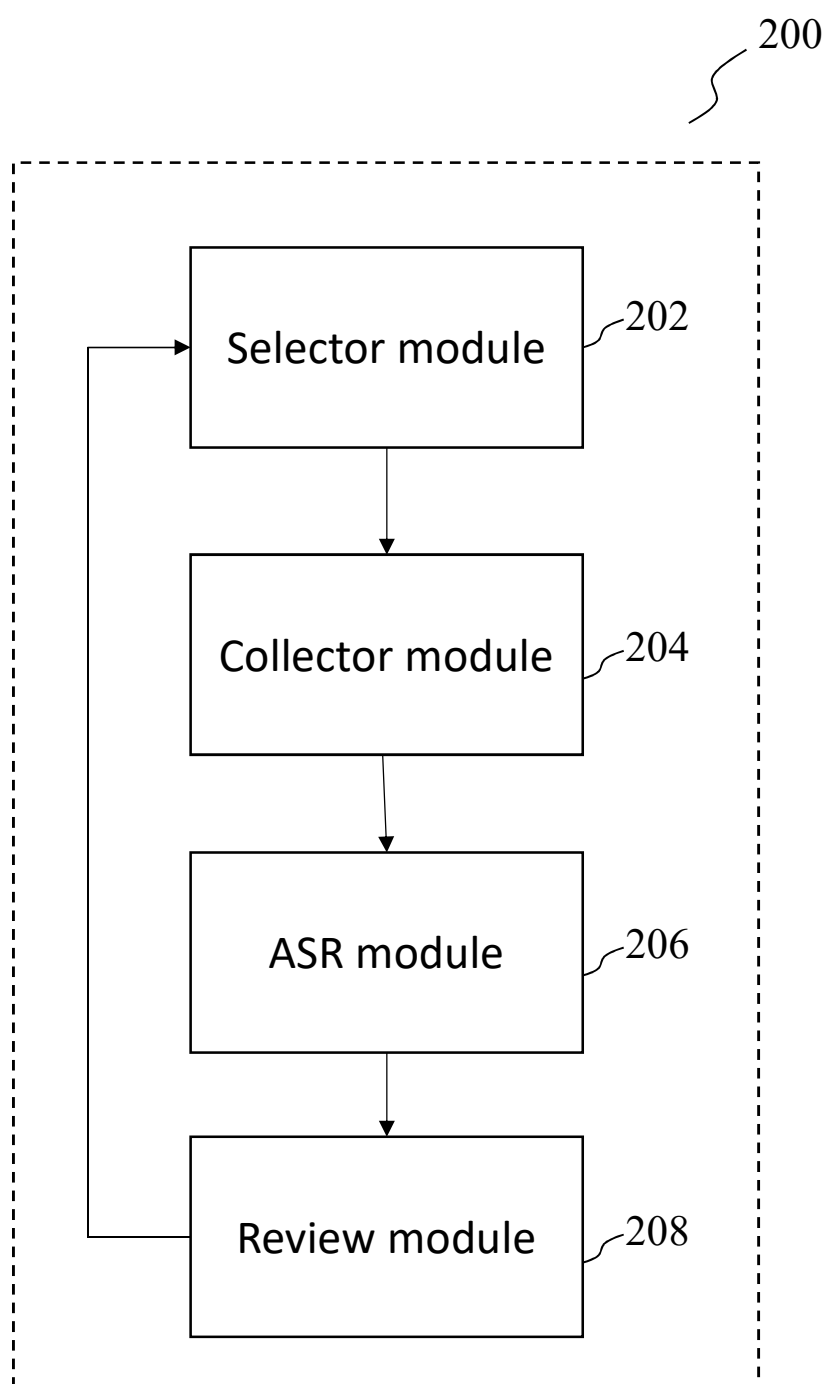


FIG. 1



 Modules may be optionally integrated and implemented as a single processor

FIG. 2



FIG. 3

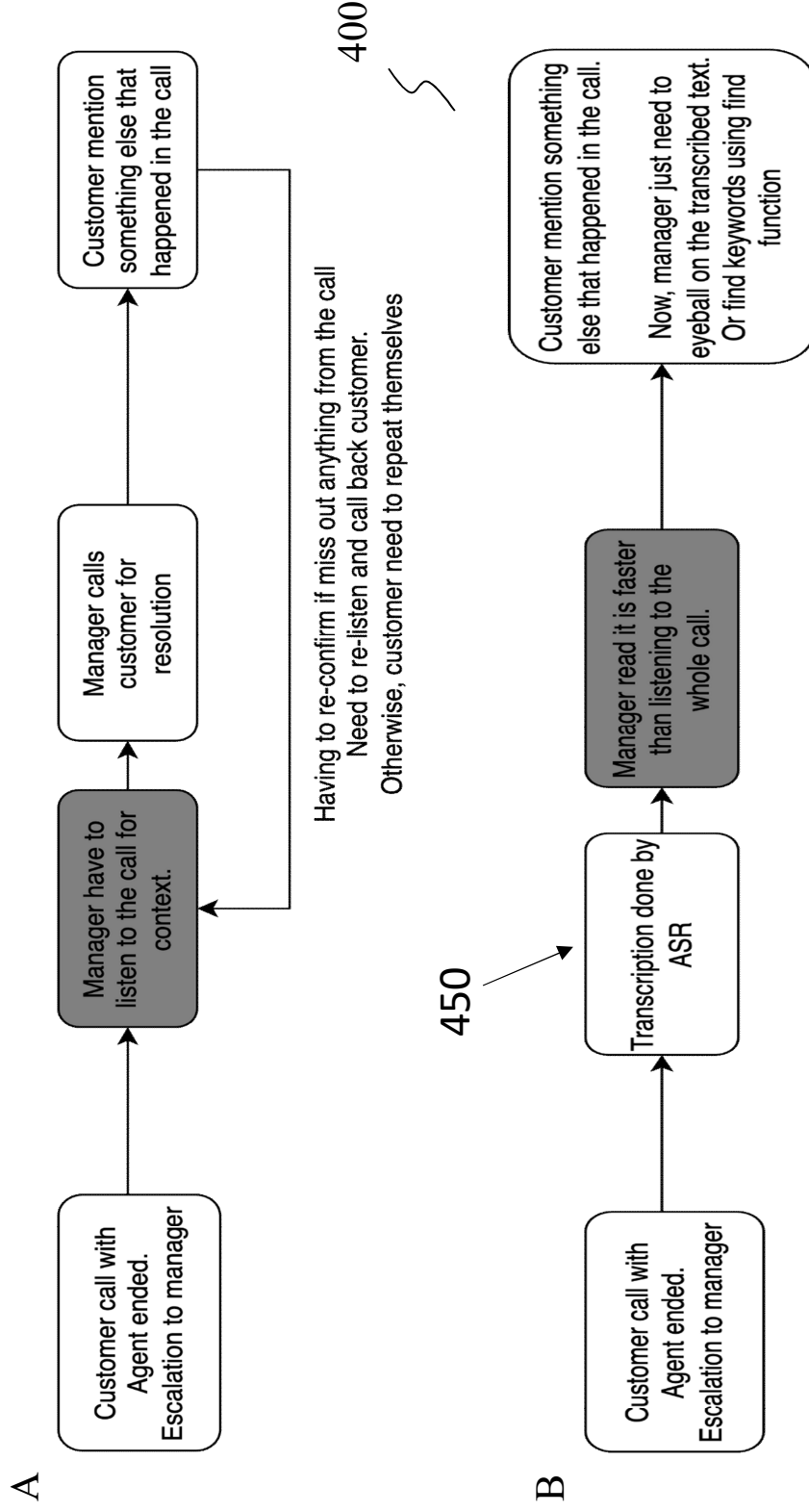


FIG. 4

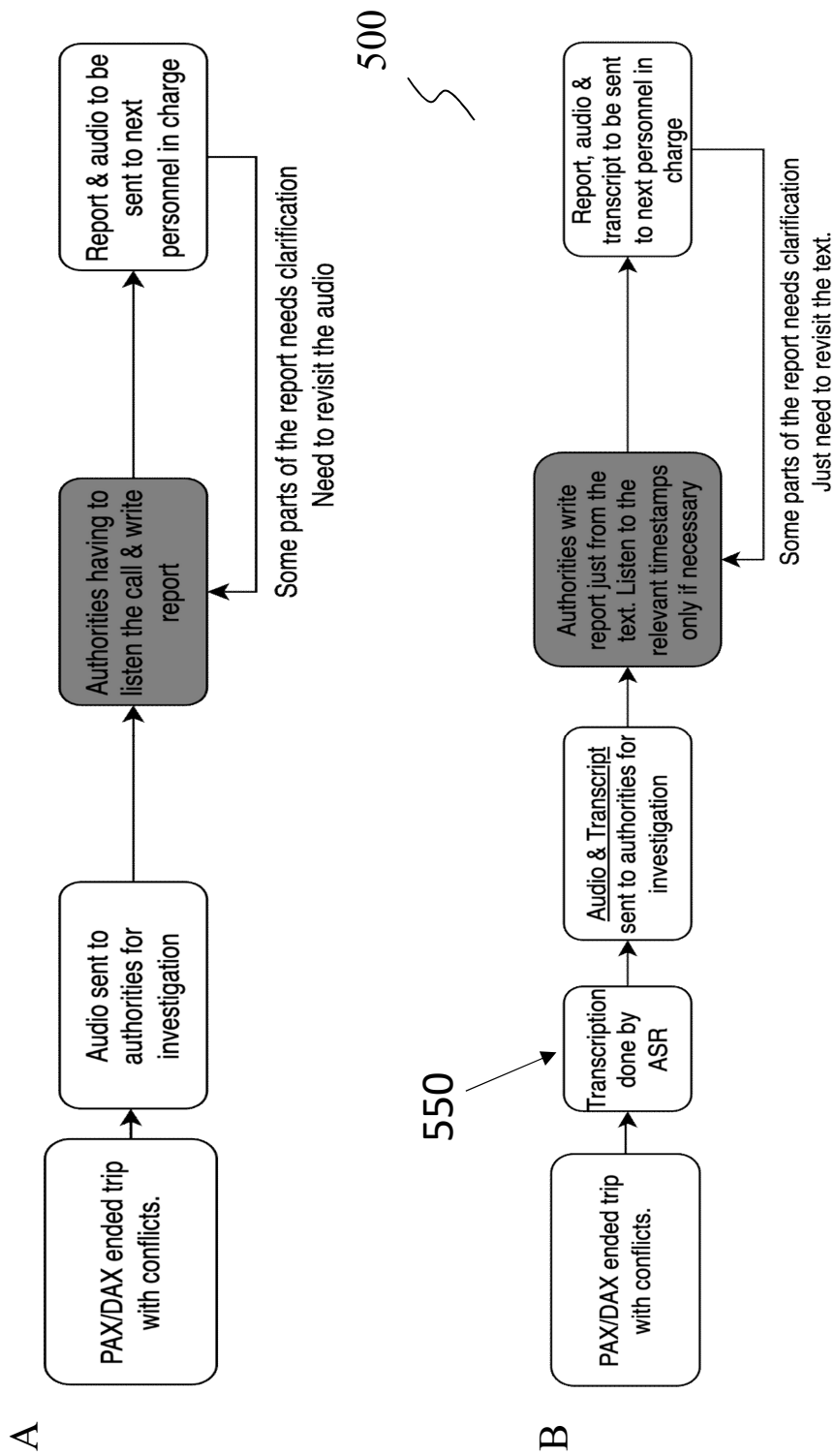


FIG. 5

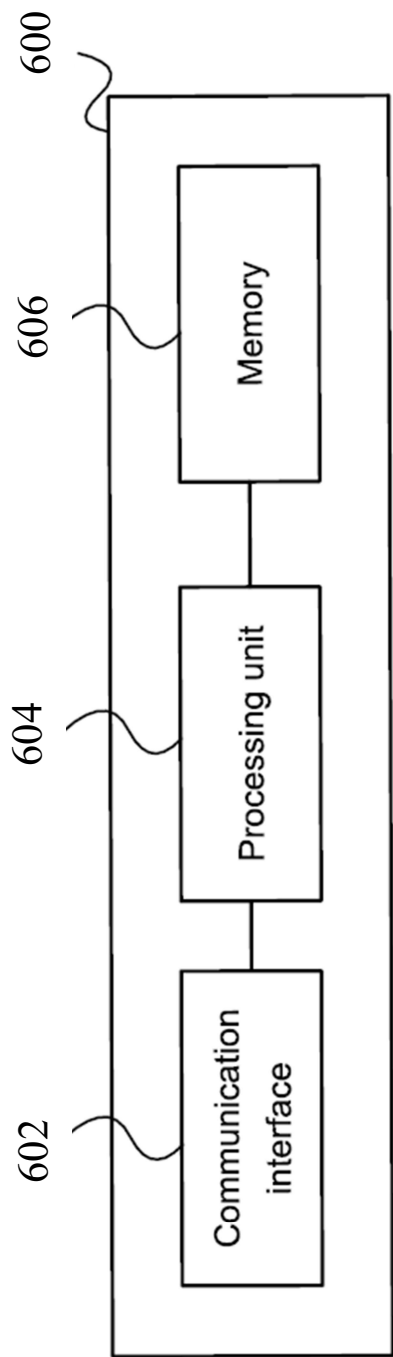


FIG.6