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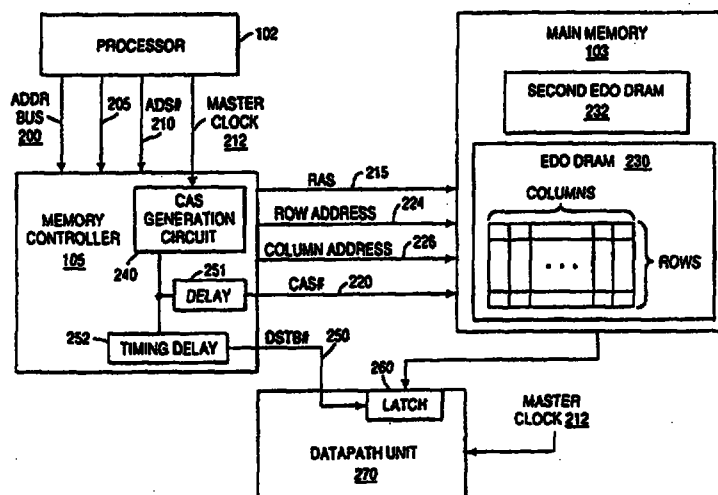
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(54) Title: **AN APPARATUS AND METHOD FOR INCREASING THE BURST RATE OF EDO DRAMS IN A COMPUTER SYSTEM**

(57) Abstract

A memory controller (105) having a data strobe (250) that tracks the column address strobe signal in a computer system having Extended Data Out (EDO) DRAMs (230). The data strobe signal follows, by a predetermined delay, the column access strobe signal, and therefore any skew in the column address strobe signal is inherently included within the data strobe signal. As a result, the data can be latched out, responsive to the data strobe signal, at approximately the center of the valid window.



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