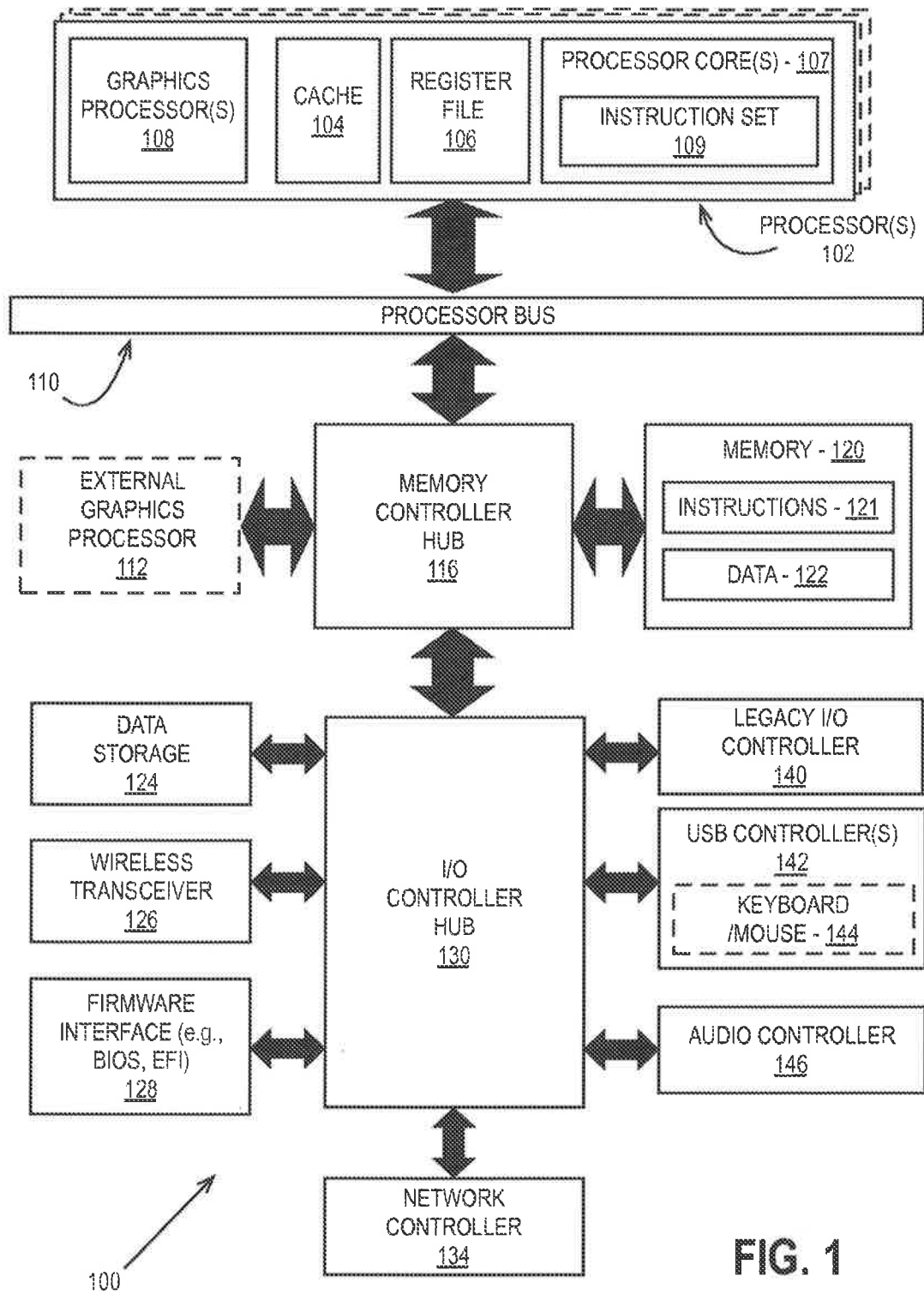


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PROCESSOR 200

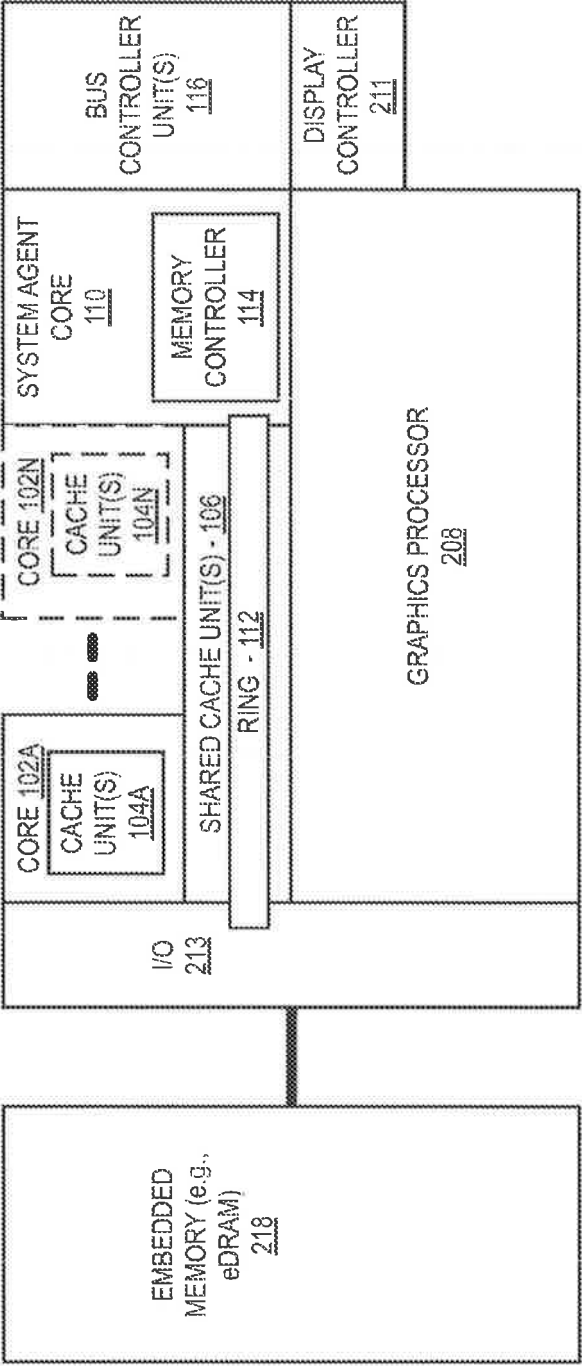


FIG. 2

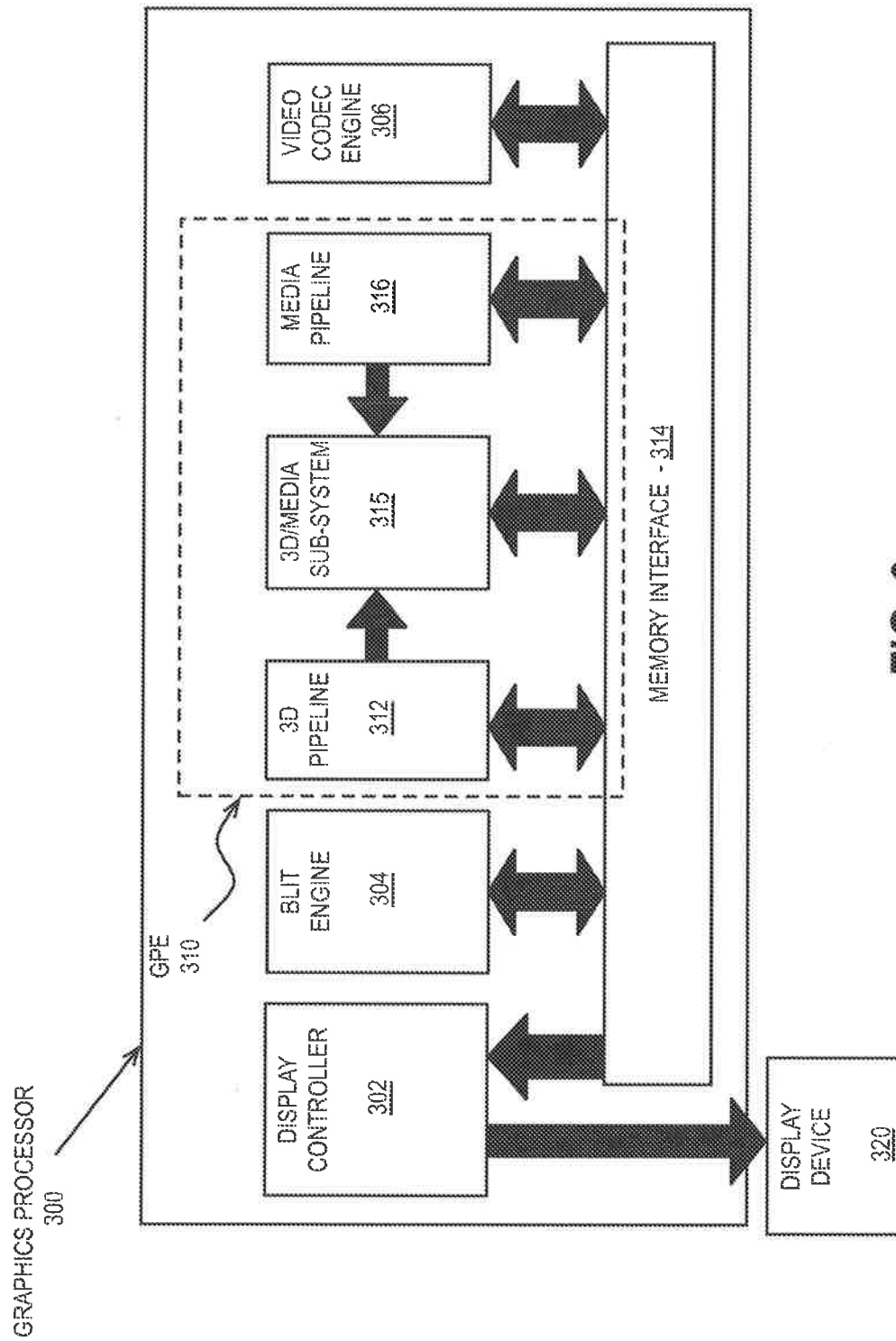


FIG. 3

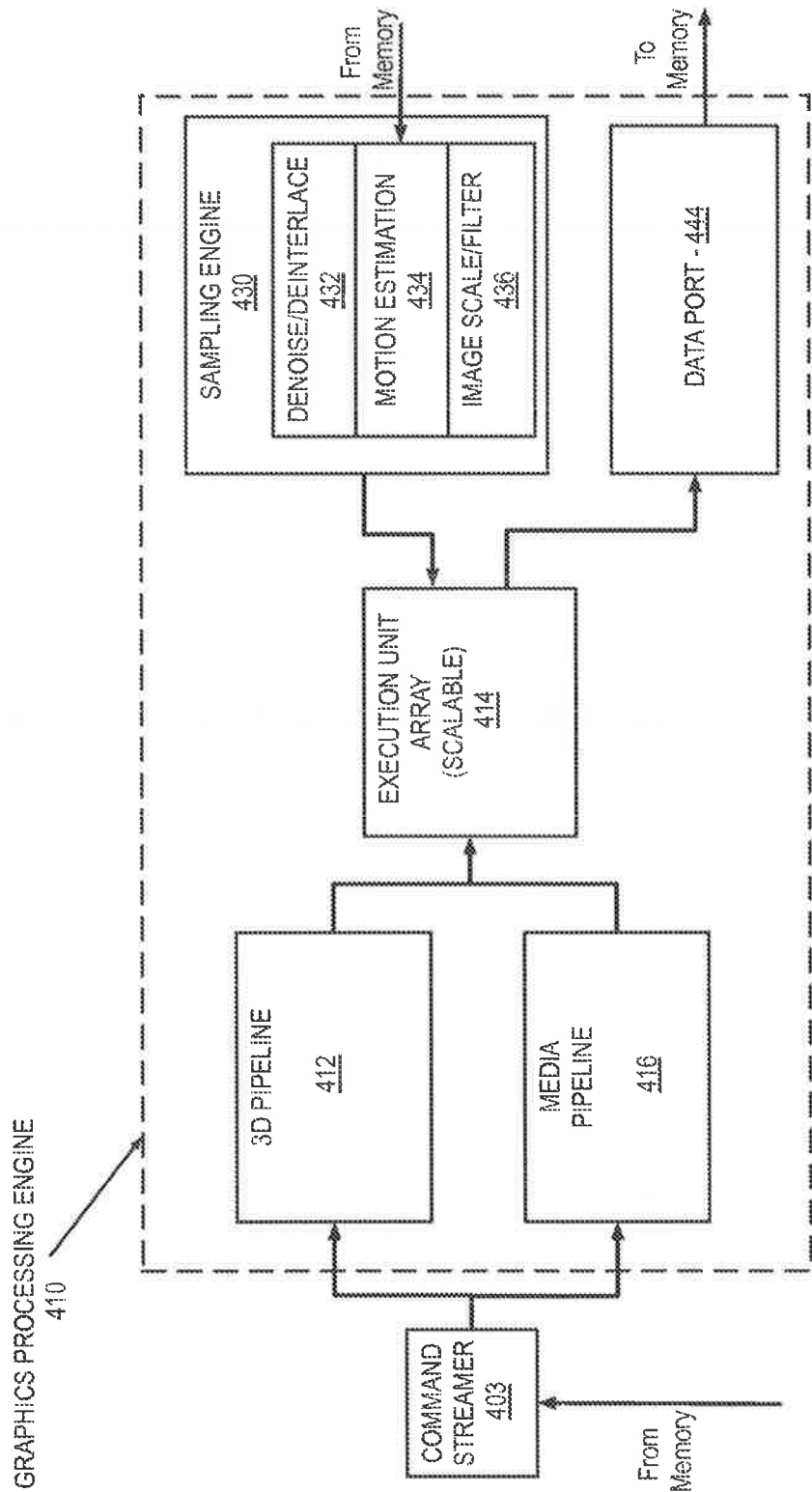


FIG. 4

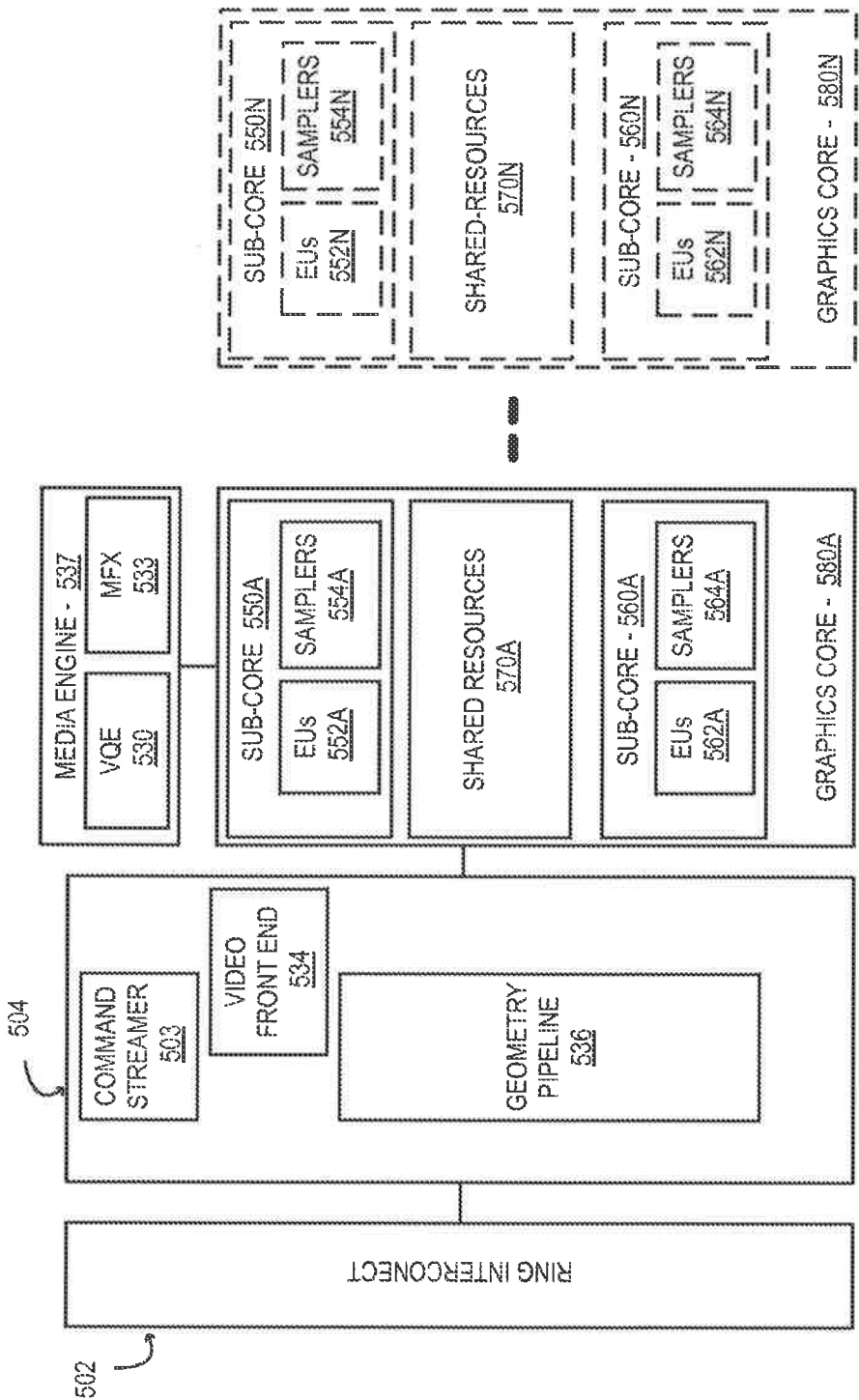


FIG. 5

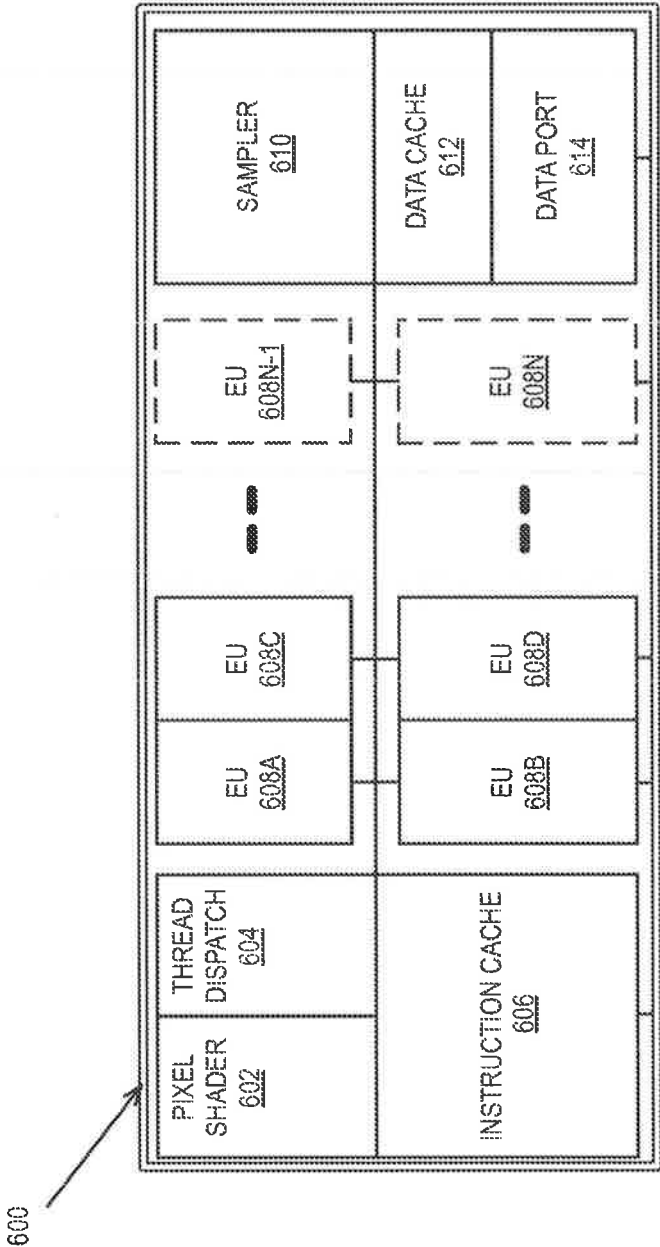
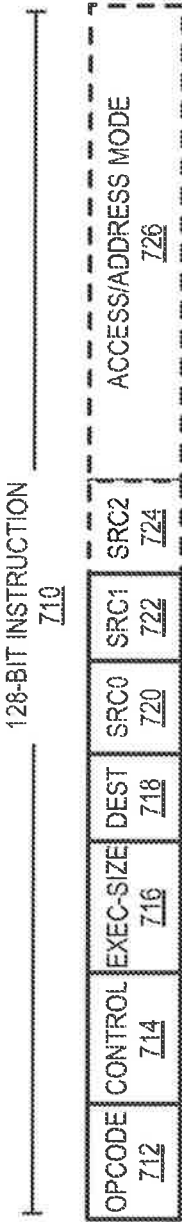


FIG. 6

GRAPHICS CORE INSTRUCTION FORMATS

700



64-BIT COMPACT INSTRUCTION 730



OPCODE DECODE 740

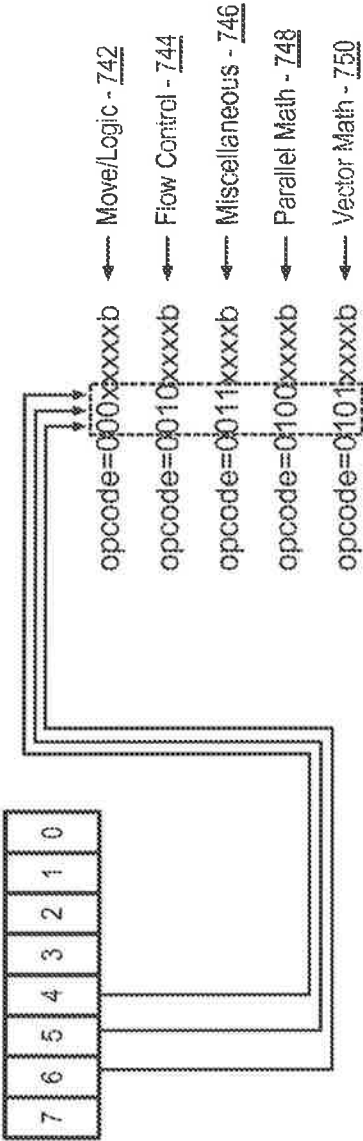


FIG. 7

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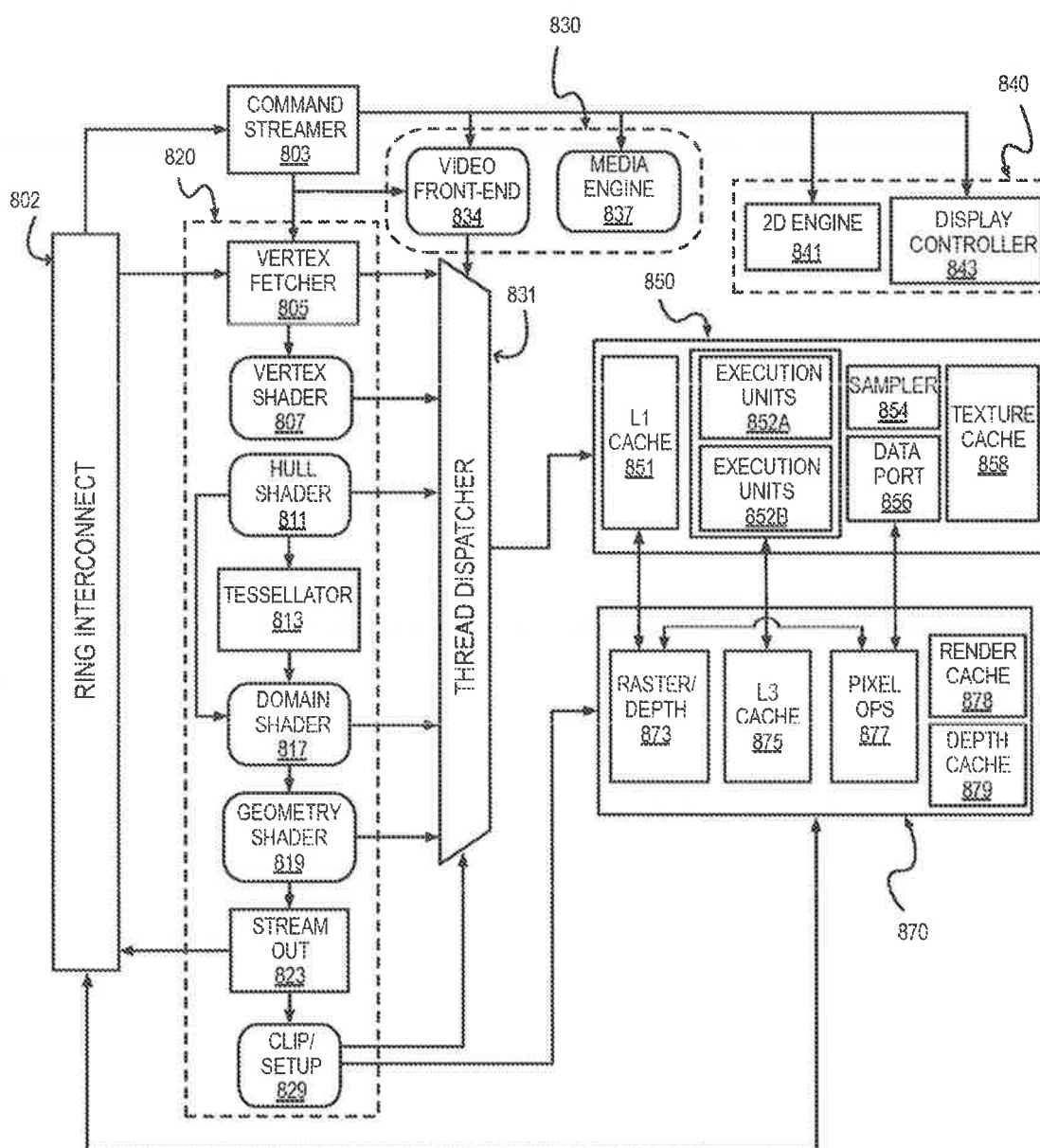


FIG. 8

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FIG. 9A

SAMPLE COMMAND FORMAT

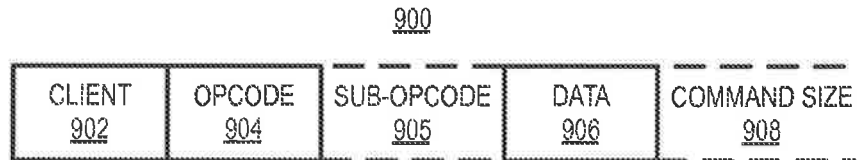
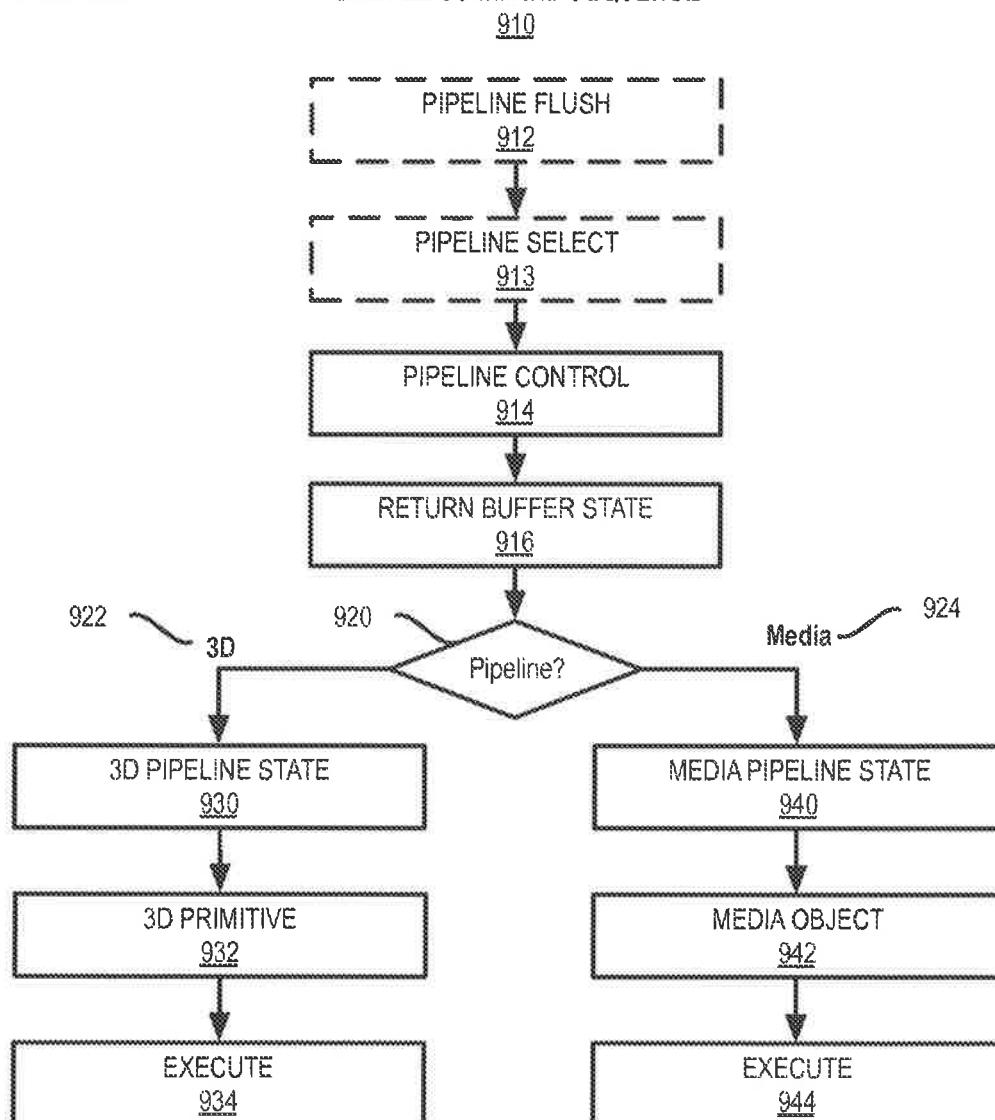


FIG. 9B

SAMPLE COMMAND SEQUENCE



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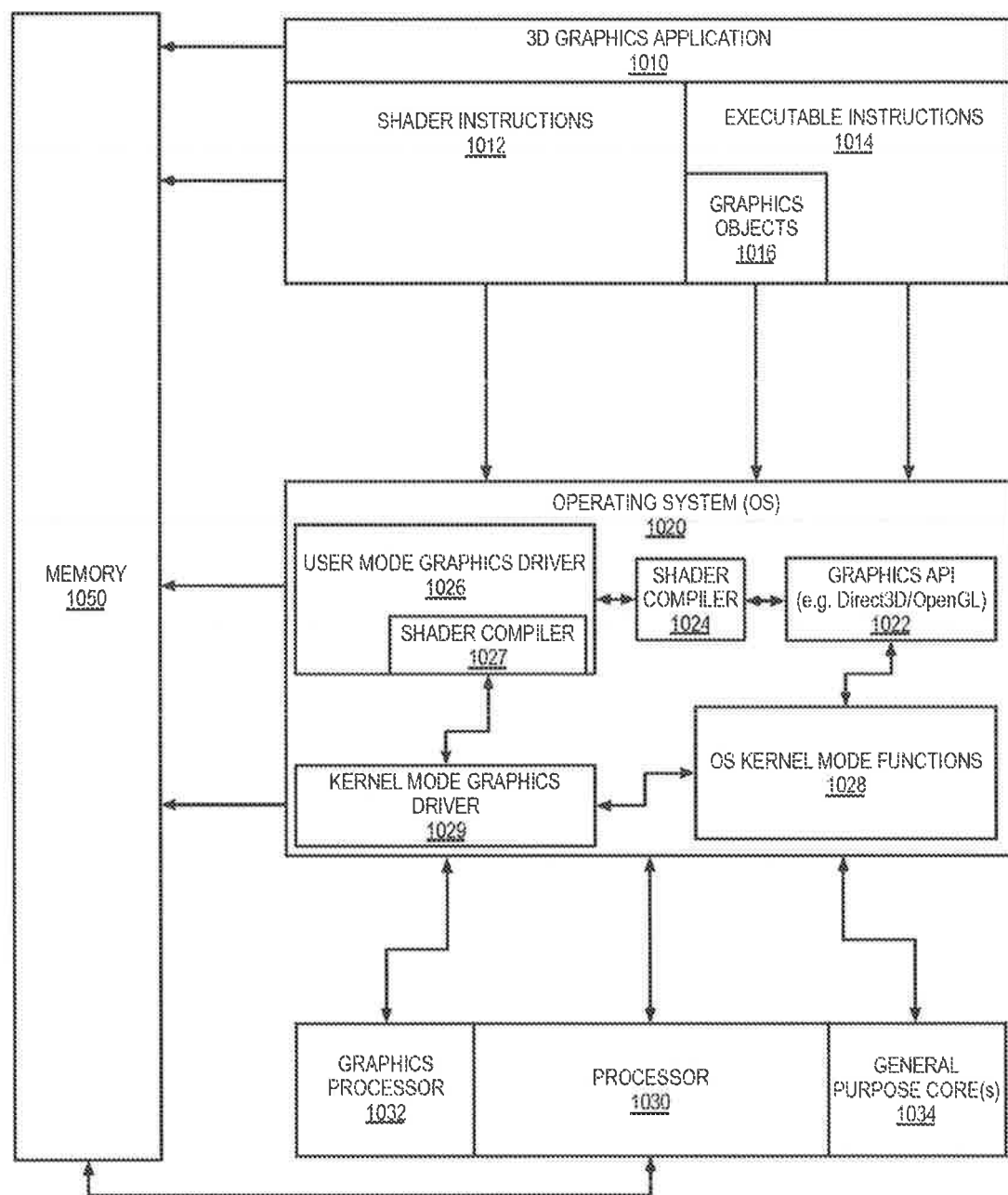


FIG. 10

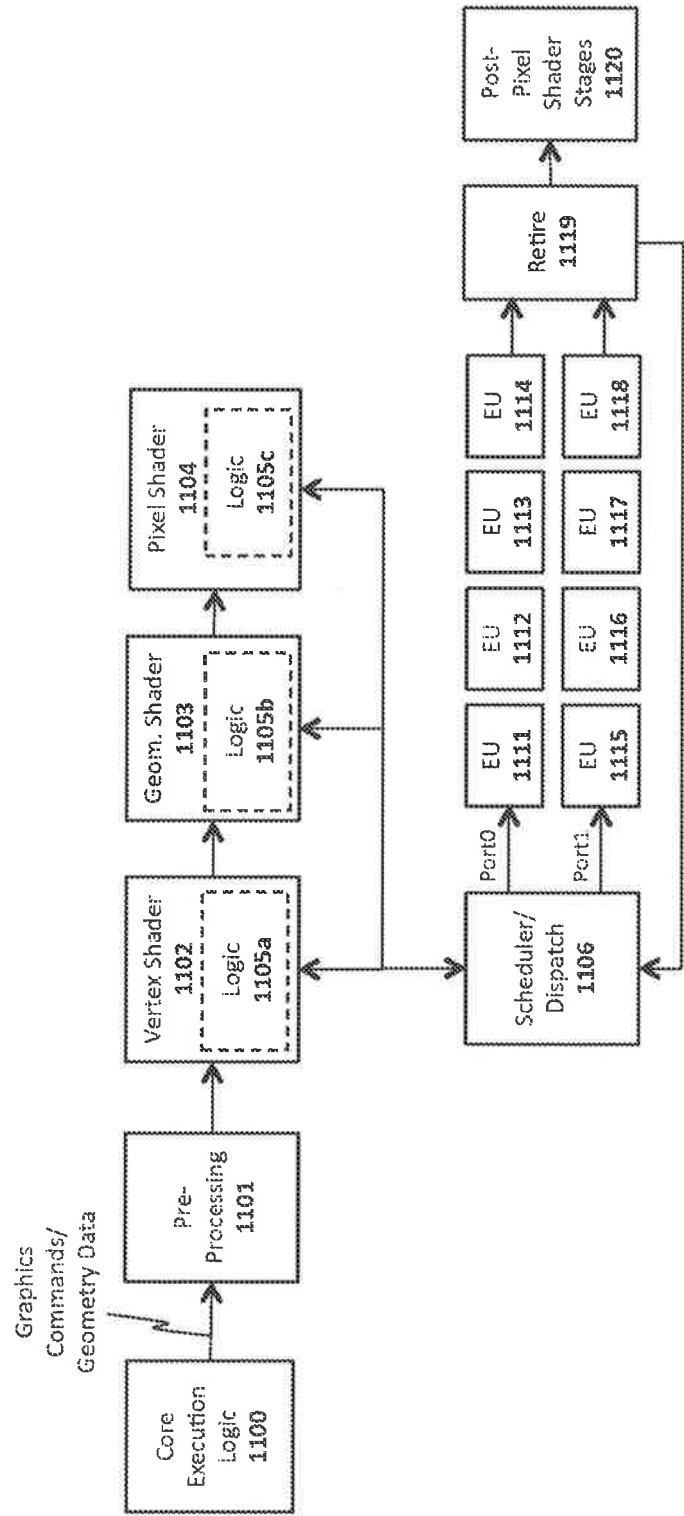
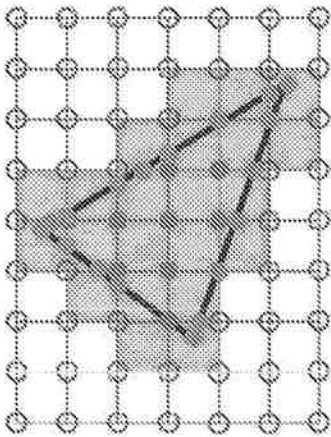
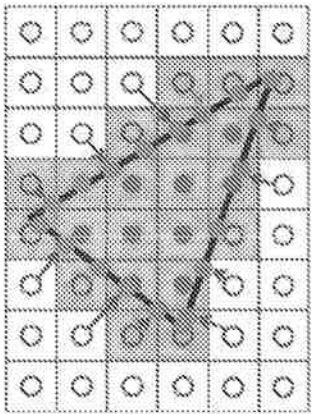


FIG. 11



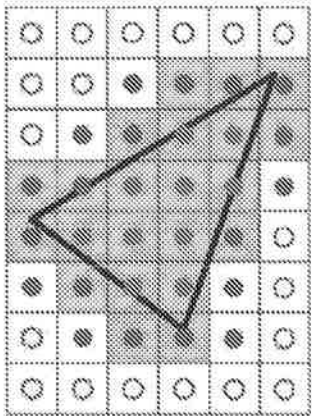
Clipping

FIG. 12c



Snapping

FIG. 12b



Extrapolation

FIG. 12a

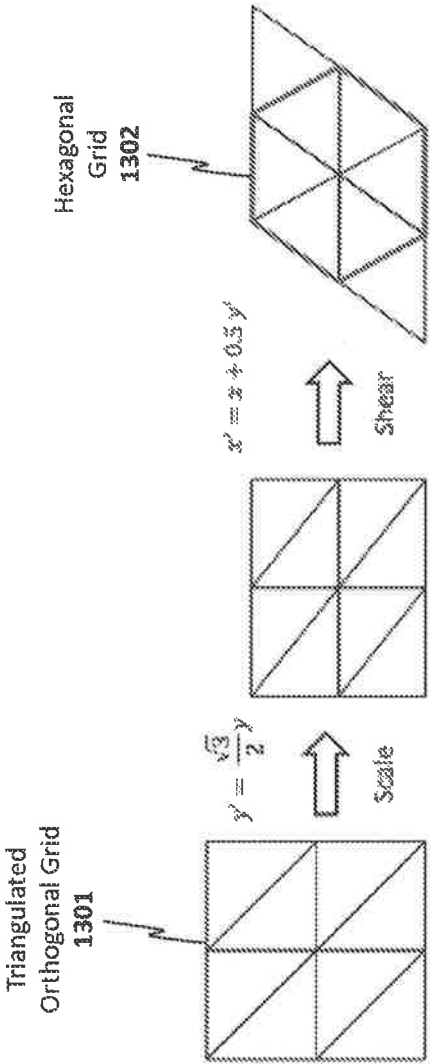


FIG. 13

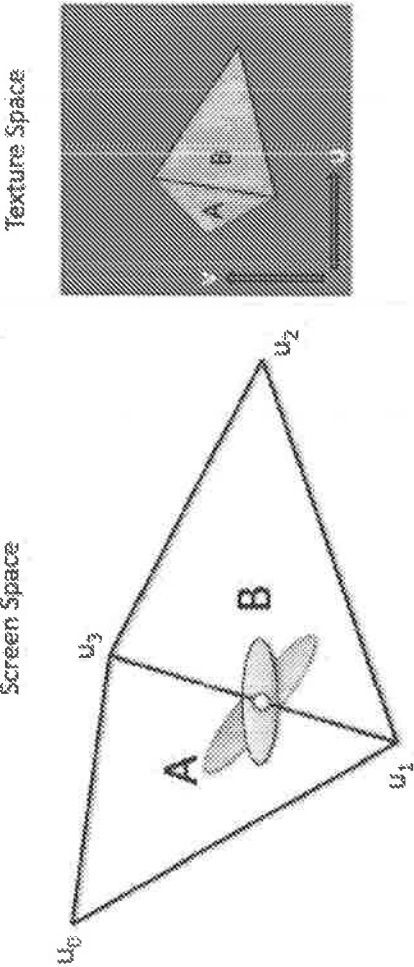


FIG. 14

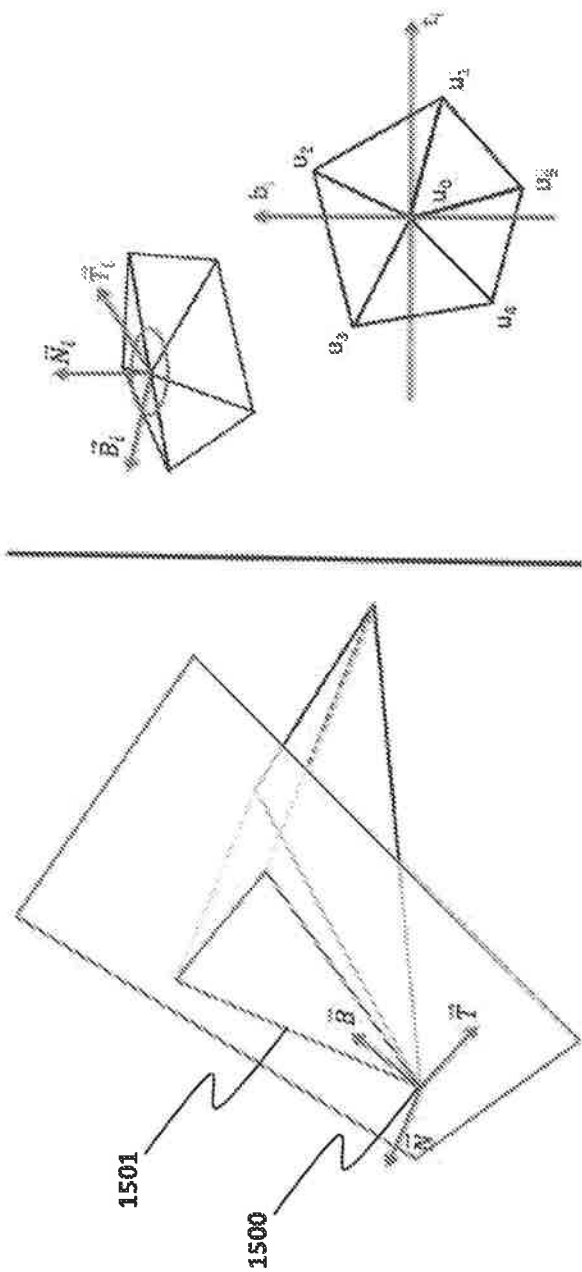


FIG. 15

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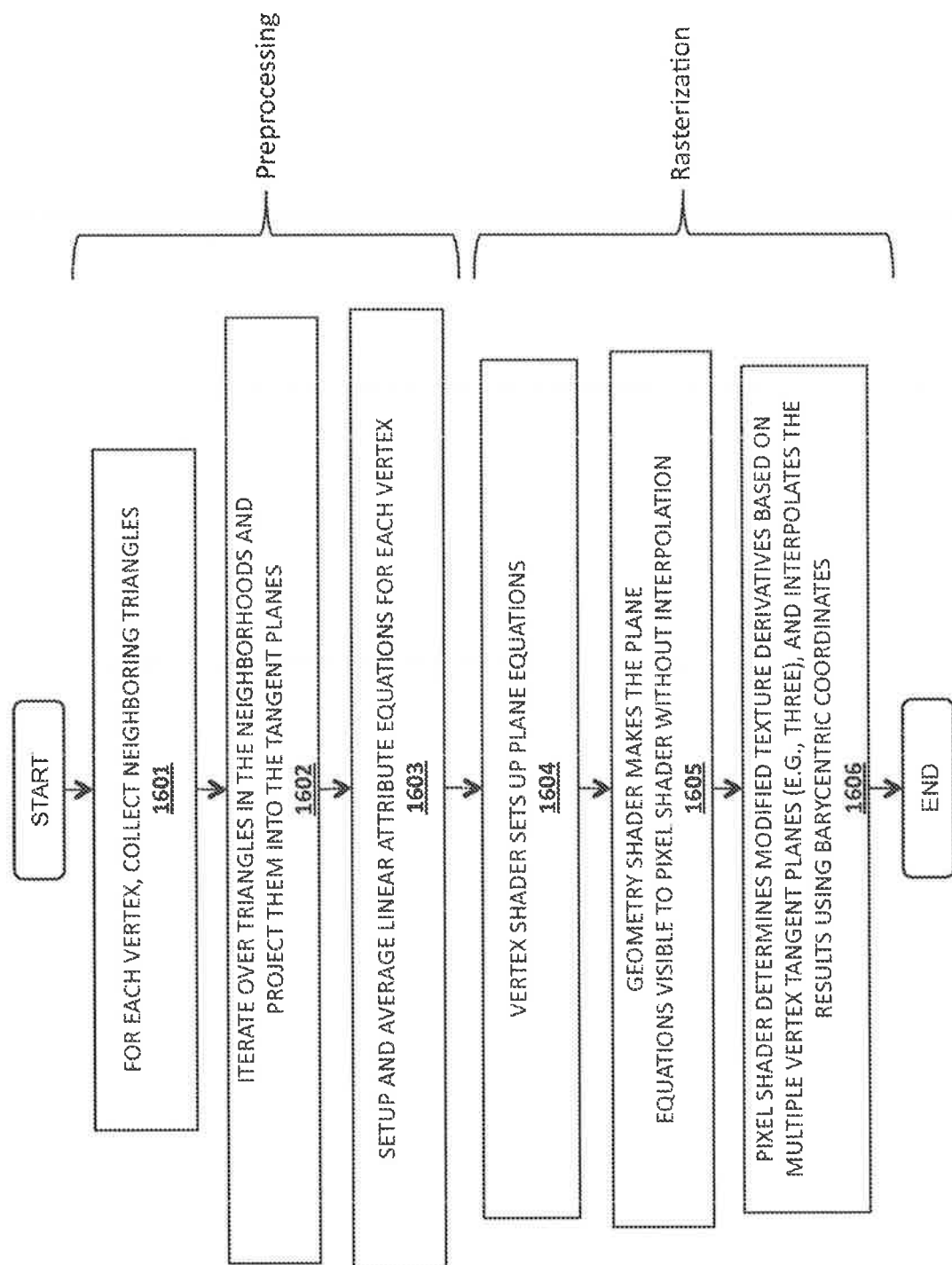


FIG. 16