

Abstract of the Disclosure

A source of switched supply voltage (30) is applied to a potential divider (54,55) and coupled to an output terminal (52) via a capacitor (53). An output tap (57) of the potential divider is coupled to the output terminal via the conduction path of a grounded base transistor (60). In operation the transistor (1) operates in an inverted mode for initially charging the capacitor, (2) operates in either a normal or an inverted mode for regulating a grid bias voltage at said output terminal for grid current flow of either direction relative to the output terminal and (3) operates in an isolation mode upon removal of the switched supply voltage whereby the capacitor applies a negative grid cut-off bias to the output terminal, said transistor in said isolation mode preventing discharge of the capacitor thereby providing a grid cut-off bias at said output terminal having an extended cut-off time constant due to the lack of any discharge path for the capacitor other than leakage.

This Sheet Has Been Replaced

