

ABSTRACT OF THE DISCLOSURE

05 An image defect correcting circuit for a solid state
imager is arranged such that levels of output signals of
pixels of a solid state image pickup element (51R, 51G,
51B) illuminated on its whole surface with a light of
uniform brightness are compared with a predetermined
level, data indicative of a position of a pixel which
10 outputs a signal of peculiar level is generated on the
basis of the compared result, a video signal output from
at least the pixel located at the position indicated by
the position data is interpolated by using output
signals of other pixels according to an interpolation
processing (63) and the output signals of the respective
15 pixels and the video signal generated by the
interpolation processing are selectively output on the
basis of the position data.

Fig 9.

20

25

30

35

This Sheet Has Been Replaced

