

THIS SHEET HAS BEEN
REPLACED

A CORING AND PEAKING CIRCUIT

Abstract of the Disclosure

A coring circuit for an input signal in the form of a differential input current having two components in phase opposition. A first pair of cascode transistors biased by a first reference voltage applies a first fraction of each of the components of the differential current to respectively first and second resistors. A second pair of cascode transistors biased by the first reference voltage provides a second fraction of each of the components of the differential current to a differential current output of the coring circuit and respectively to first and second branches of a differential stage that also receives voltages across the first and second resistors. Two coring current sources respectively connect first and second outputs of the differential stage to a supply voltage and are connected to each other through a third resistor.

Figure 2

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