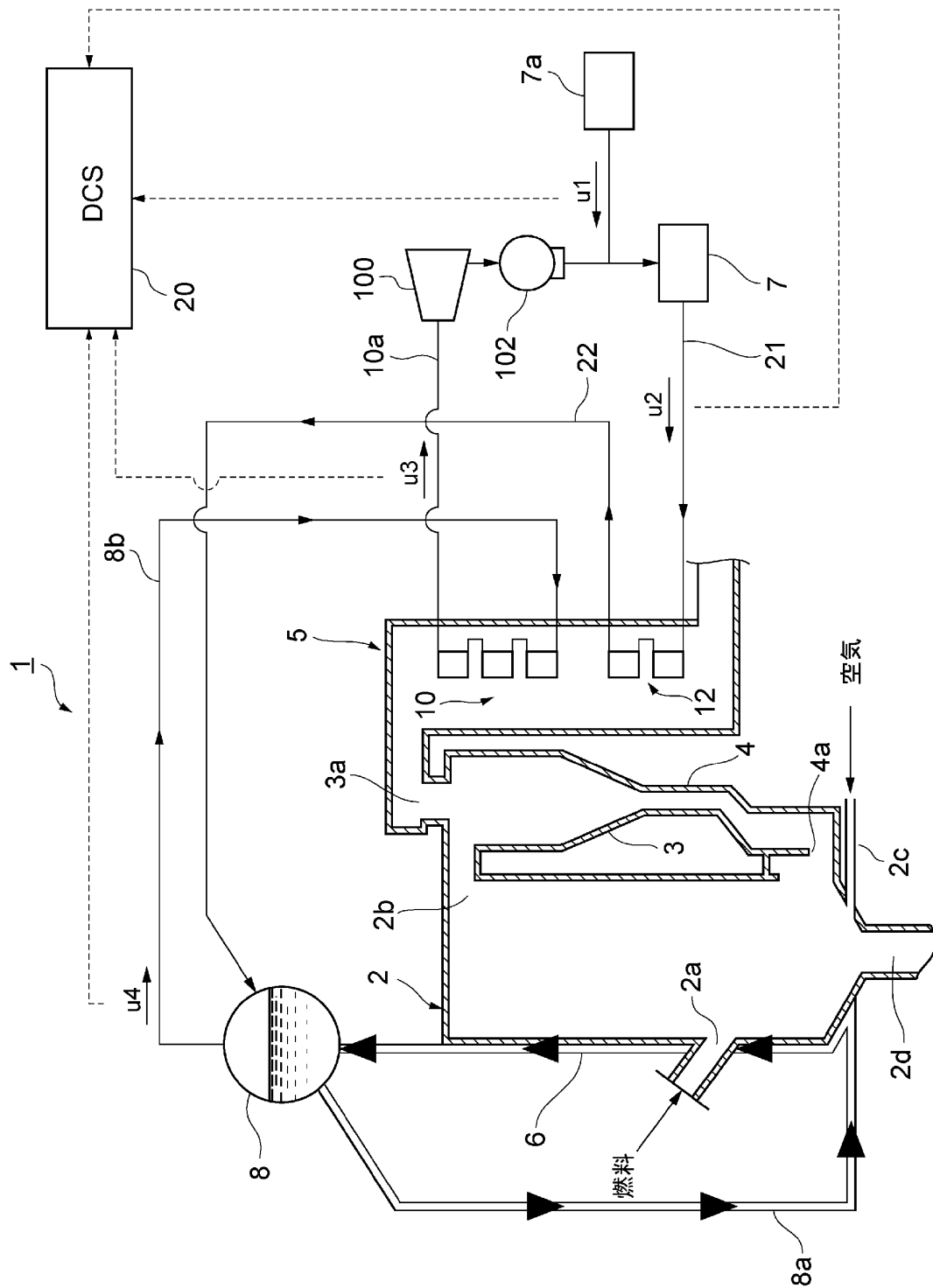
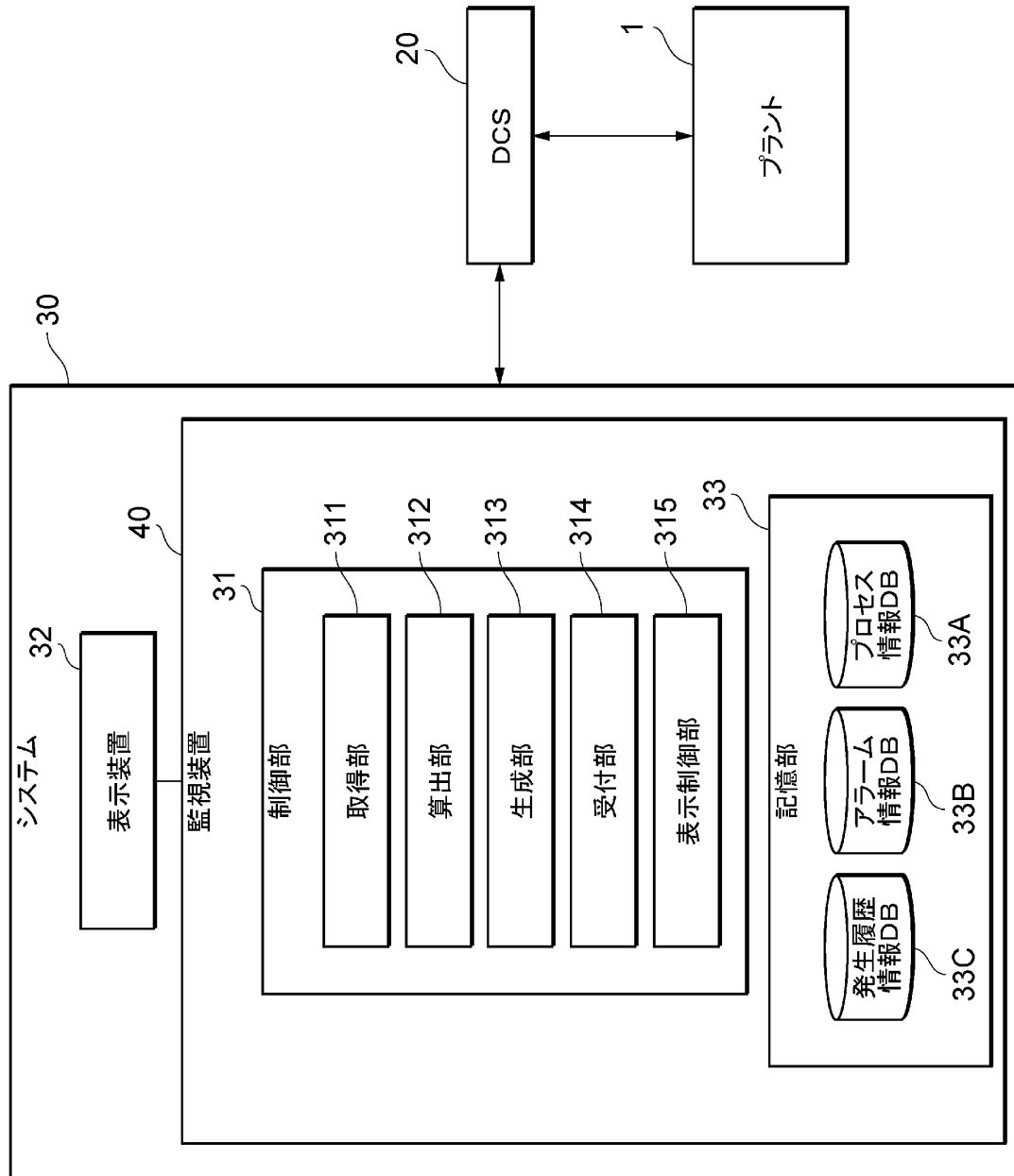


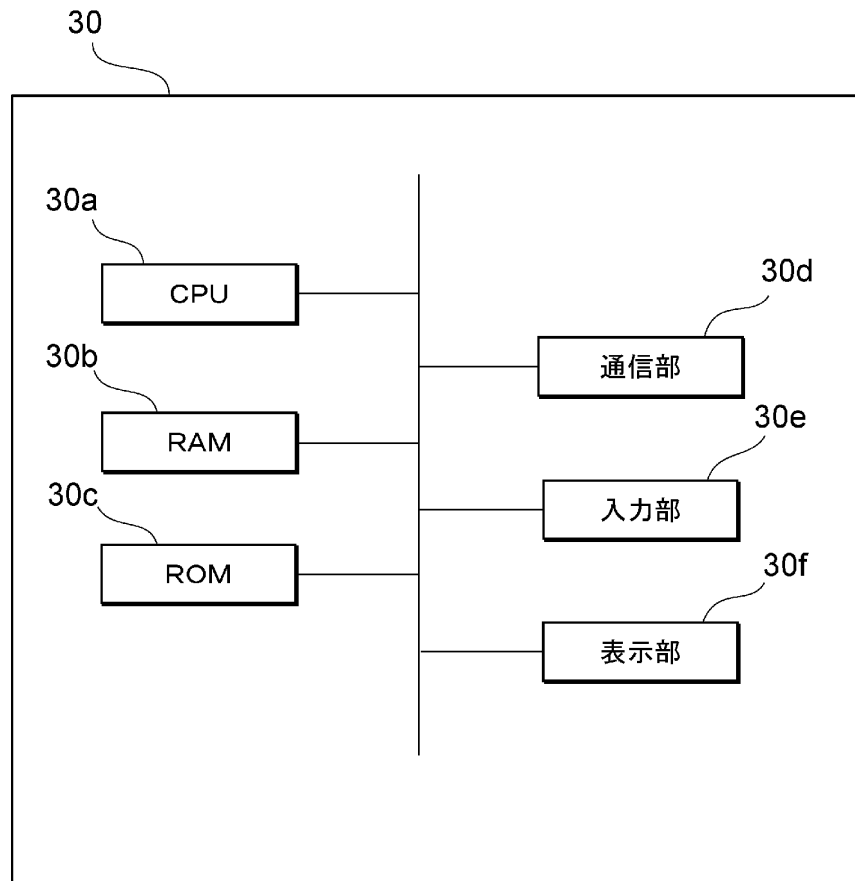
[図1]



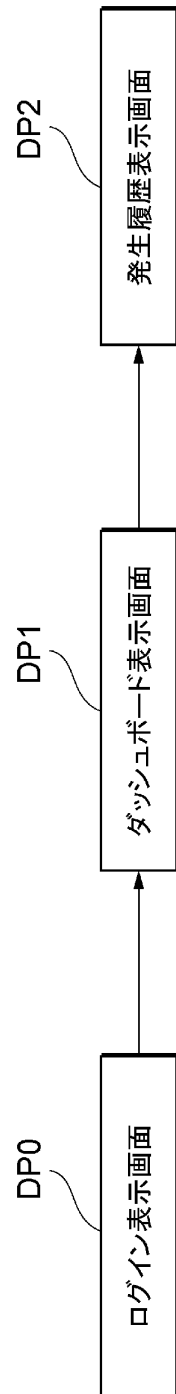
[図2]



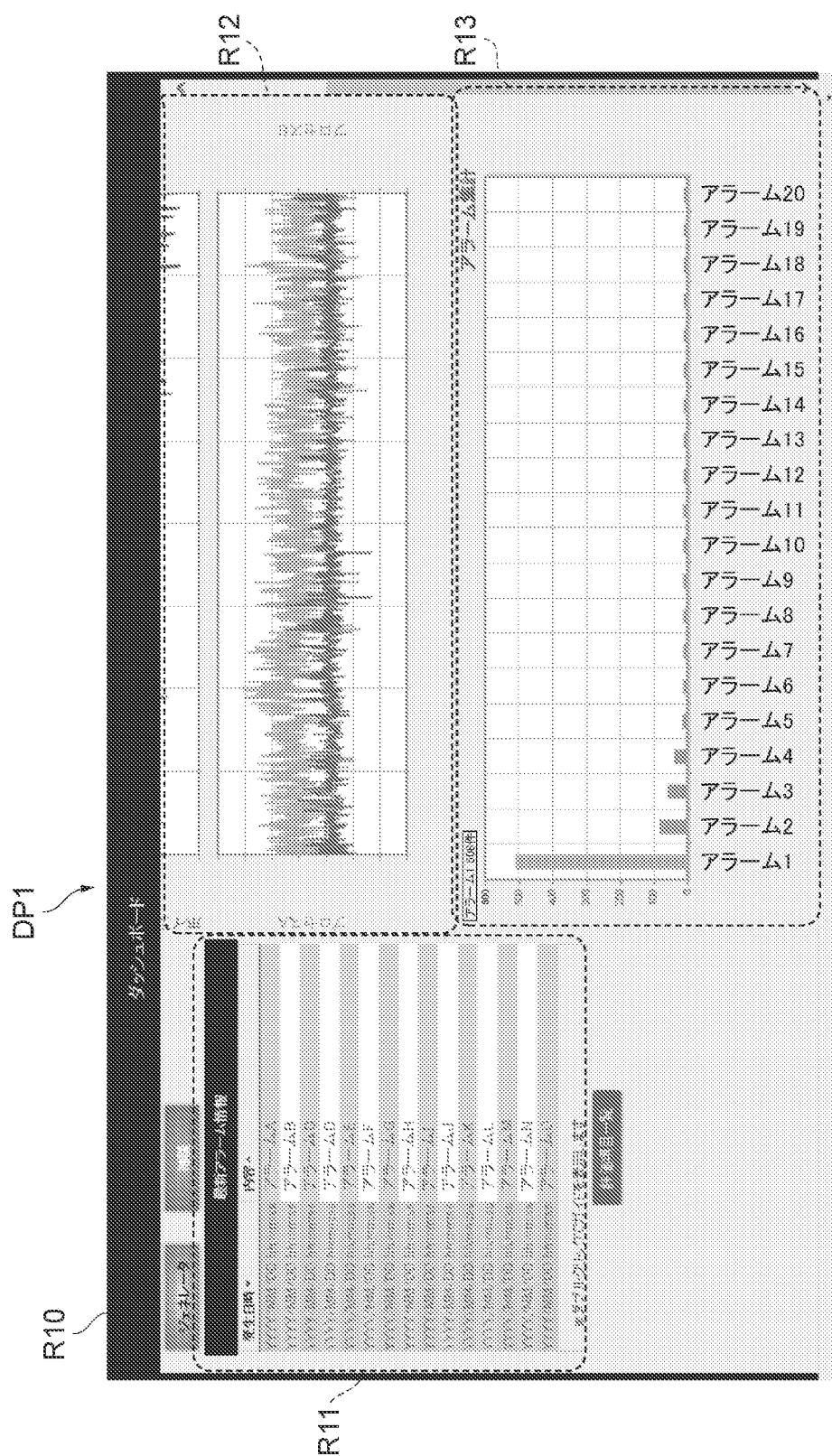
[図3]



[図4]



【図5】



[図6]

Figure 1 is a schematic diagram of a data processing system. The system is divided into three main sections: R20, R21, and R22.

- R20:** This section contains the CPU (Central Processing Unit) and the RAM (Random Access Memory). The CPU is connected to the RAM. The CPU is also connected to a display (DP2) and a keyboard (KB).
- R21:** This section contains the HDD (Hard Disk Drive) and the DB (Database). The HDD is connected to the DB. The HDD is also connected to the CPU.
- R22:** This section contains the Network (LAN) and the DB (Database). The Network is connected to the DB. The Network is also connected to the CPU.

The diagram illustrates the flow of data between these components. The CPU processes data from the keyboard and sends it to the display. It also sends data to the HDD, which stores it in the DB. The Network can also access the DB. The diagram is labeled with various components and their connections, including 'CPU', 'RAM', 'HDD', 'DB', 'LAN', 'DP2', and 'KB'.

[図7]

