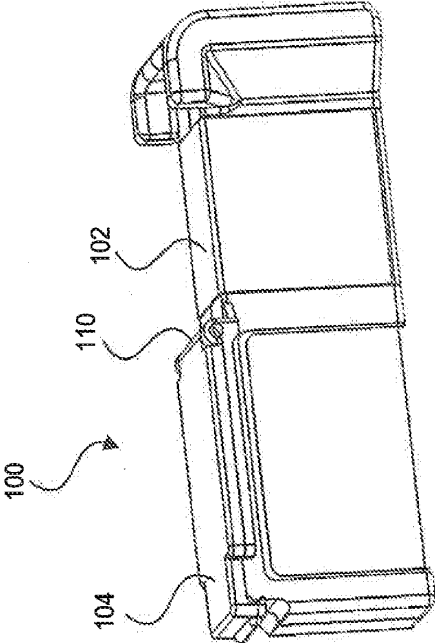
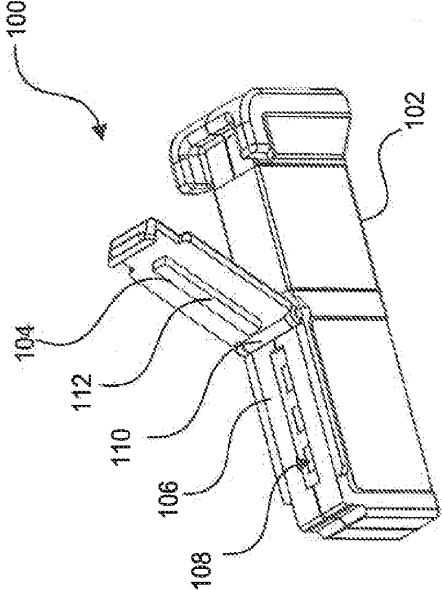


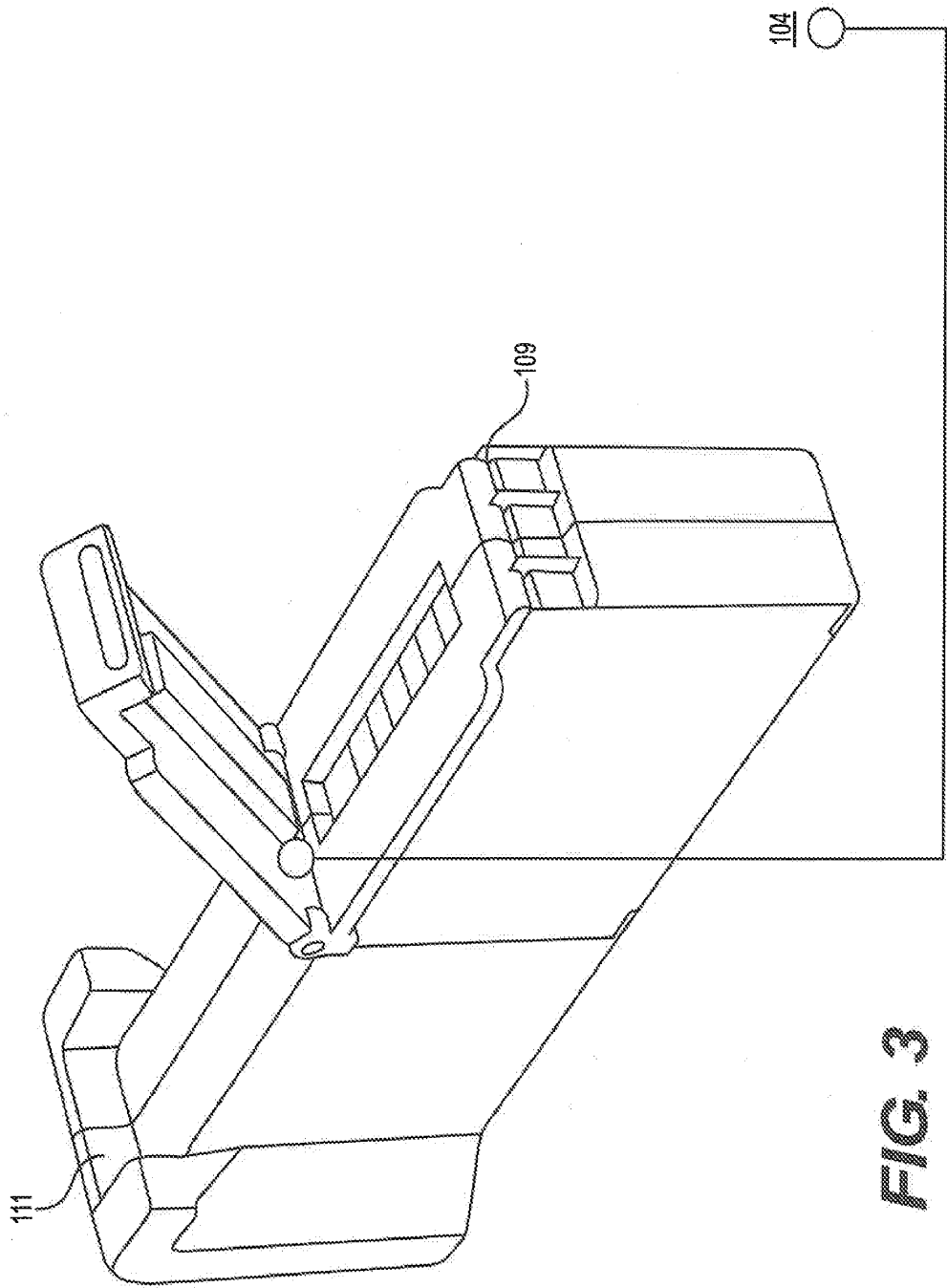
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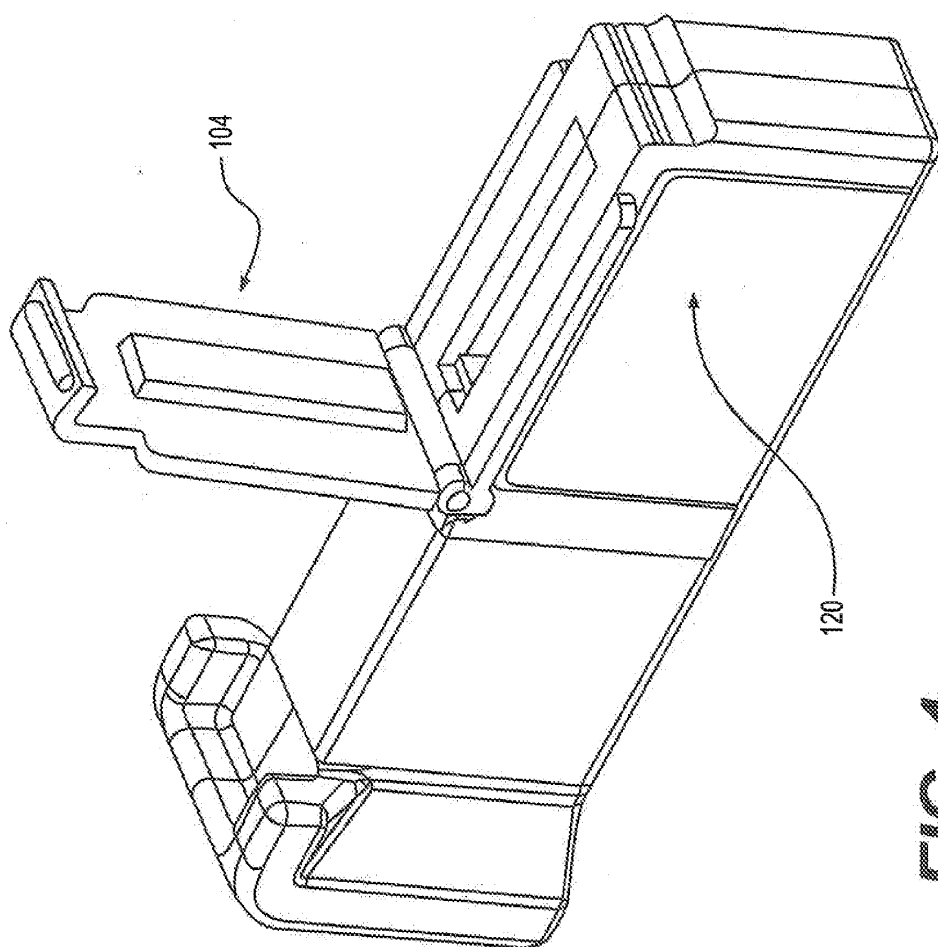


**FIG. 1**

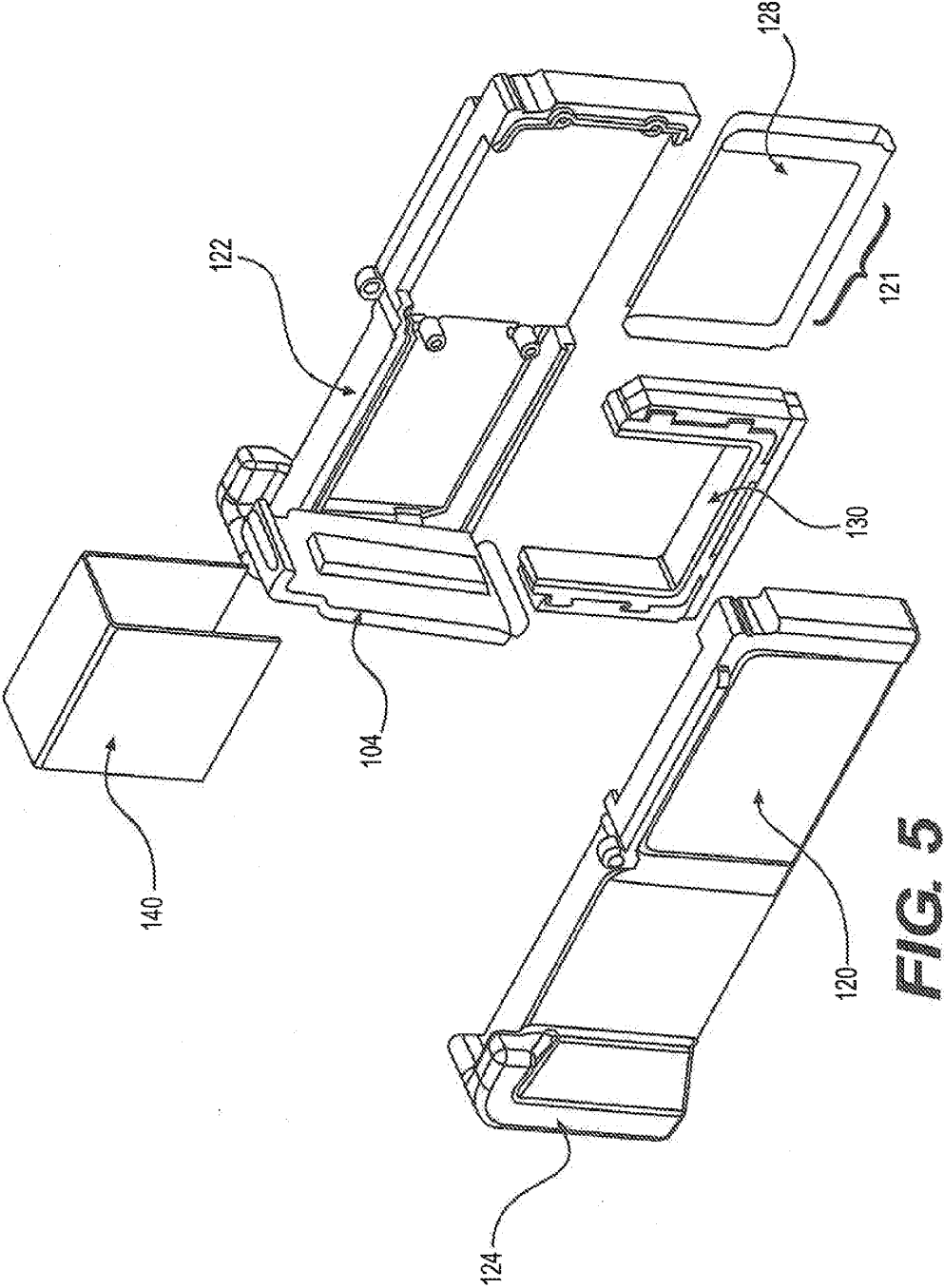


**FIG. 2**





**FIG. 4**



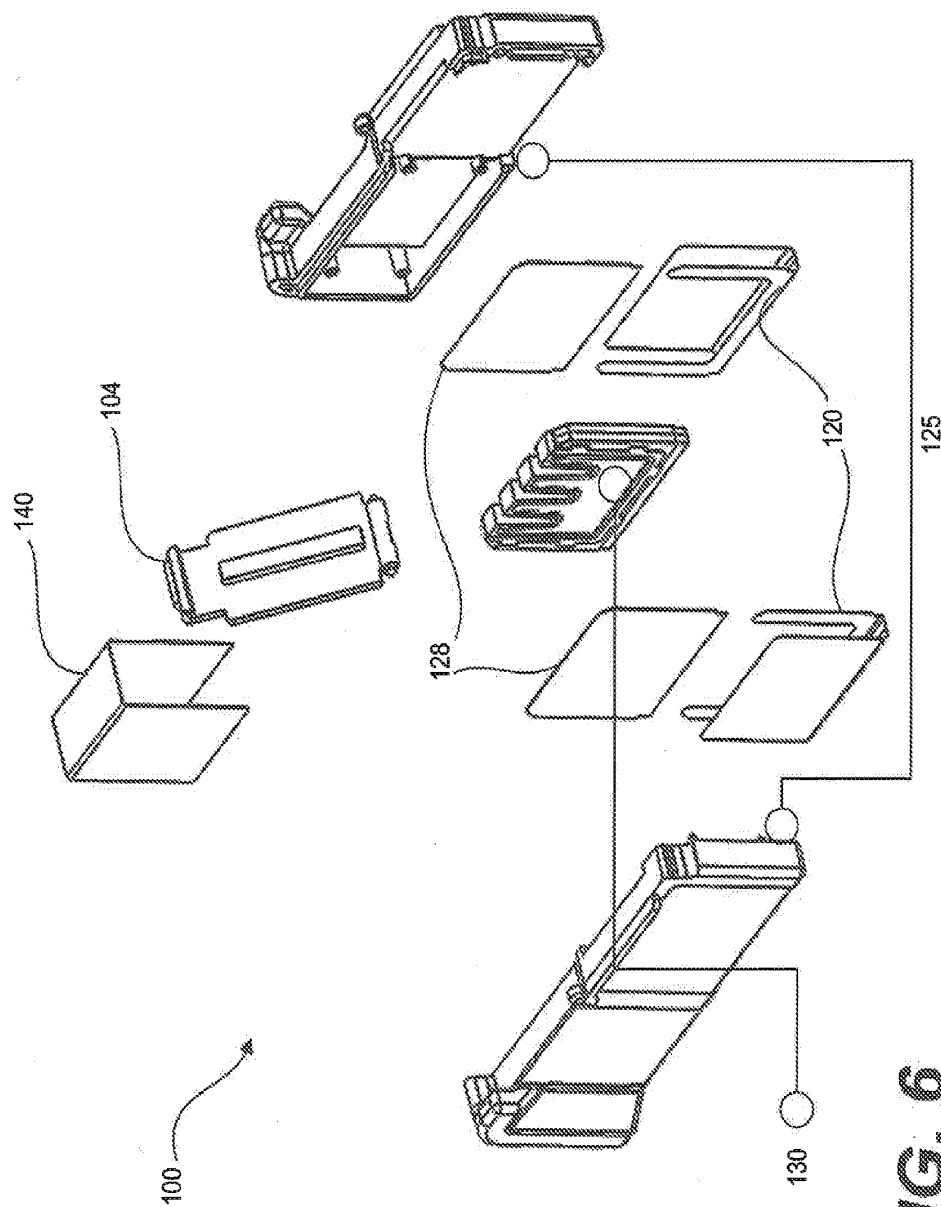
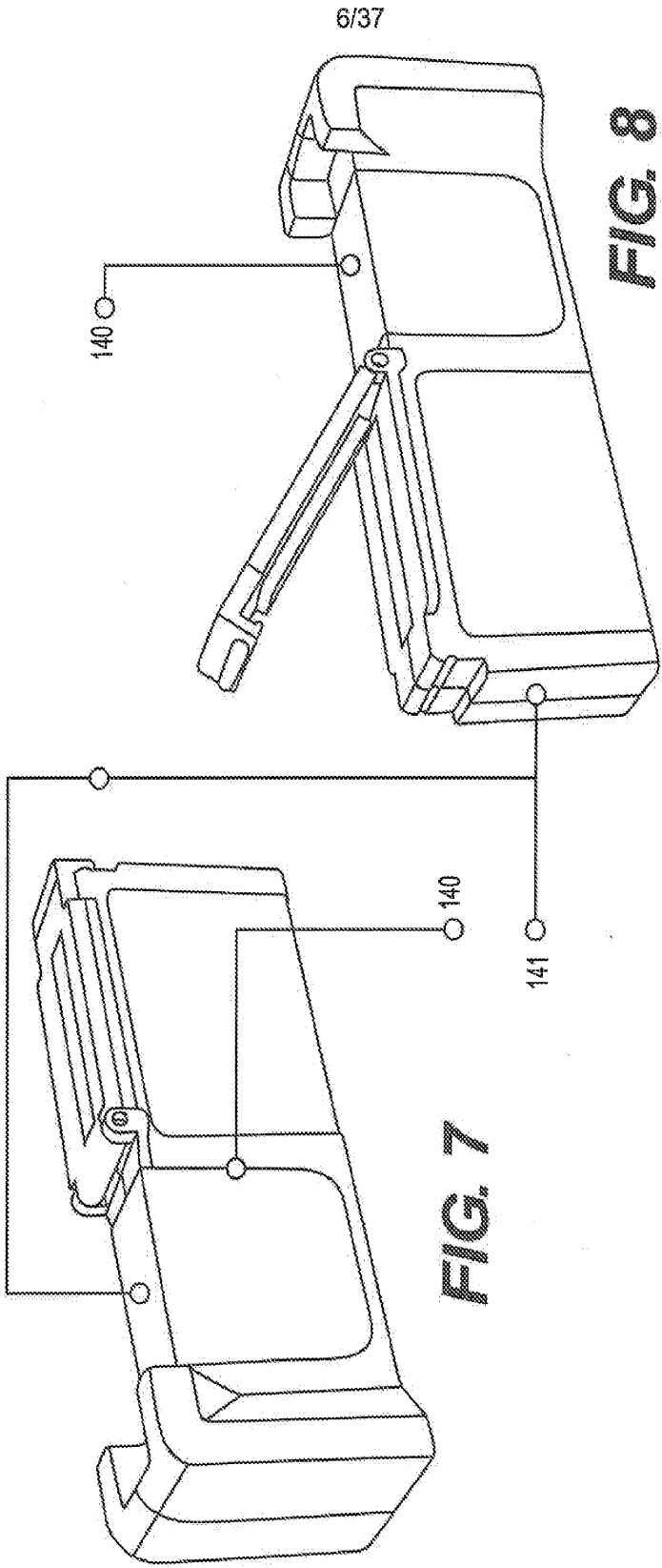


FIG. 6



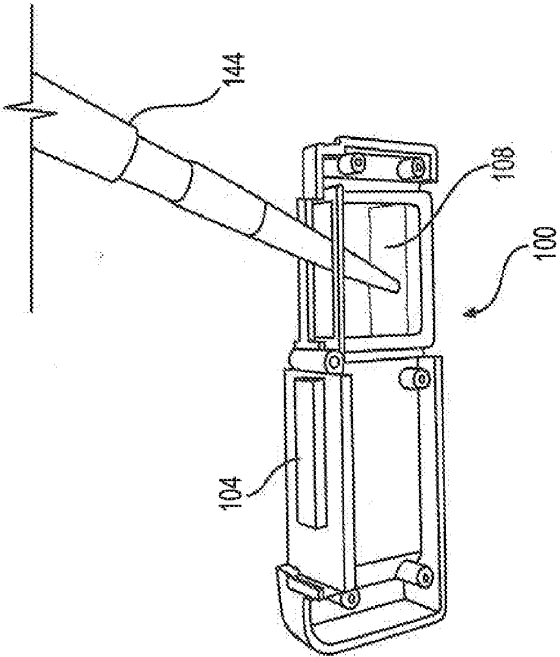


FIG. 10

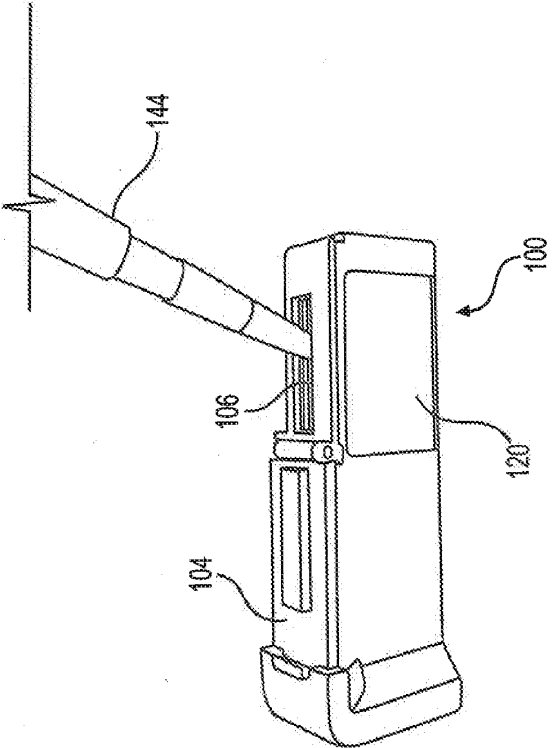


FIG. 9

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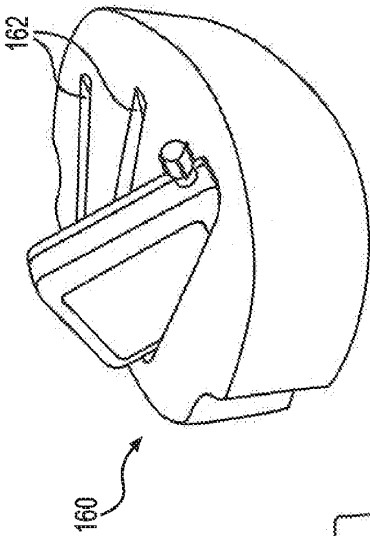


FIG. 13

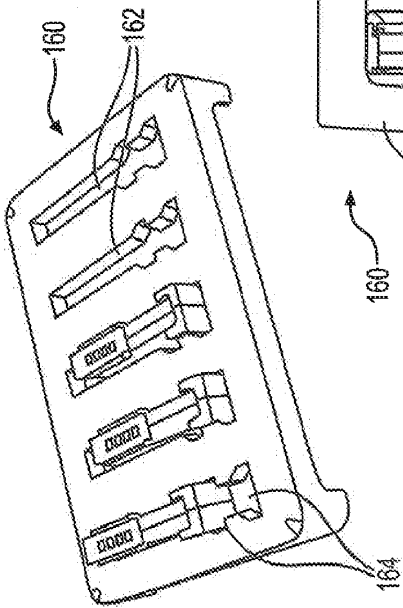


FIG. 11

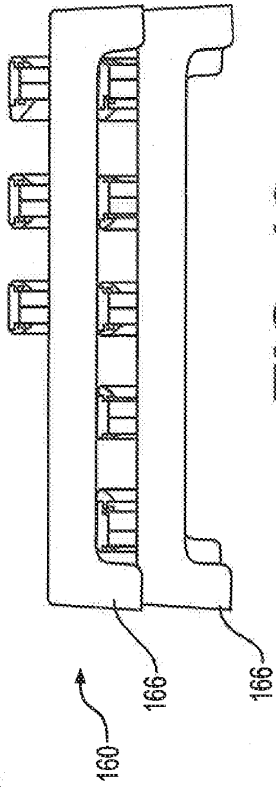


FIG. 12

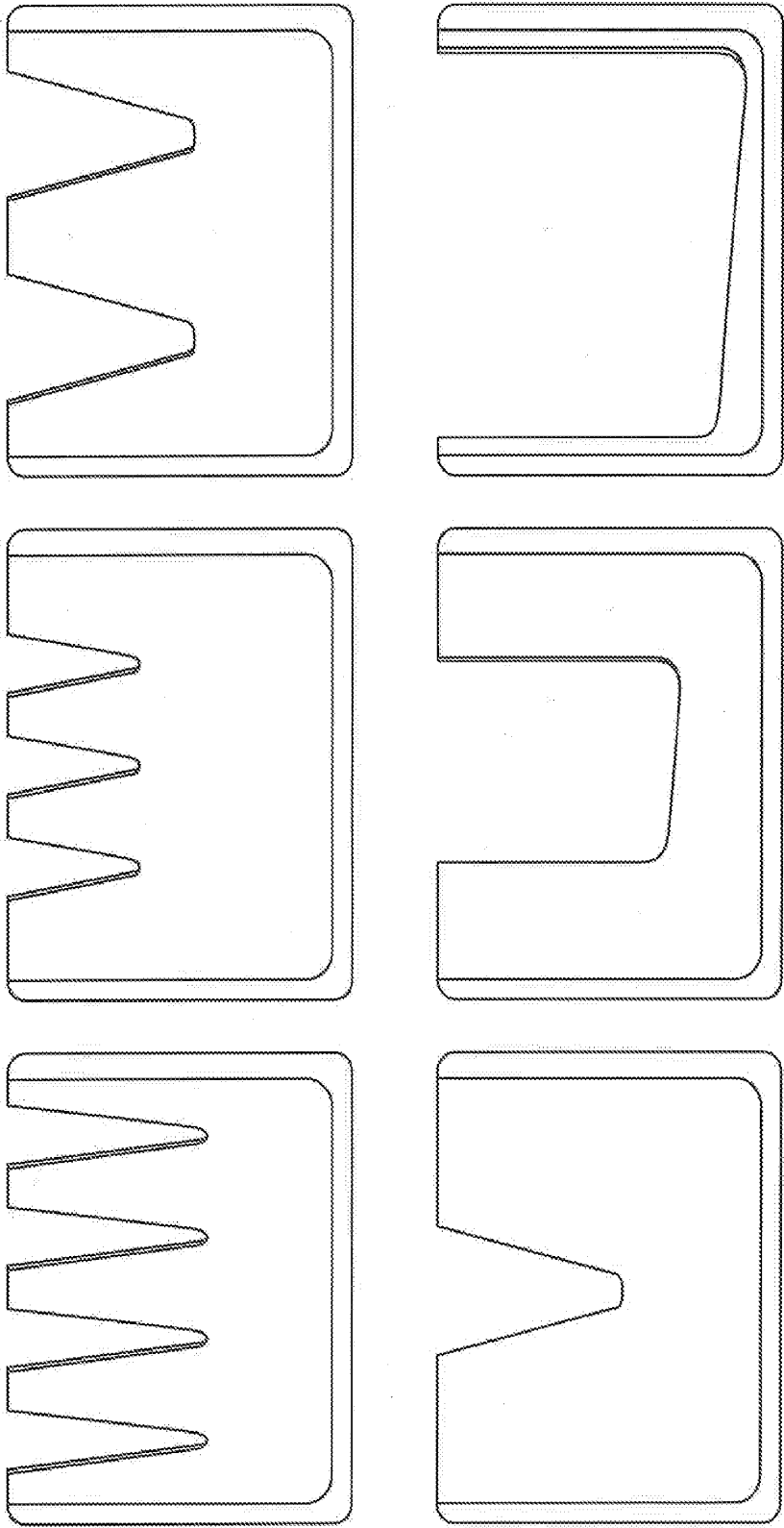
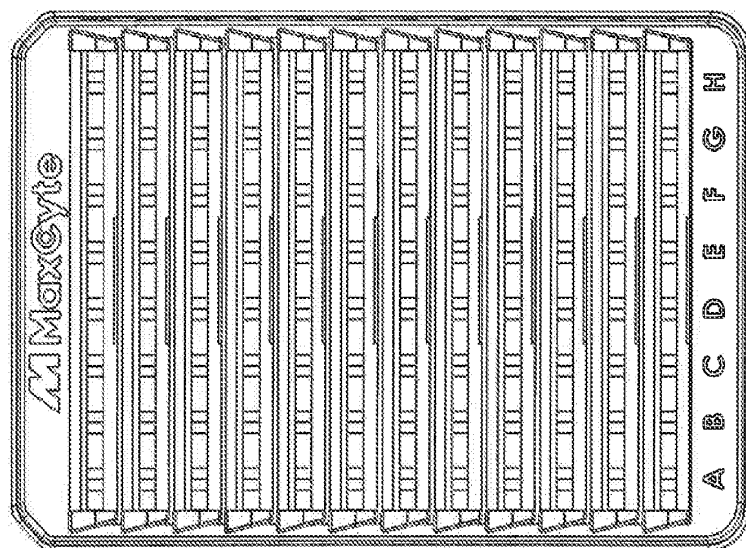
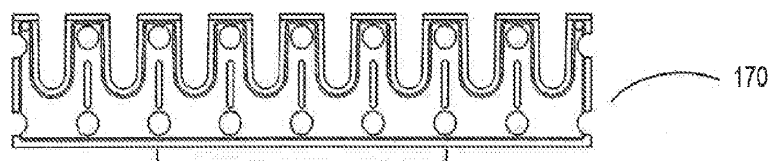


FIG. 14

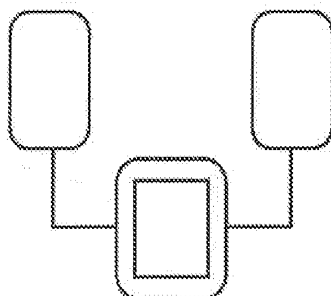
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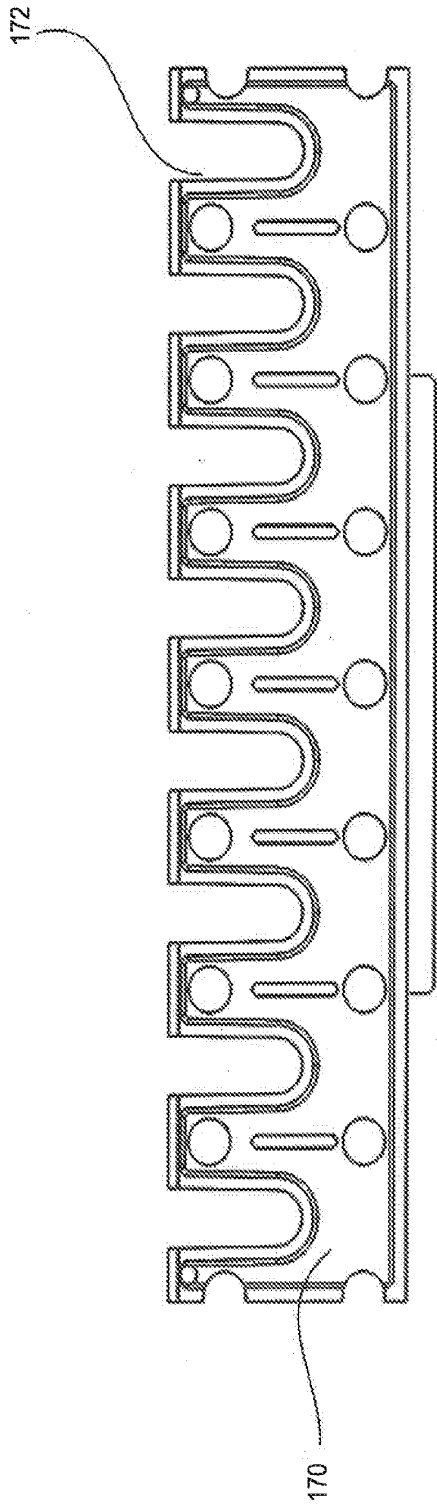


**FIG. 15**



**FIG. 16**





**FIG. 17**

FIG. 18

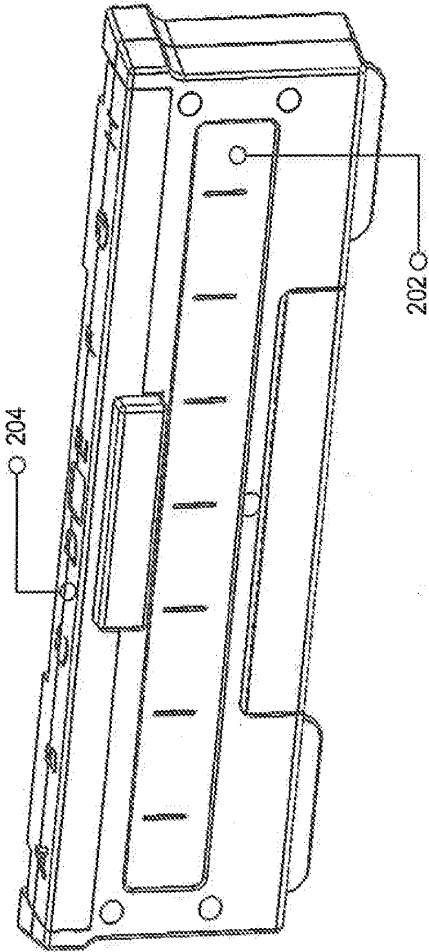
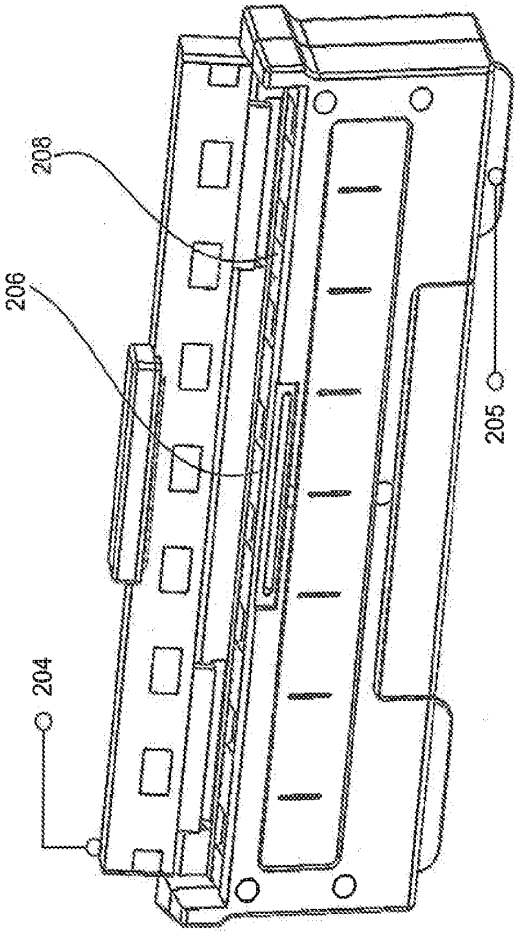


FIG. 19



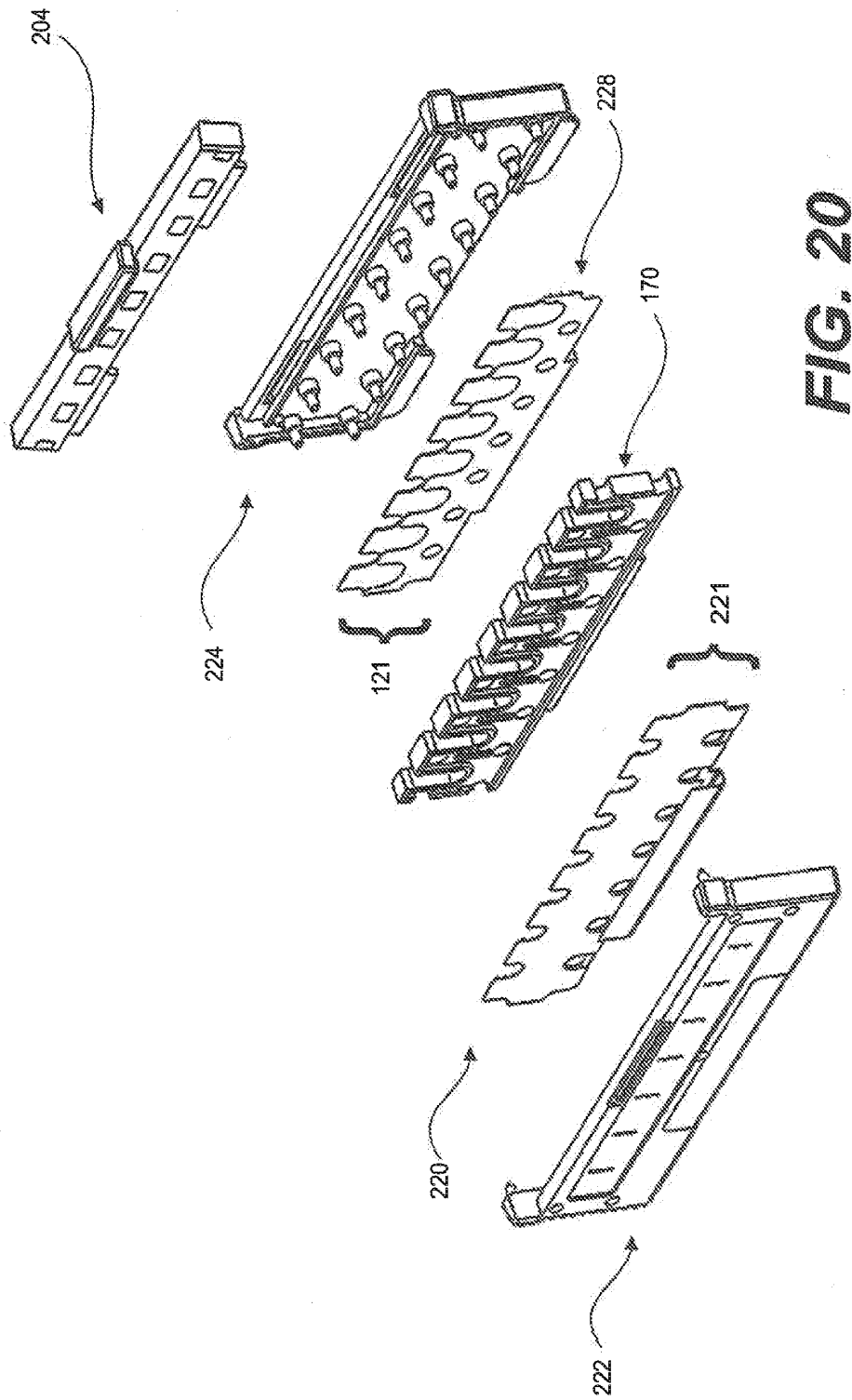


FIG. 20

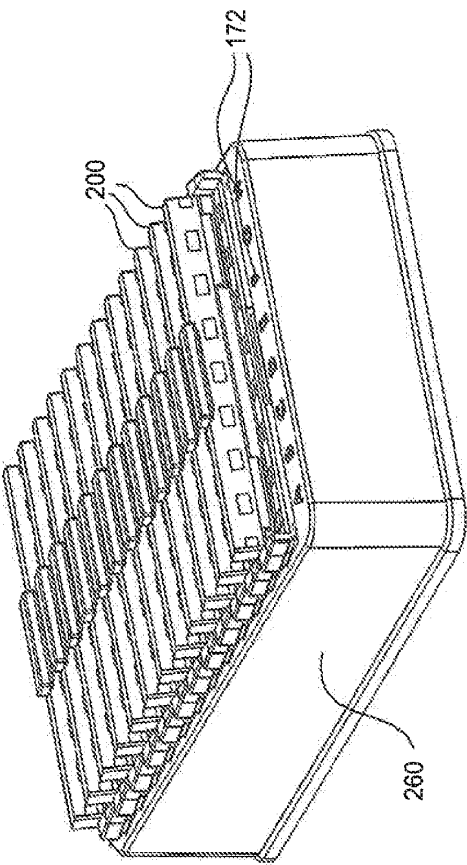


FIG. 21

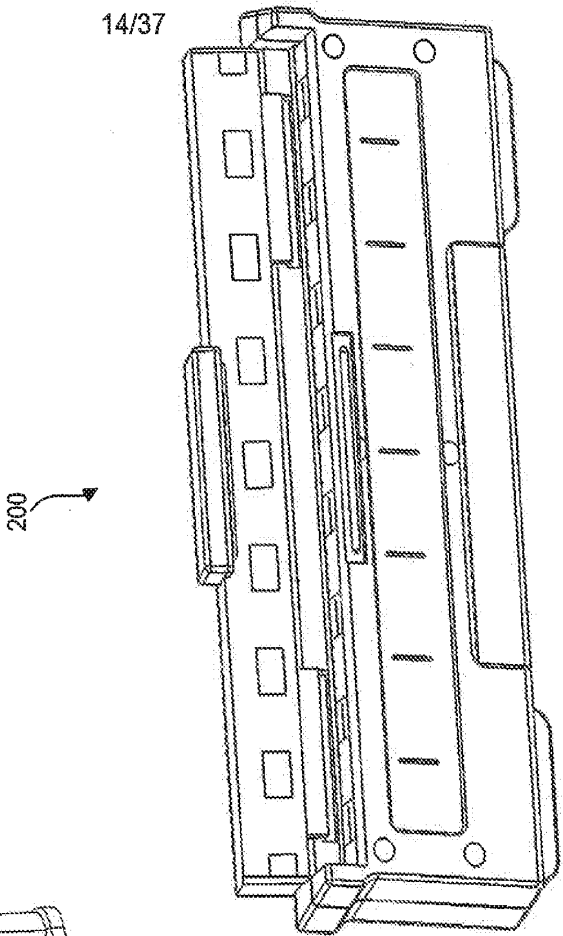


FIG. 22

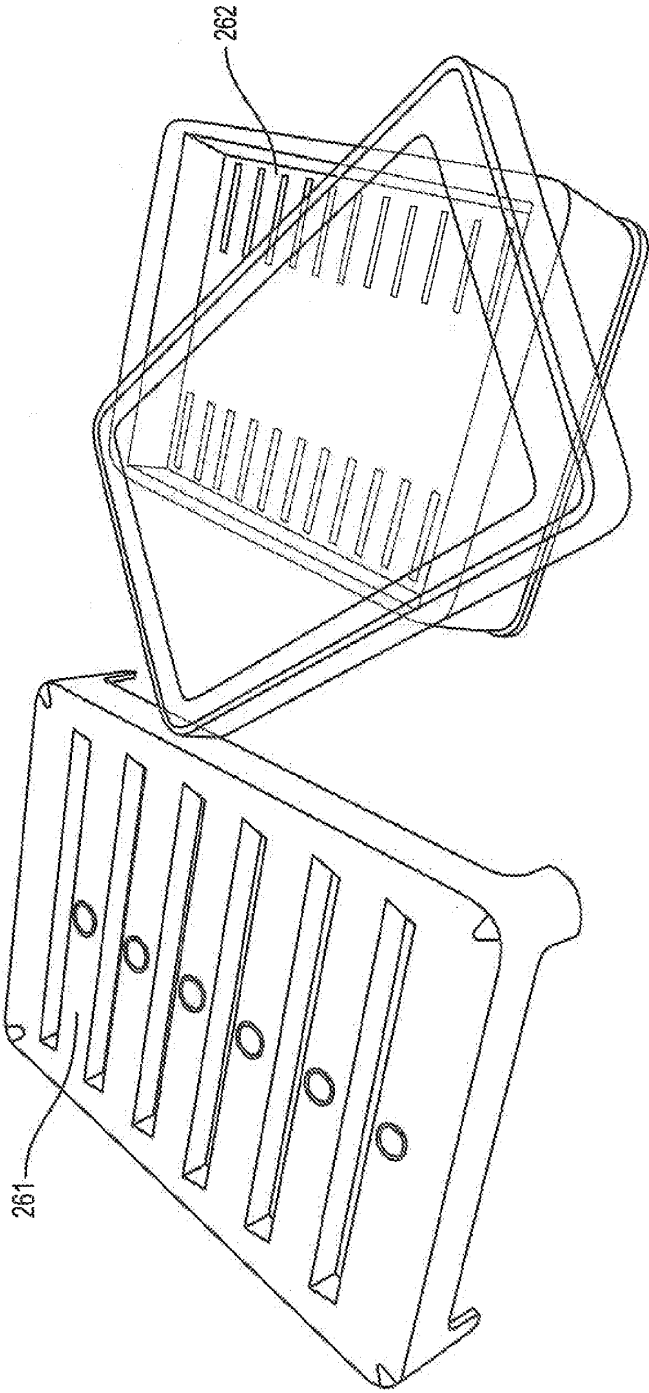


FIG. 23

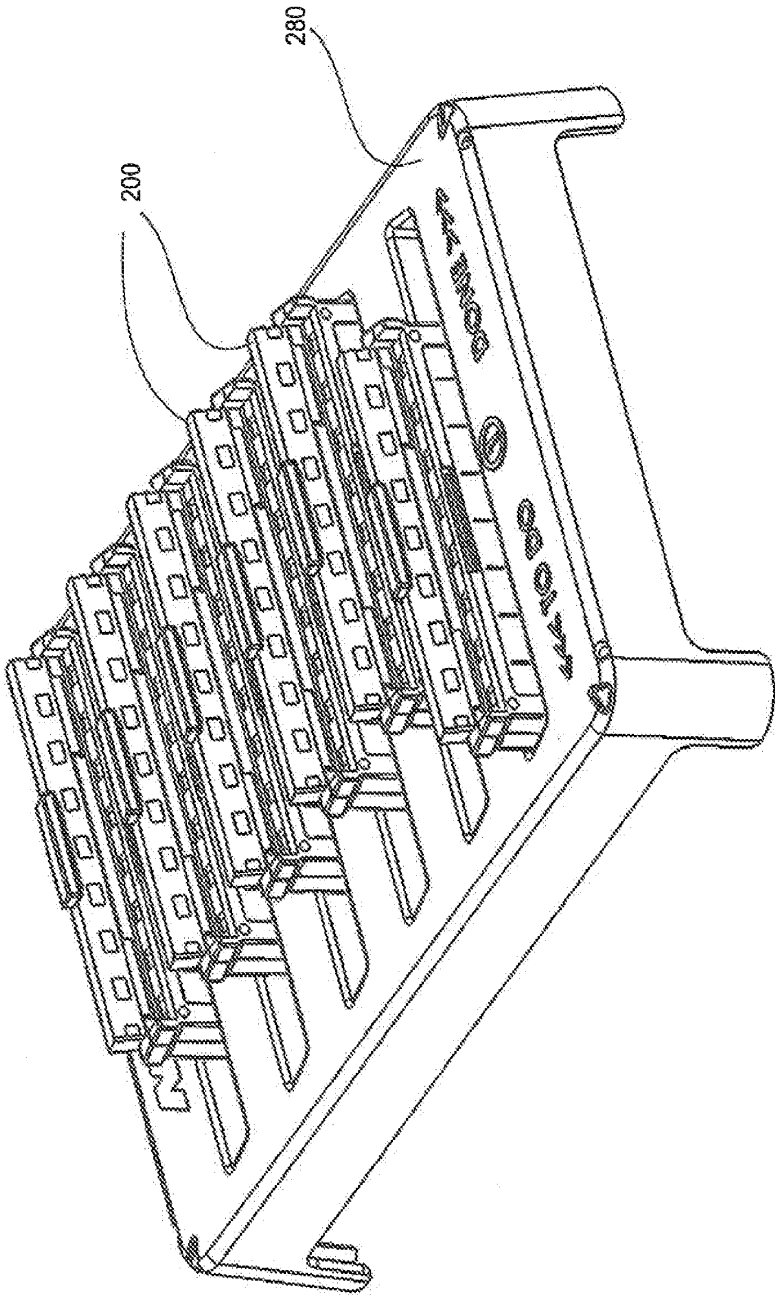
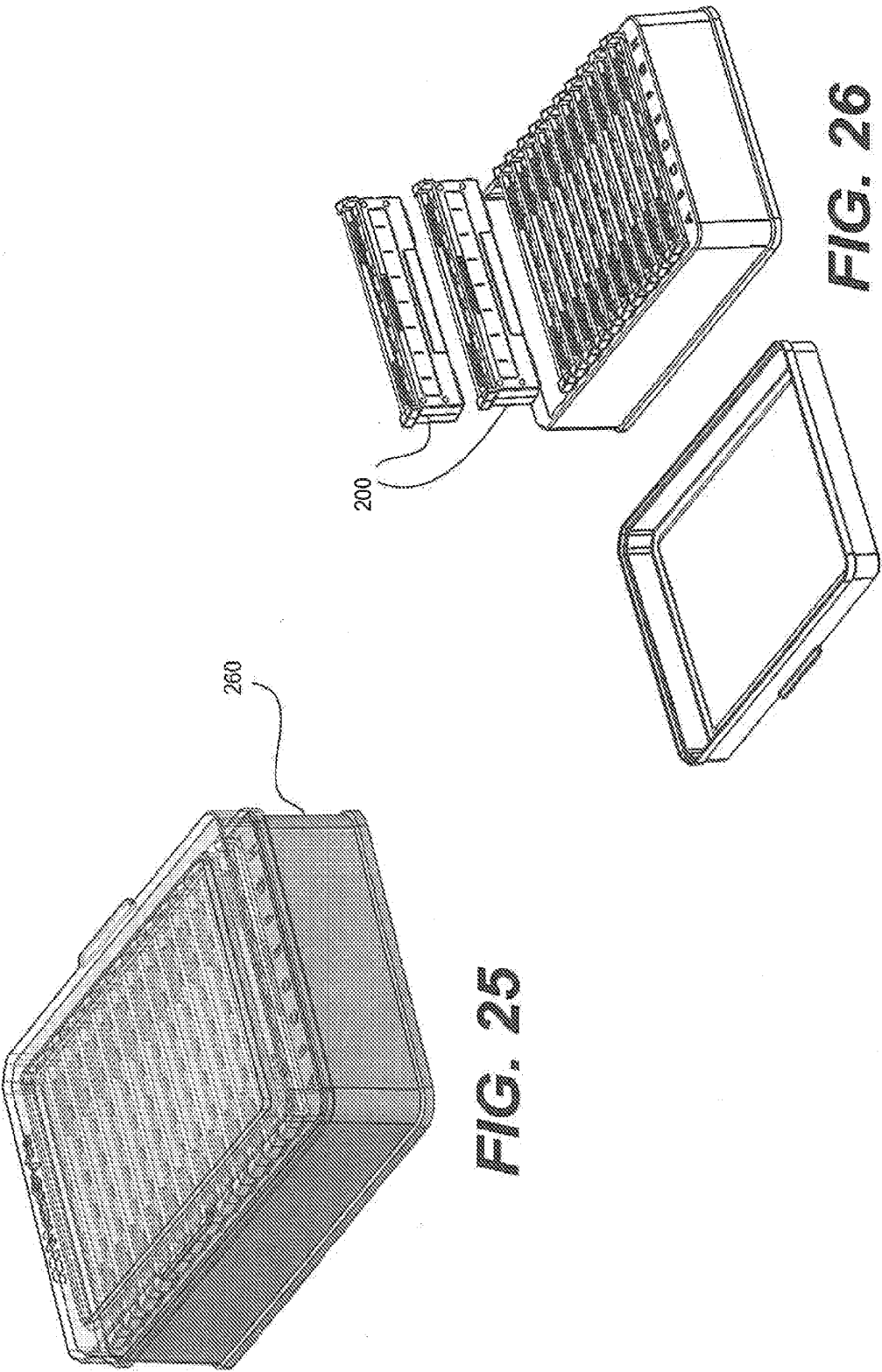


FIG. 24



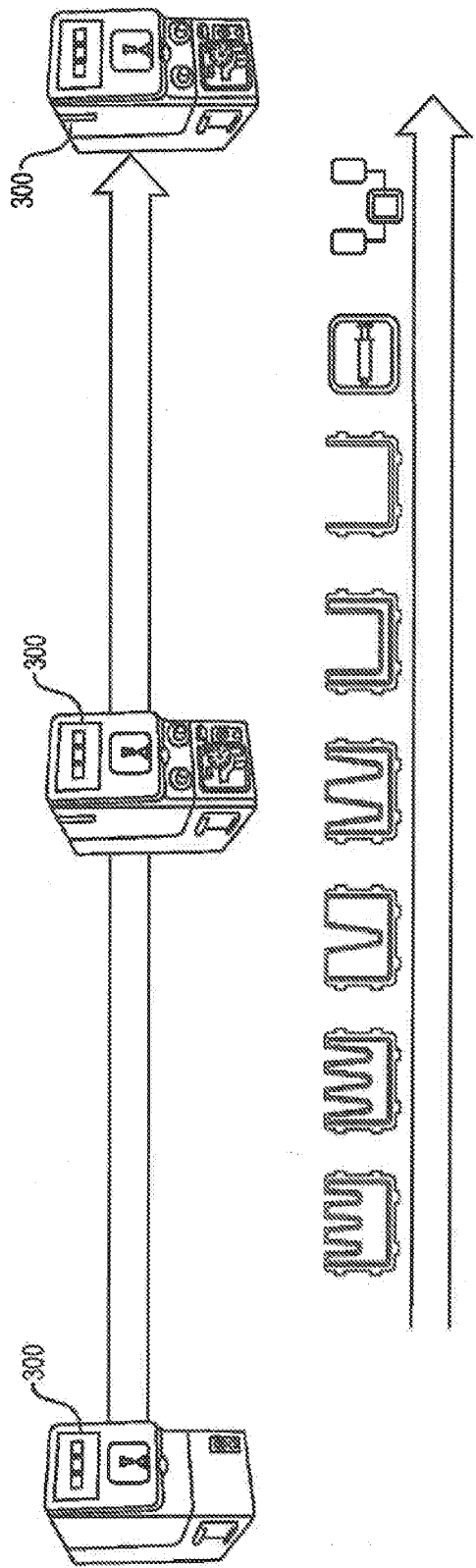


FIG. 27

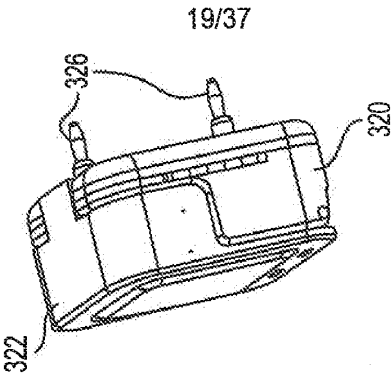


FIG. 30

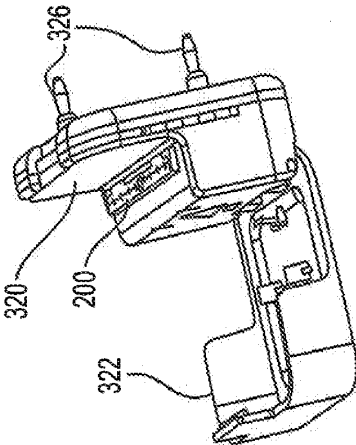


FIG. 29

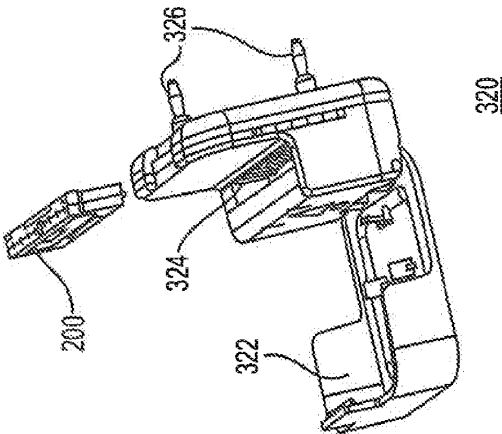
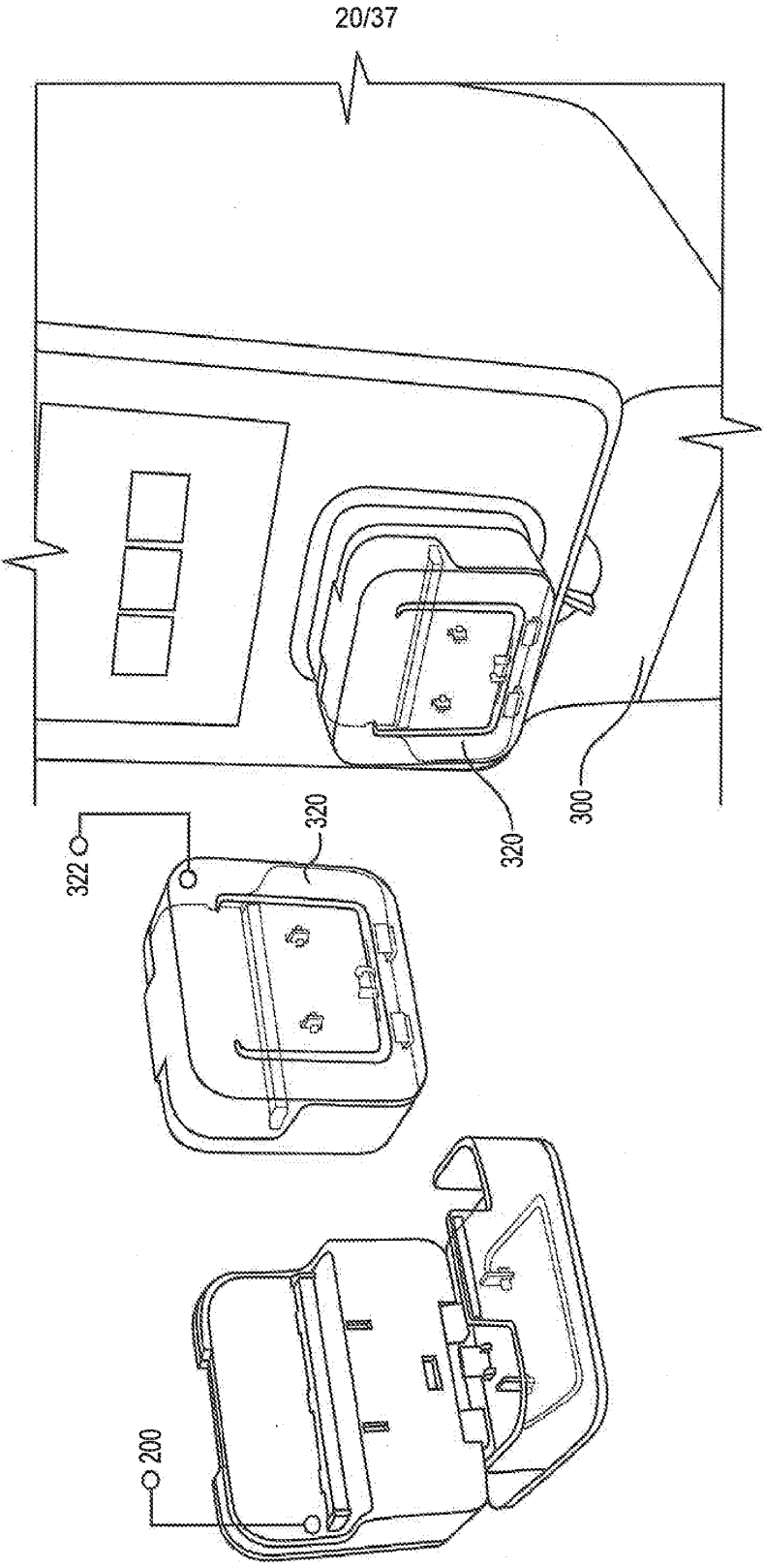
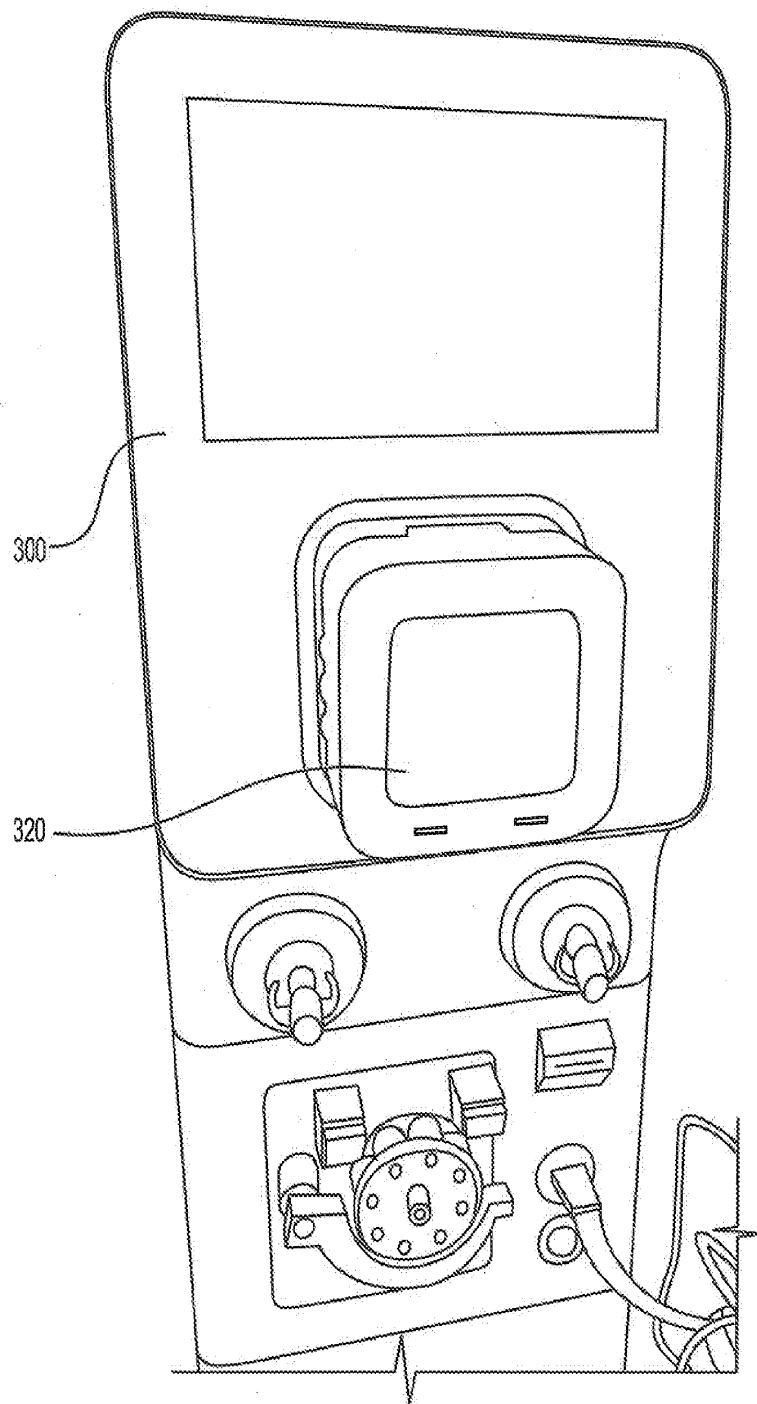


FIG. 28



**FIG. 31**



**FIG. 32**

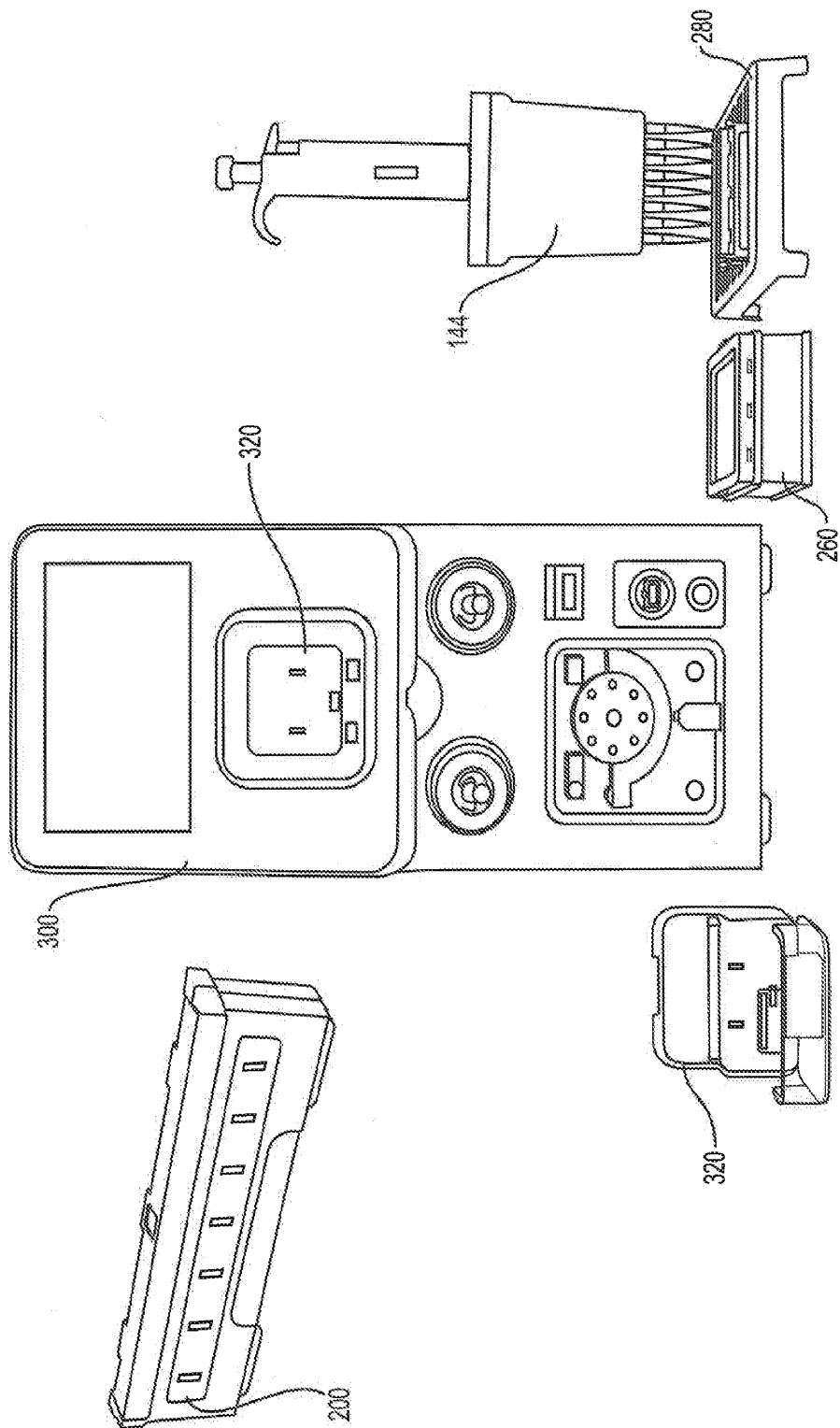
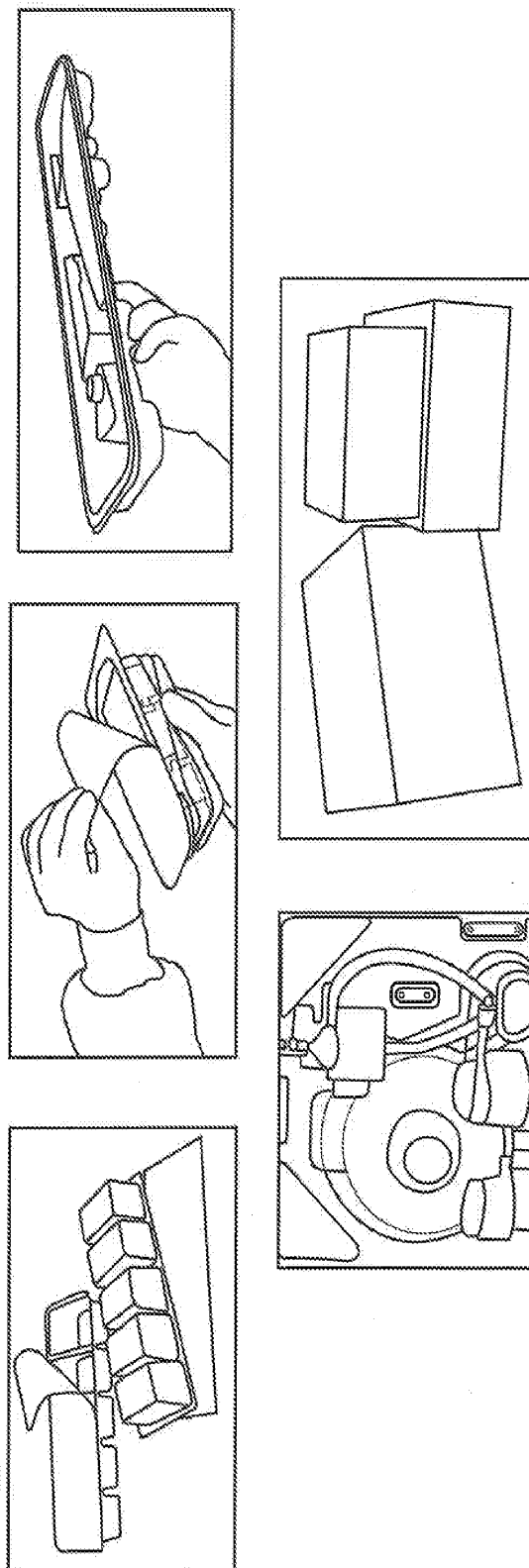


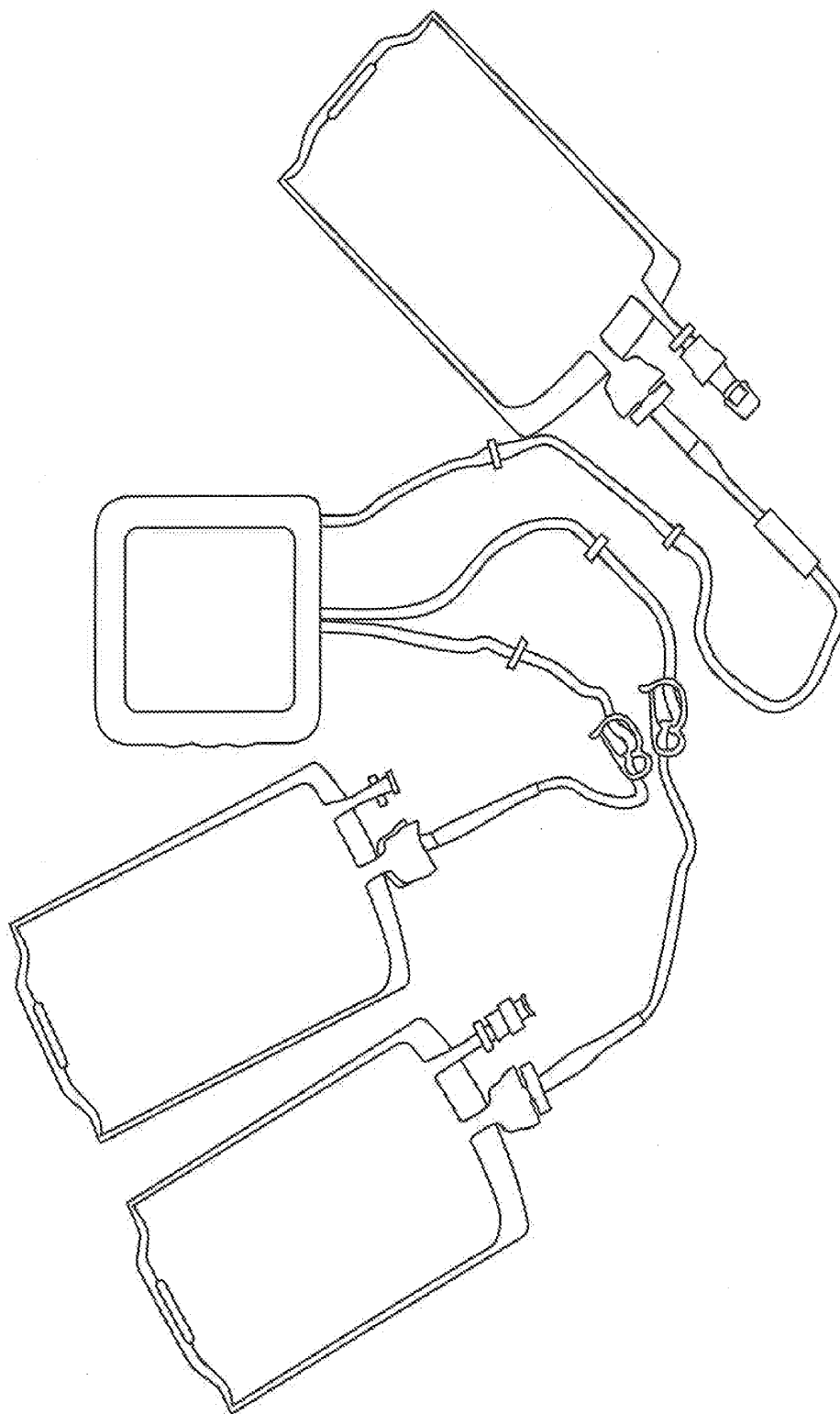
FIG. 33

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**FIG. 34**

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**FIG. 35**

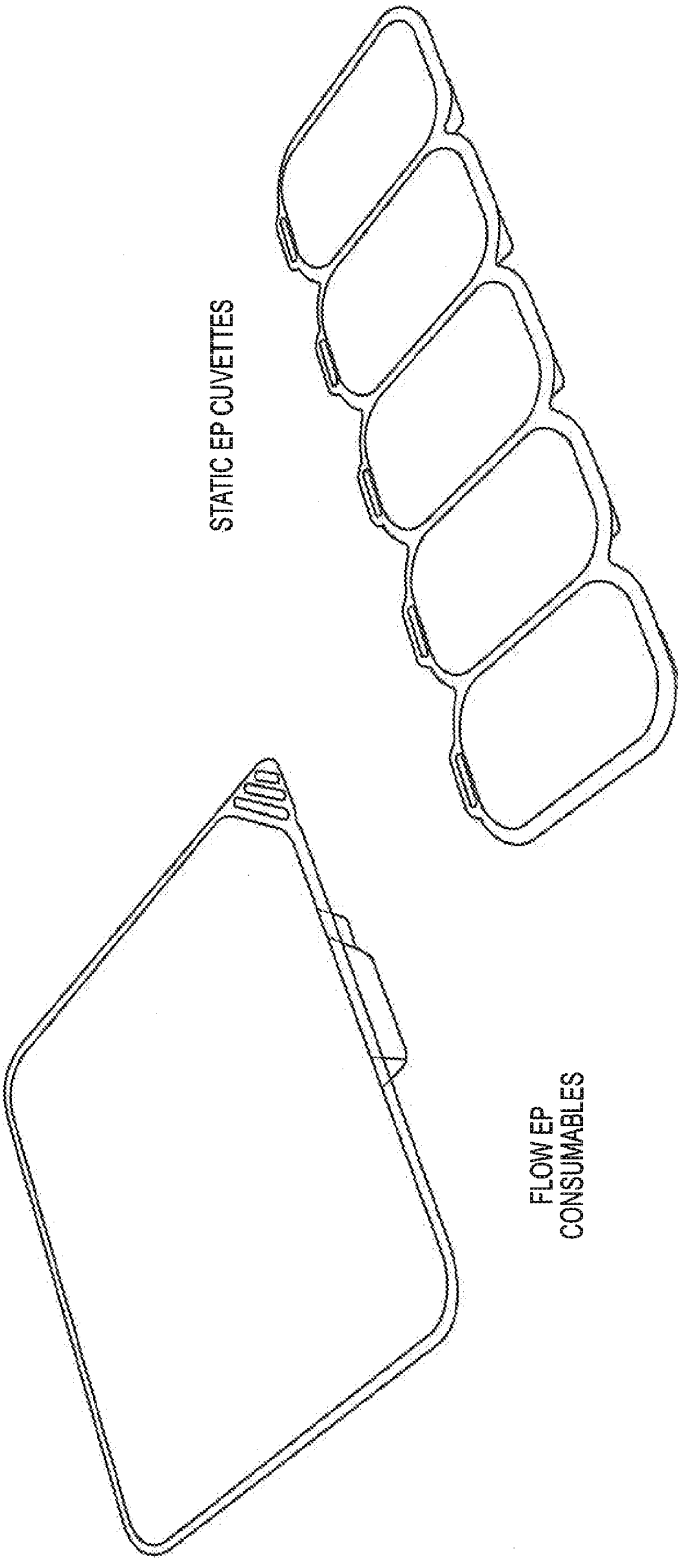


FIG. 36

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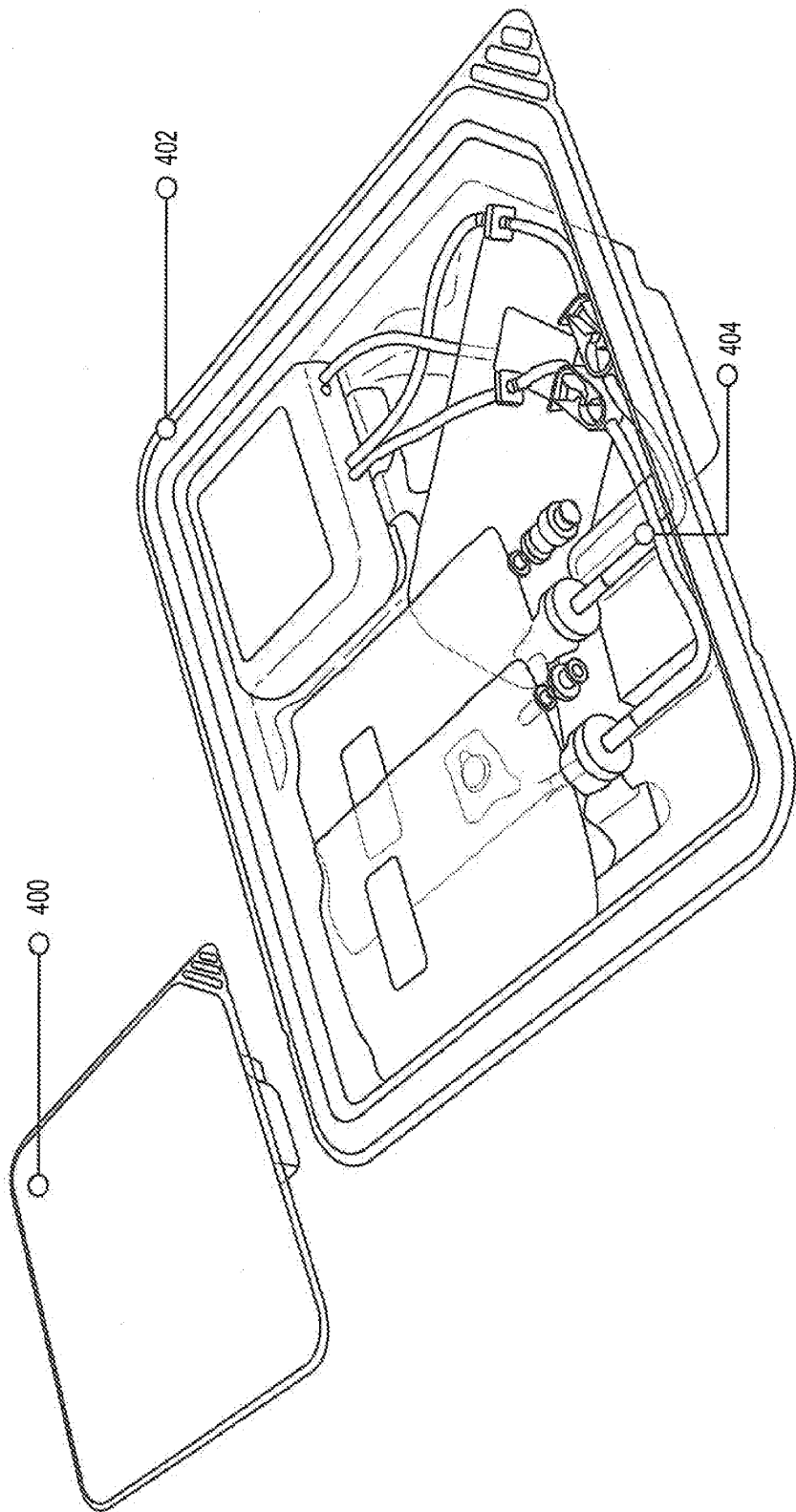


FIG. 37

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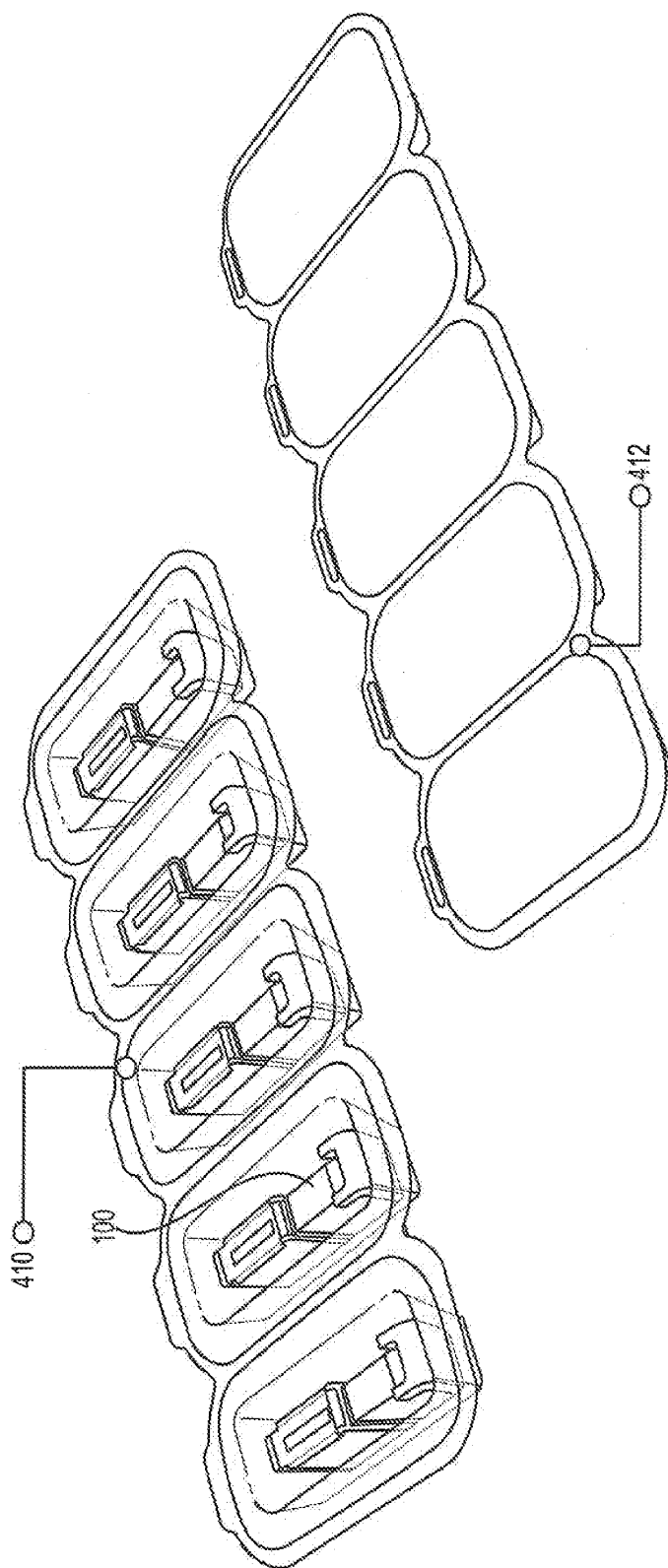
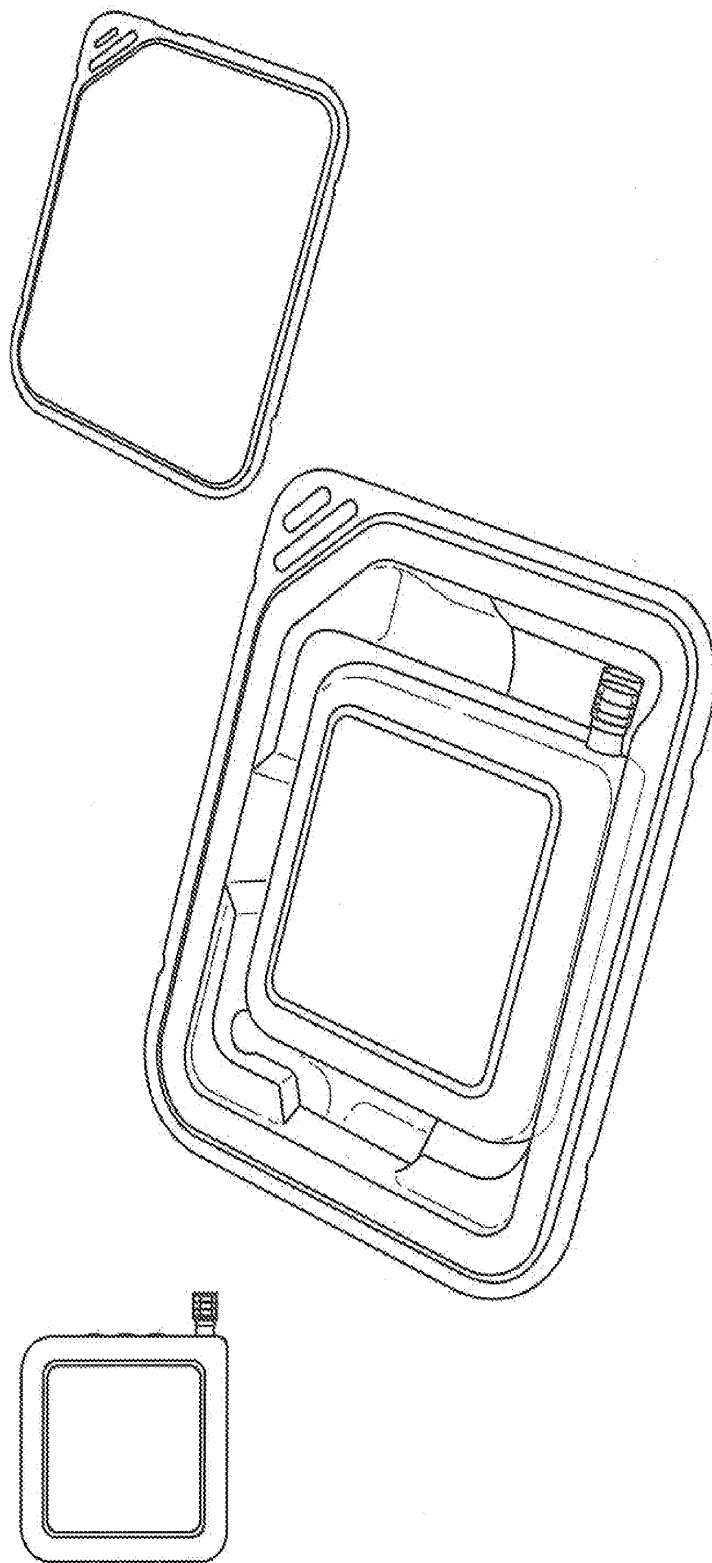


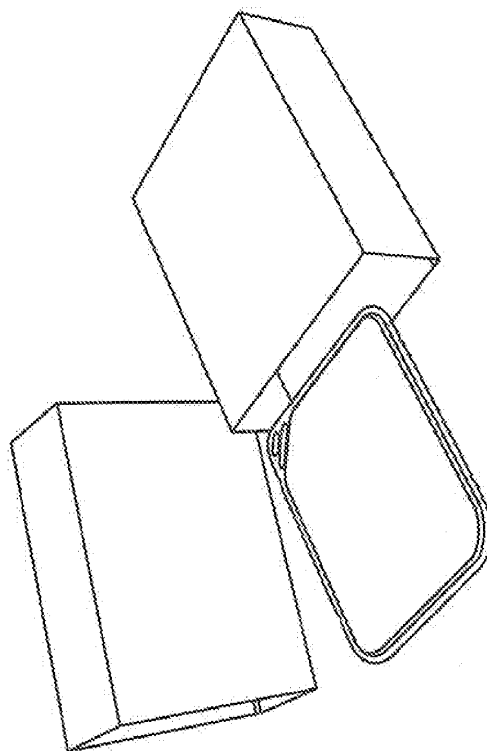
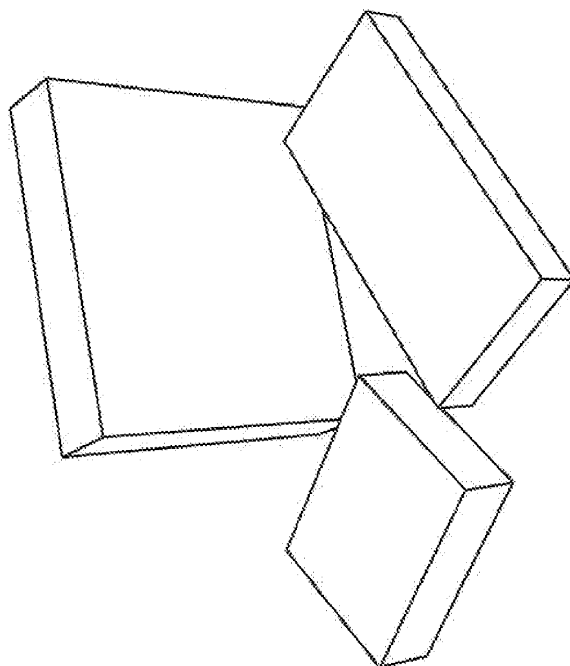
FIG. 38

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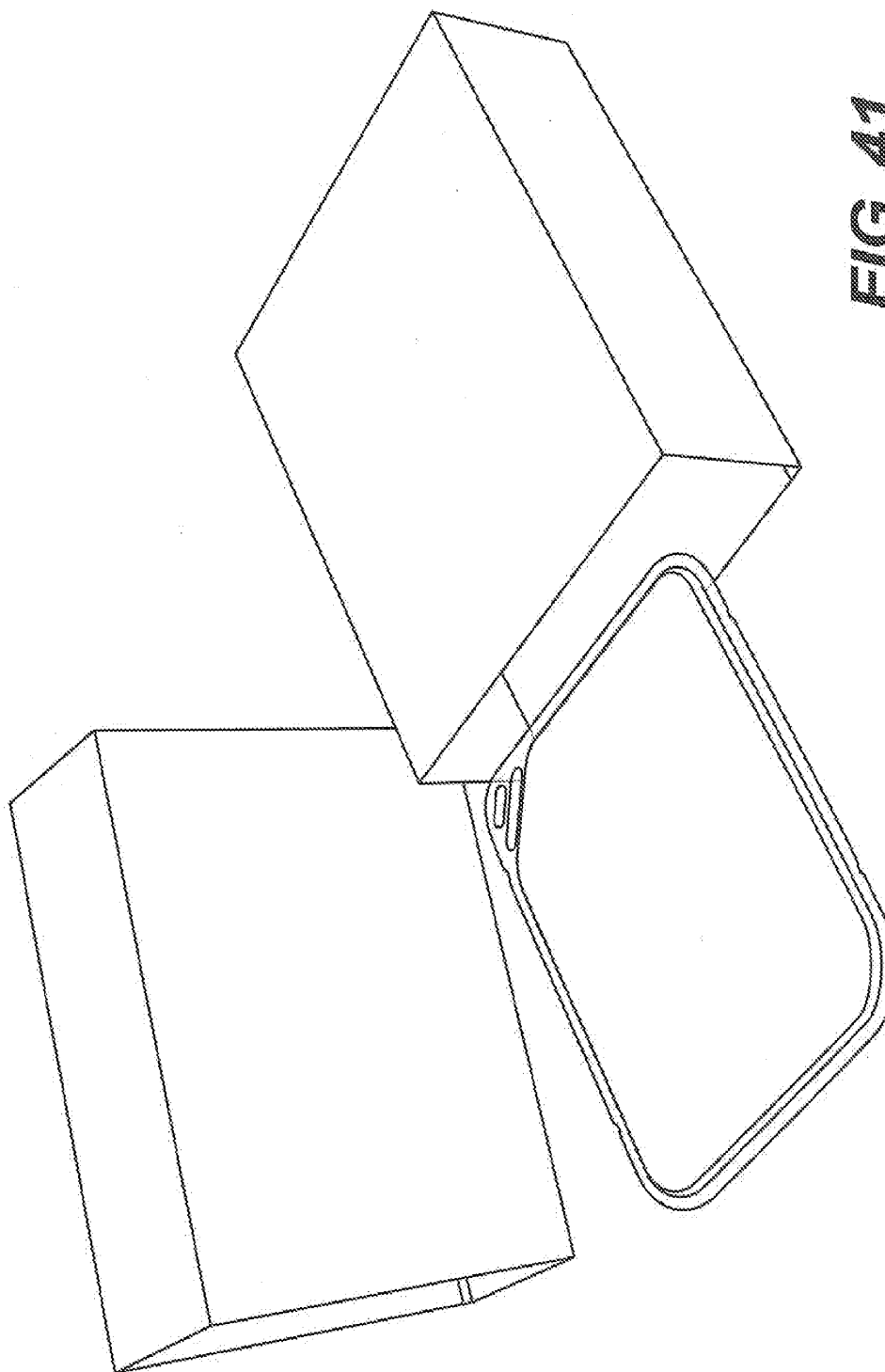
**FIG. 39**

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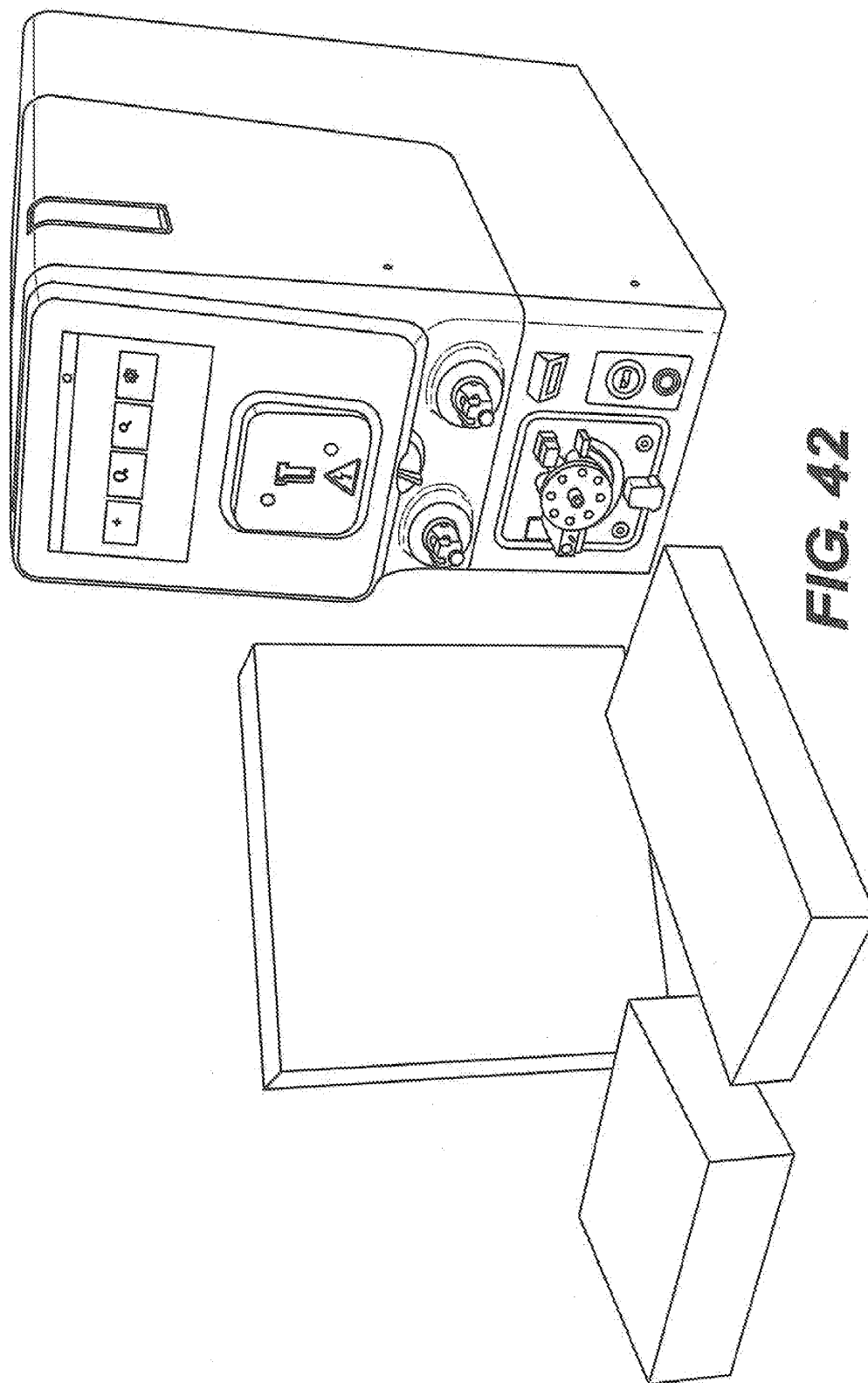
**FIG. 40**

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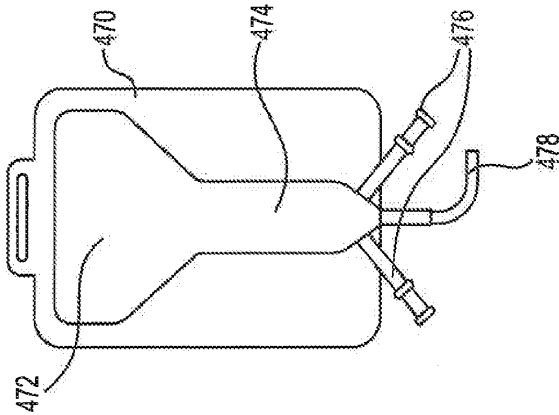
**FIG. 41**

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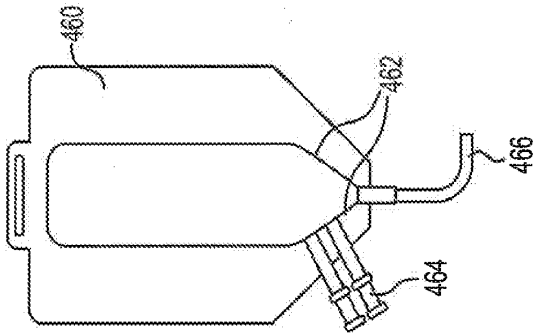


**FIG. 42**

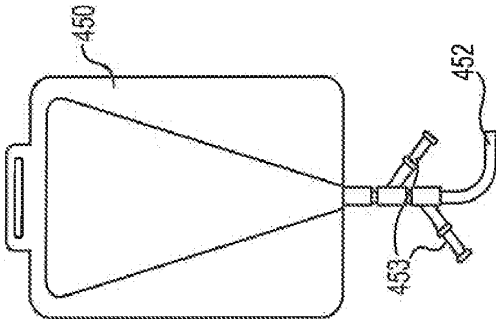
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**FIG. 45**

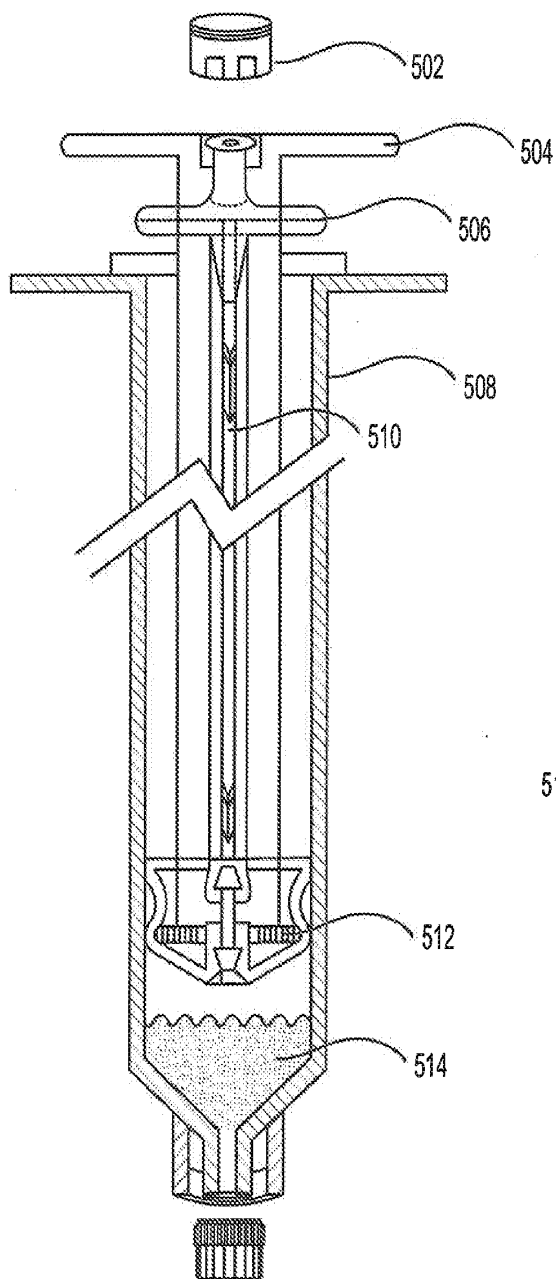


**FIG. 44**



**FIG. 43**

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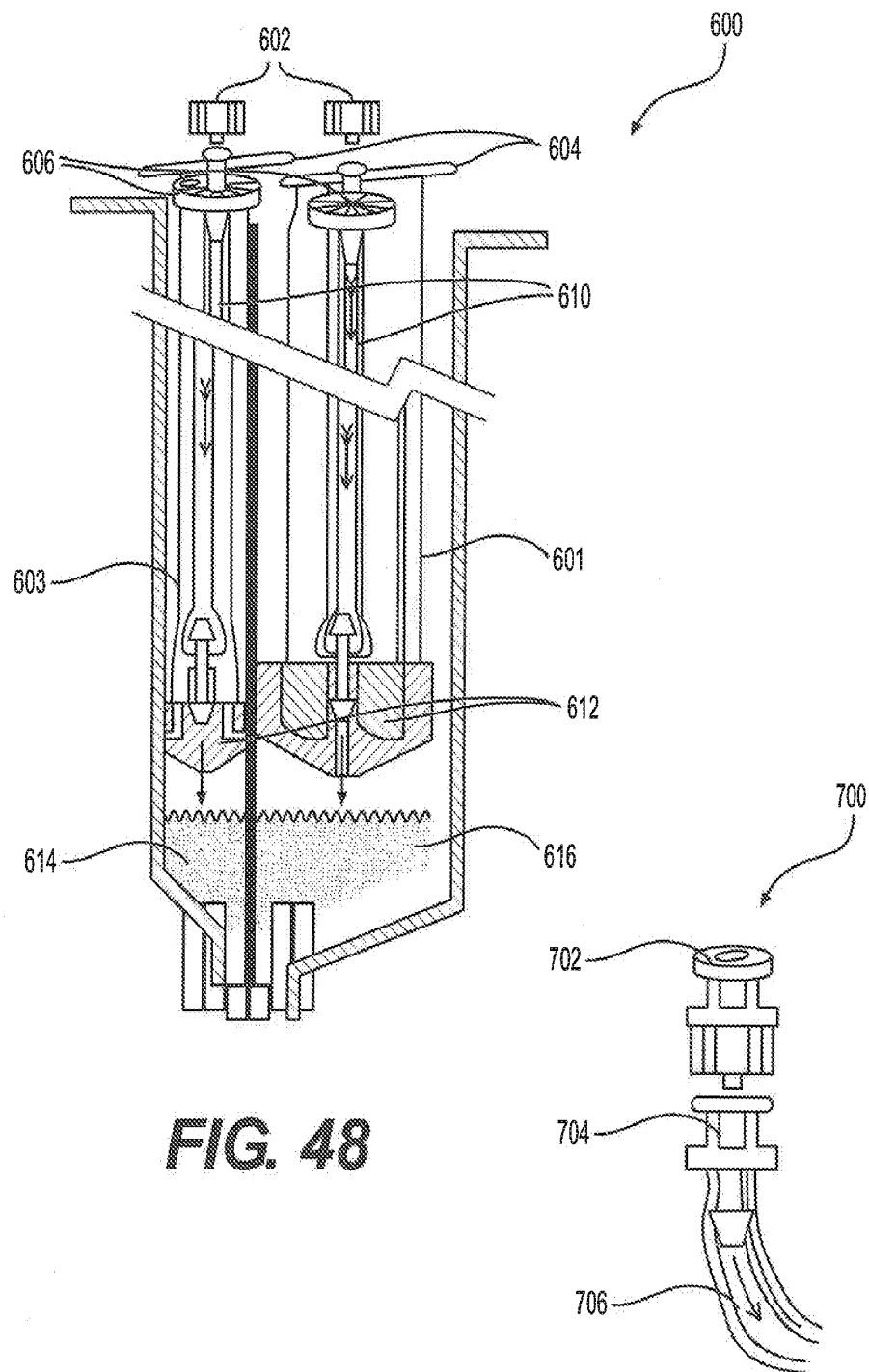


**FIG. 46**



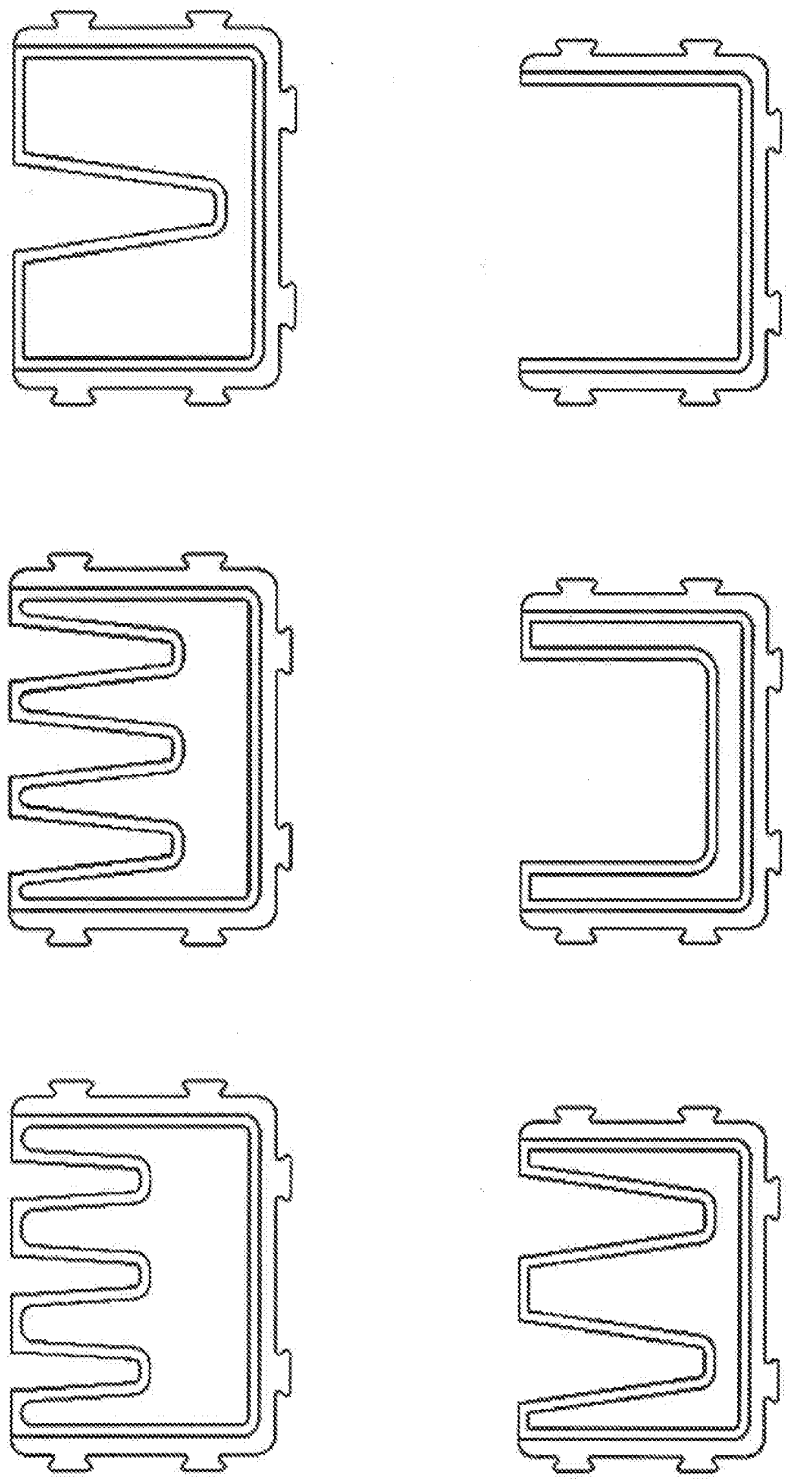
**FIG. 47**

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**FIG. 48**

**FIG. 49**



**FIG. 50**

Performance Testing - Viability

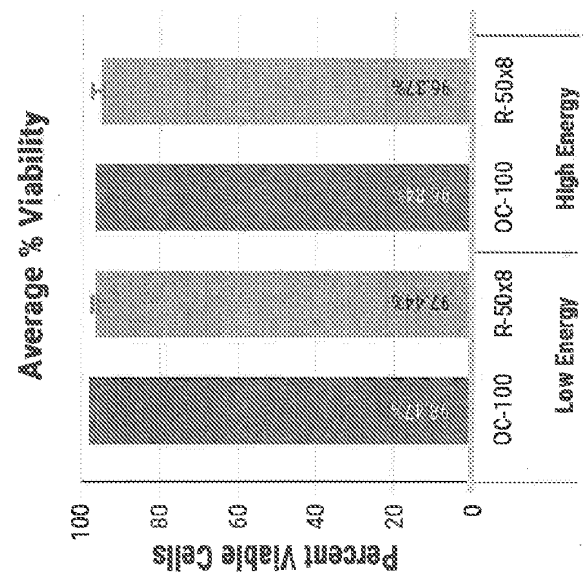


FIG. 51

Performance Testing - Efficiency

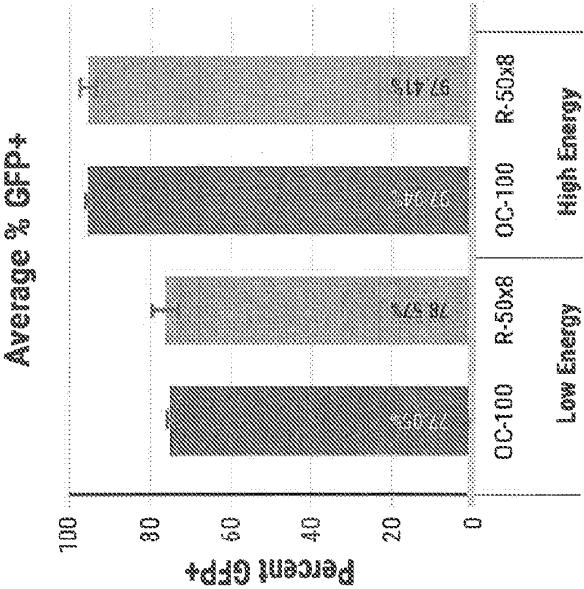


FIG. 52