

LOW VOLTAGE DEVICE OPERABLE WITH A HIGH VOLTAGE
SUPPLY

ABSTRACT

This invention discloses an integrated circuit device including transistors having predetermined upper voltage limits, a multi-layer metal interconnect structure connecting the transistors to each other and to an external voltage source having a voltage in excess of the predetermined upper voltage limits of a first plurality of the transistors, and a voltage reducer connected along the interconnect structure between the external voltage source and the first plurality of the transistors.

Fig. 1

This Sheet Has Been Replaced

