

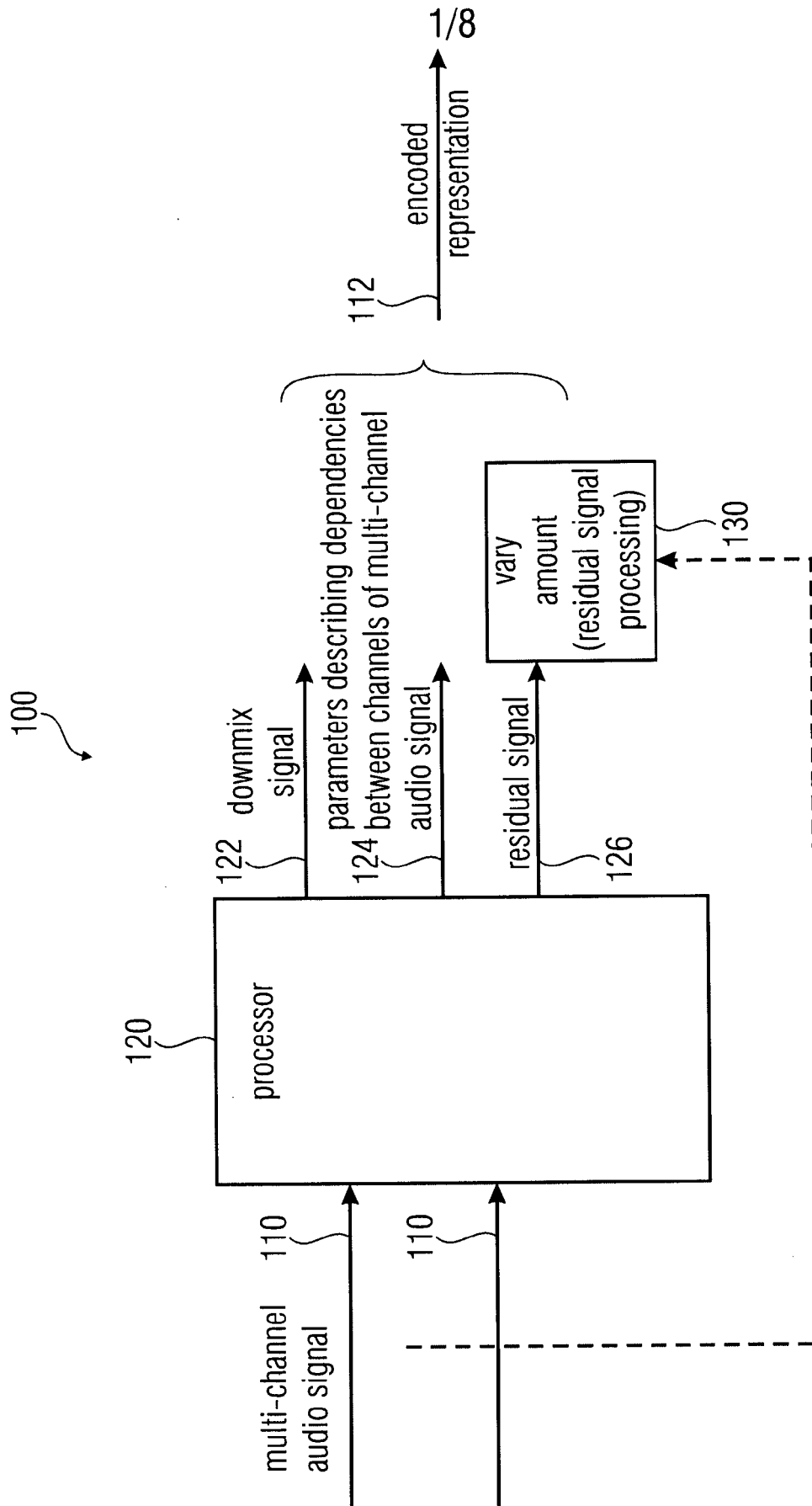


FIG 1

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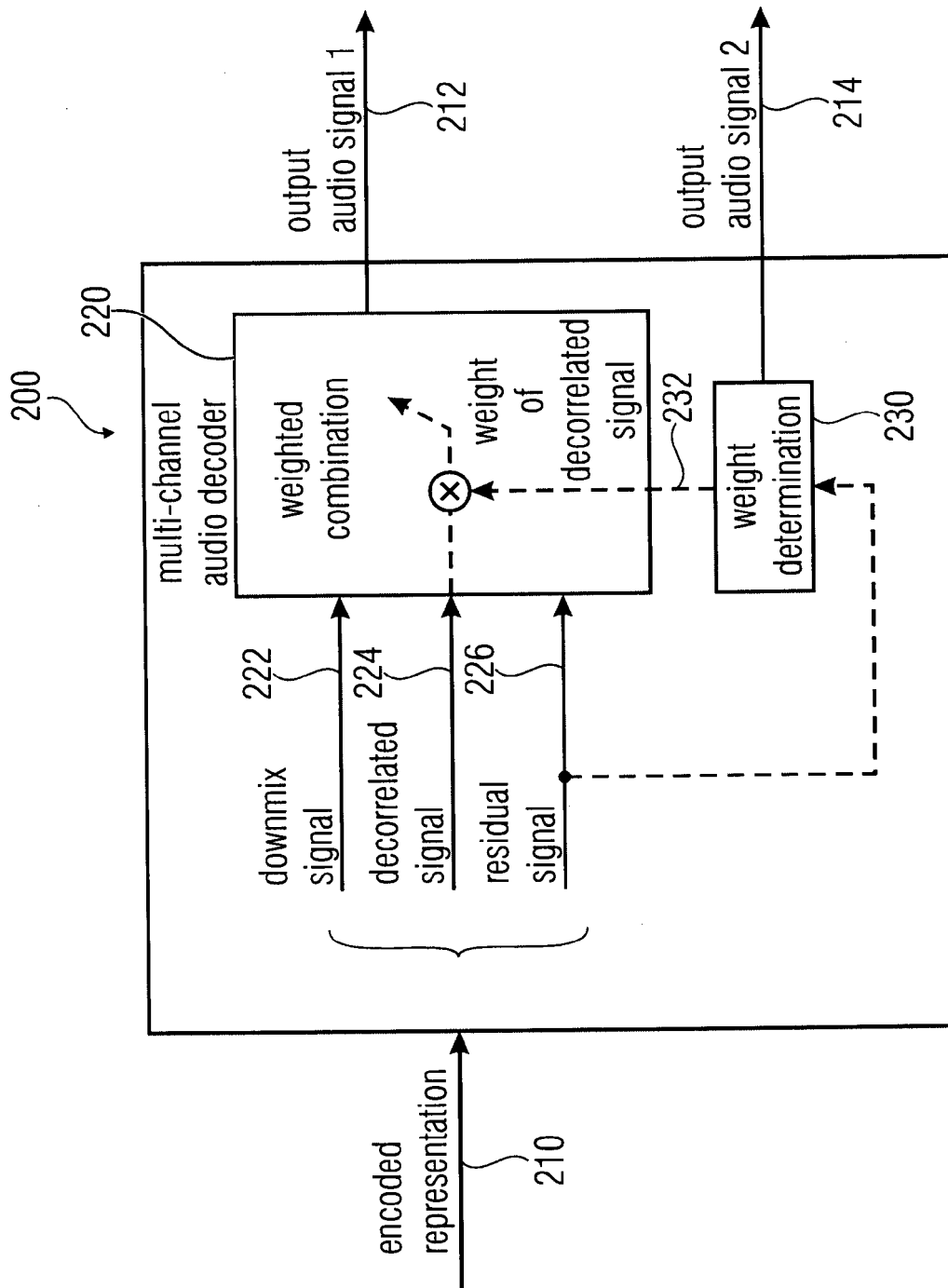


FIG 2

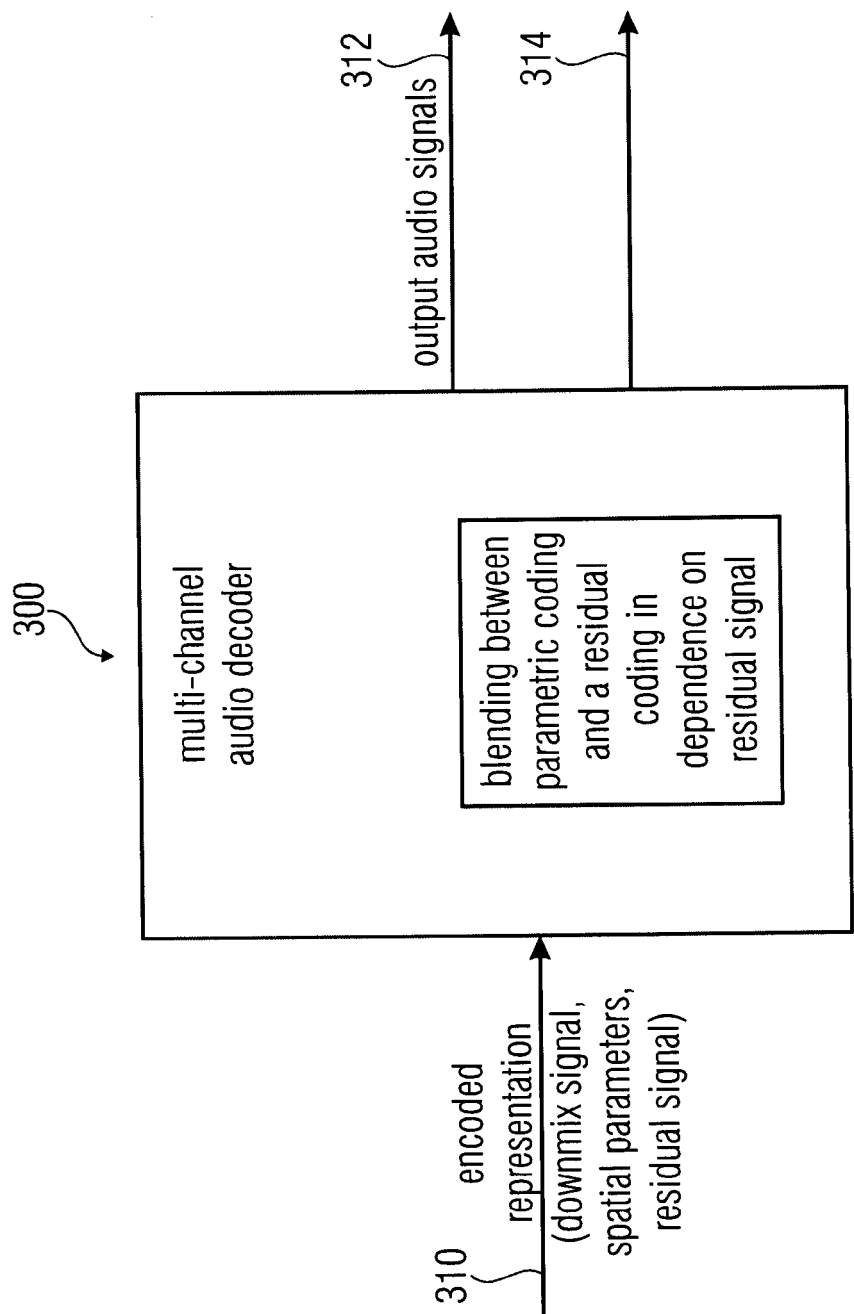


FIG 3

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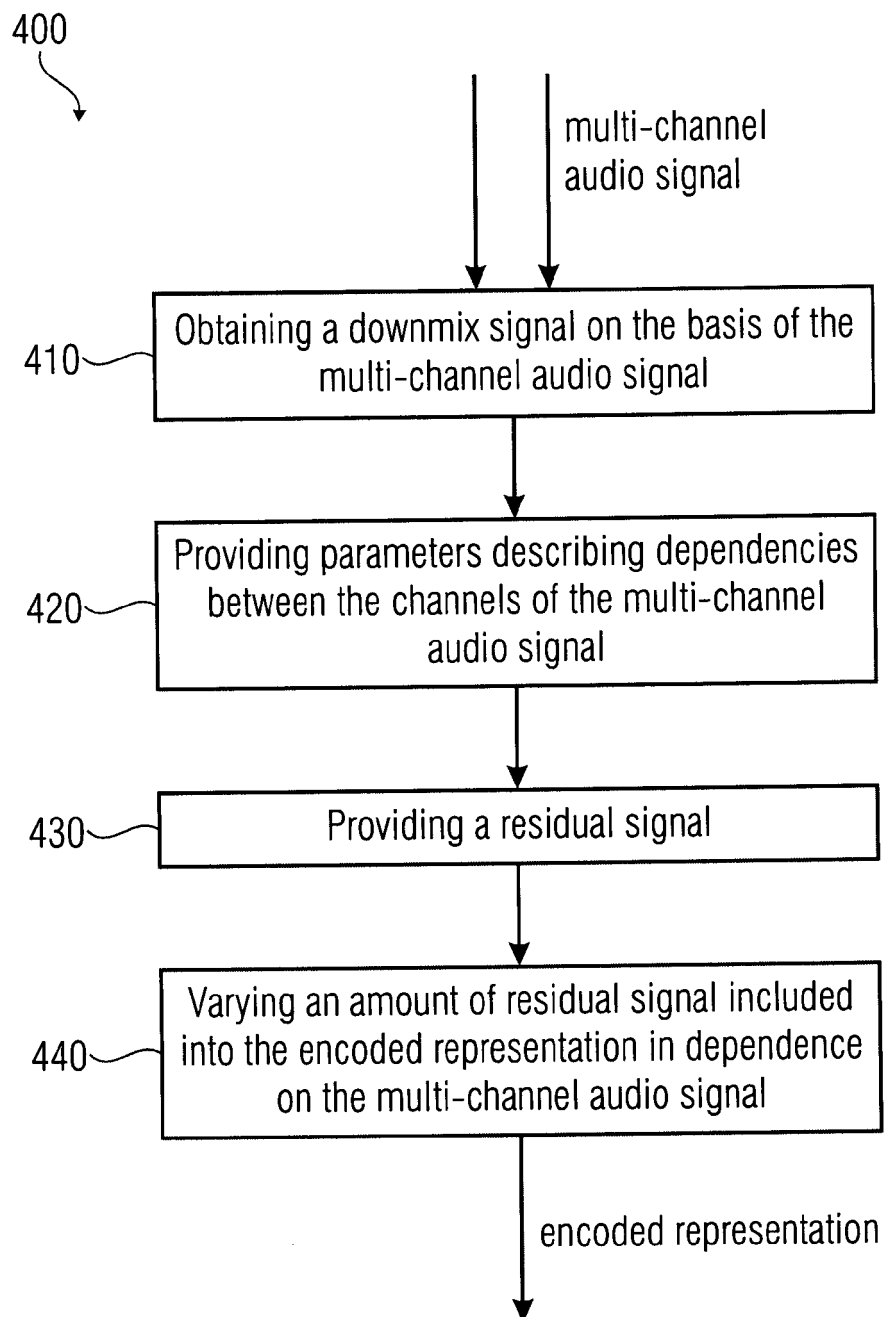


FIG 4

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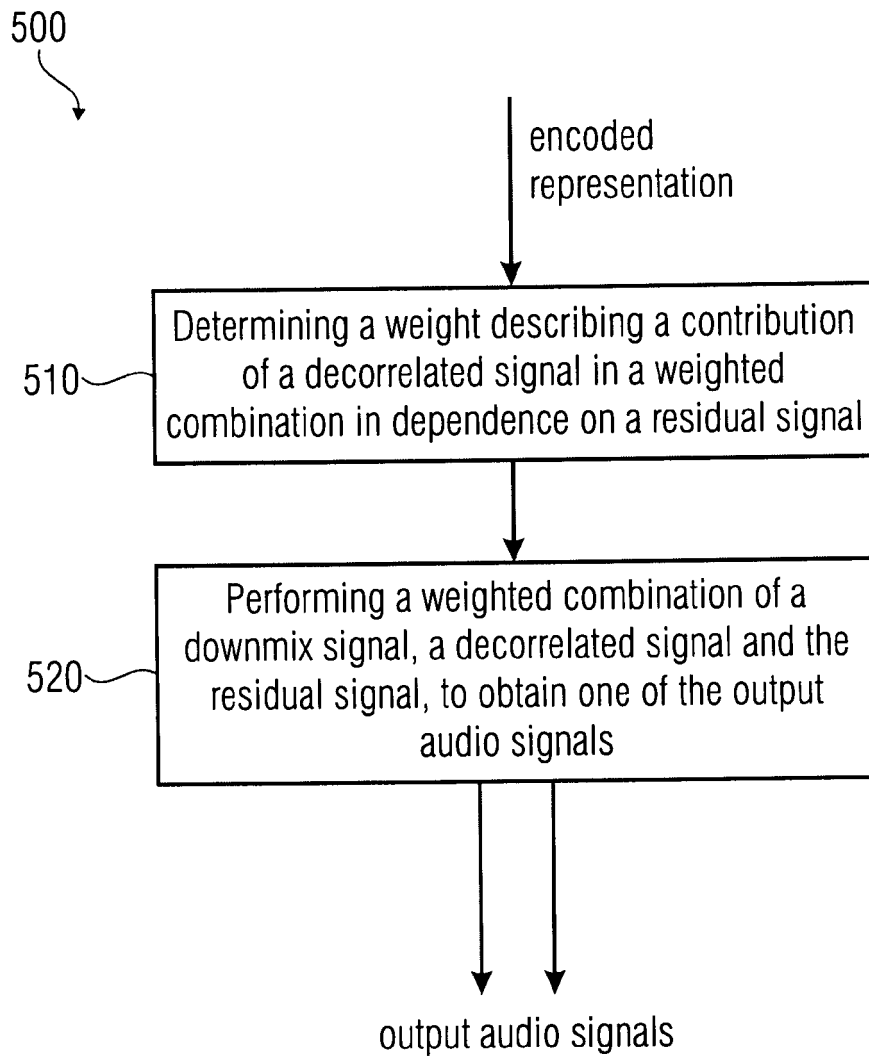


FIG 5

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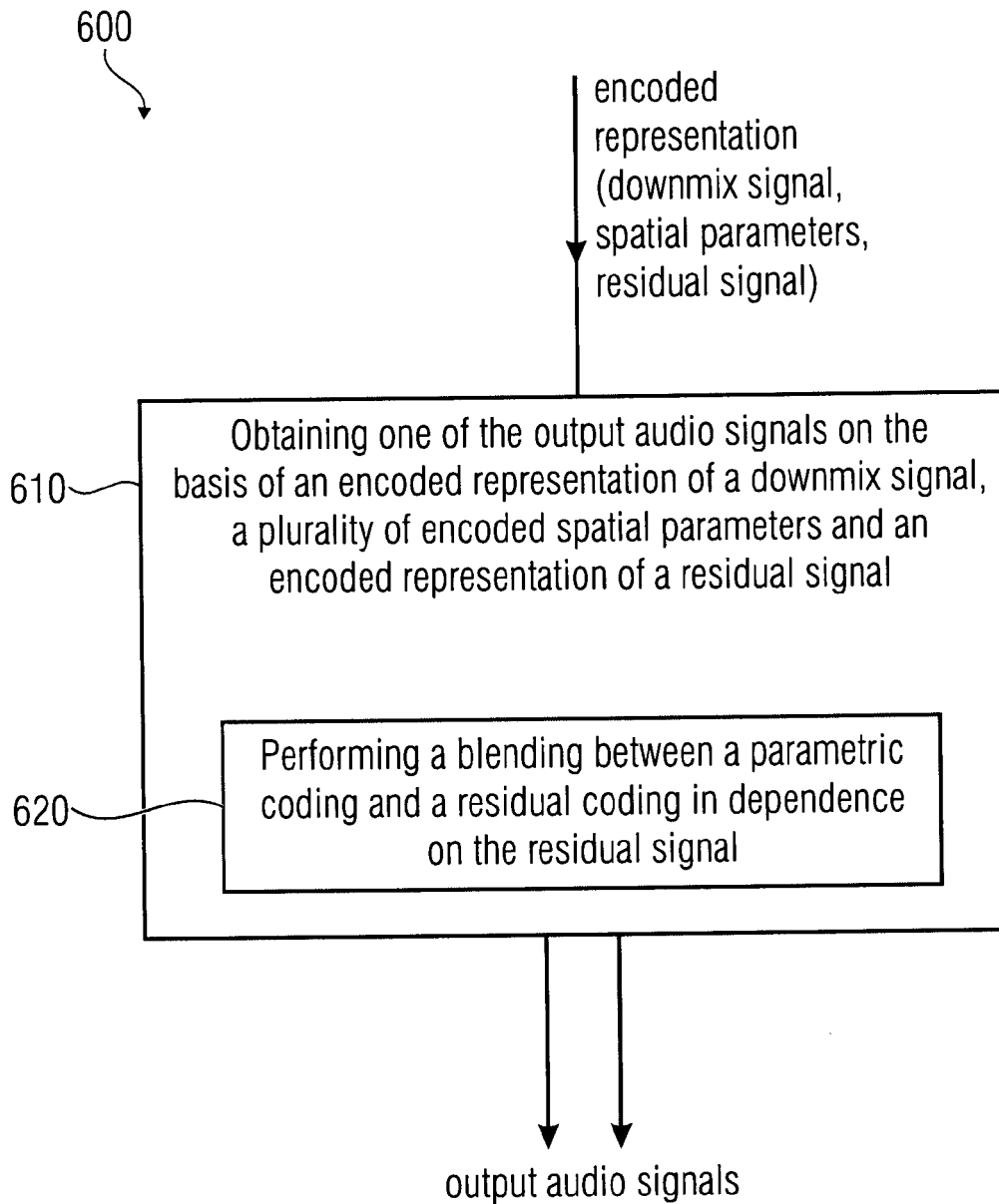
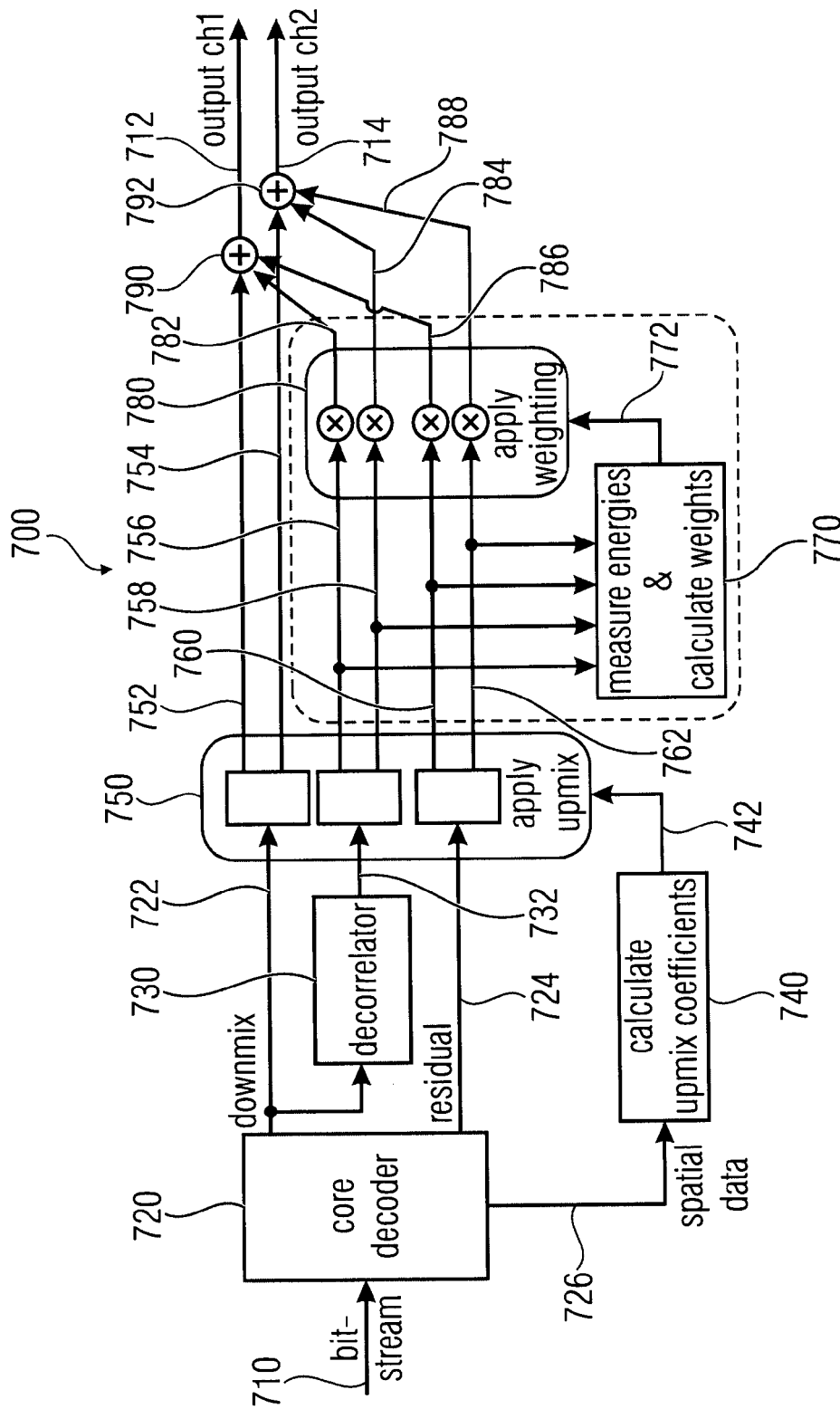


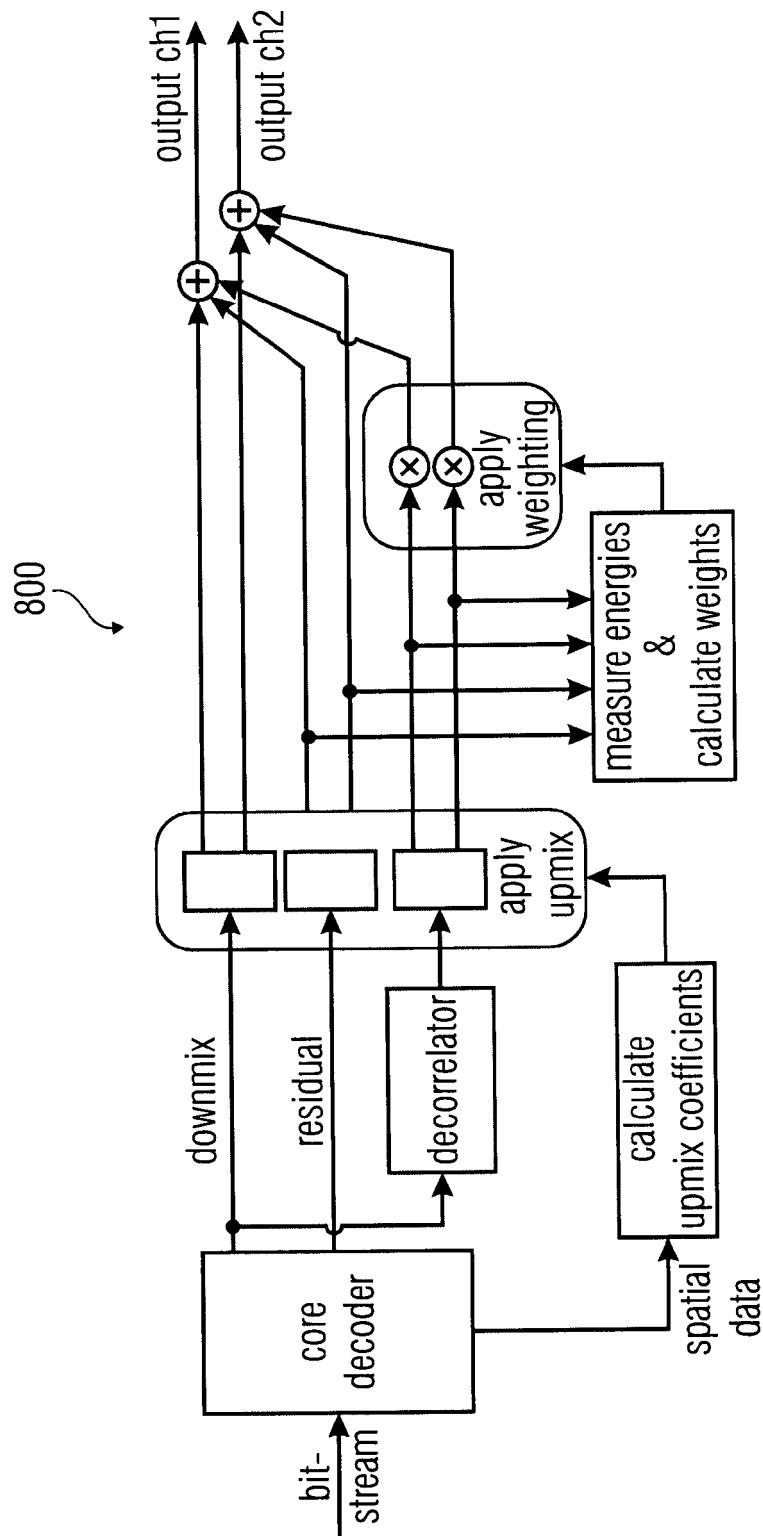
FIG 6

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FLOW DIAGRAM OF DECODER
FIG 7

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SCHEMATICS OF HYBRID RESIDUAL DECODER

FIG 8