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(54) Title: CIRCUIT ARRANGEMENT WITH A FILTER QUADRIPOLE		
(57) Abstract A description is given of a circuit arrangement comprising a filter quadripole having two output terminals; and a voltage follower circuit having two terminals, said terminals having identical electric potentials when the voltage follower circuit is in its turned-on state, each of the terminals of the voltage follower circuit being connected to one of the output terminals of the filter quadripole respectively, and the voltage follower circuit only being in its turned-on state during the turn-on time interval of the circuit arrangement. This circuit arrangement is suitable for a receiver circuit, more particularly, for a pager and has a very brief turn-on time.		