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(71) Applicant (for all designated States except US): **LAM RESEARCH CORPORATION** [US/US]; 4650 Cushing Parkway, Fremont, CA 94538 (US).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **PATRICK, Roger** [US/US]; 102 Whit's Road, Mountain View, CA 94040 (US). **RICHARDSON, Brett** [US/US]; 3709 Tortosa Court, San Ramon, CA 94583 (US). **WILLIAMS, Norman** [US/US]; 39838 Portrero Drive, Newark, CA 94560 (US).

(74) Agent: **NGUYEN, Joseph, A.**; 10121 Miller Avenue, Suite 201, Cupertino, CA 95014-3469 (US).

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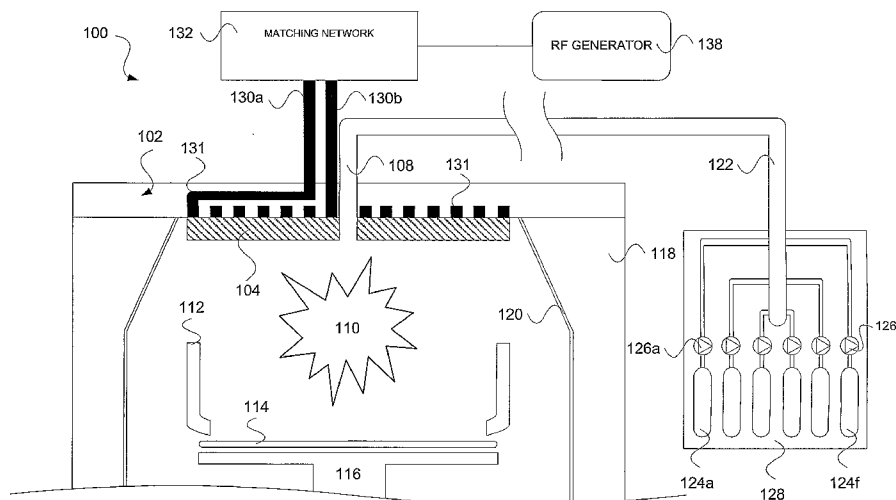
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(54) Title: METHODS AND APPARATUS FOR OPTIMIZING A SUBSTRATE IN A PLASMA PROCESSING SYSTEM



(57) Abstract: An arrangement for processing a semiconductor substrate in a plasma processing system is disclosed. The arrangement includes providing a RF coupling structure having a first terminal and a second terminal, the first terminal being coupled with a first electrical measuring device, the second terminal being coupled with a second electrical measuring device. The arrangement also includes coupling a compensating circuit to the second terminal. The arrangement further includes providing a feedback circuit coupled to receive information from the first electrical measuring device and the second electrical measuring device, an output of the feedback circuit being employed to control the compensating circuit in order to keep a ratio between a first electrical value at the first terminal and a second electrical value at the second terminal substantially at a predefined value.

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