05 DEC 2006

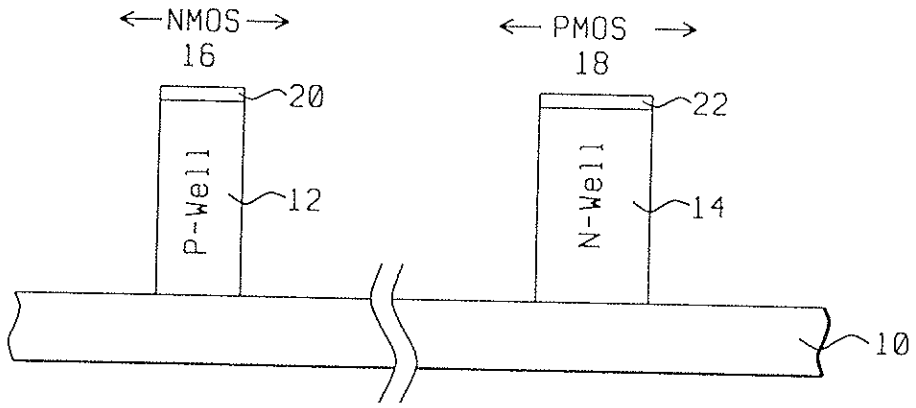


FIG. 1

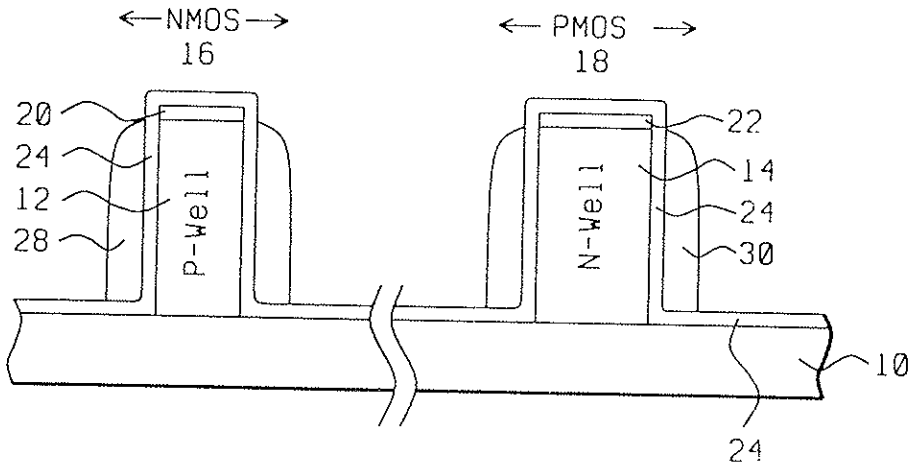


FIG. 2

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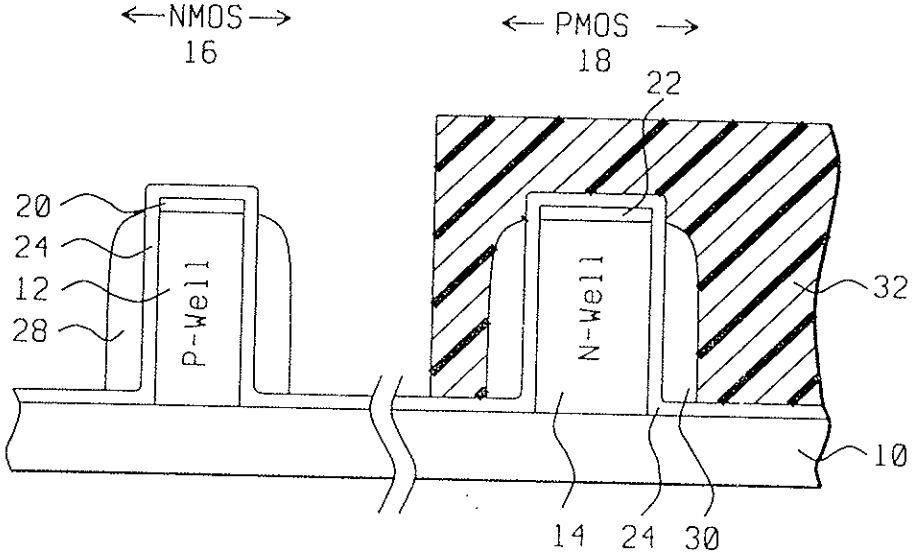


FIG. 3

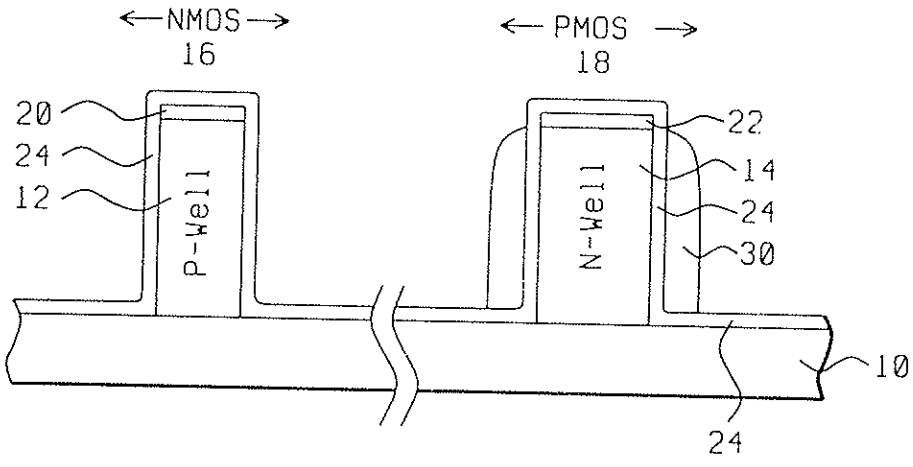


FIG. 4

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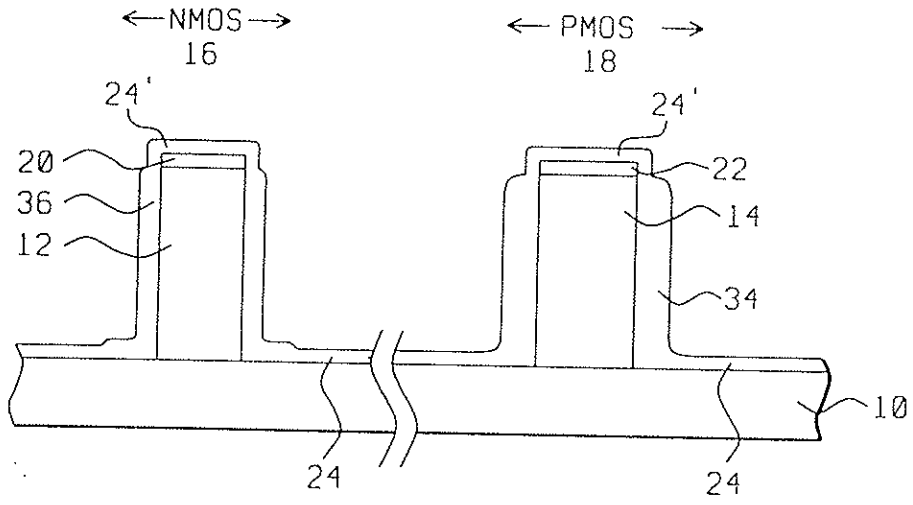


FIG. 5

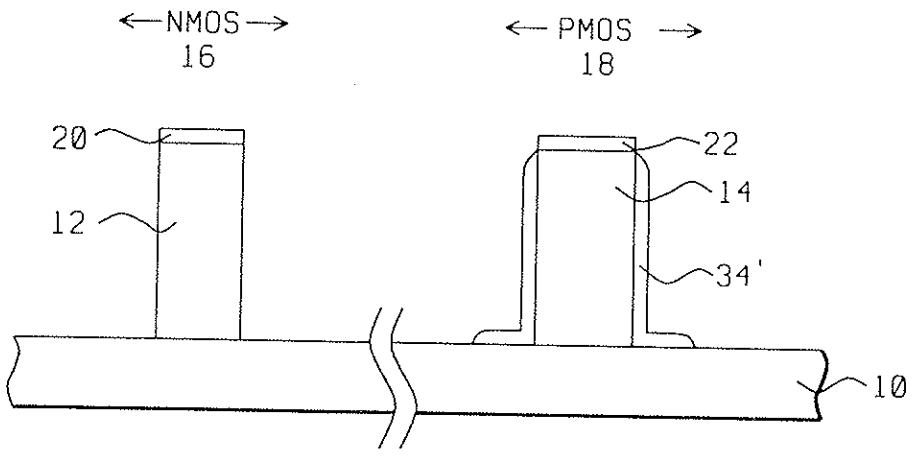


FIG. 6

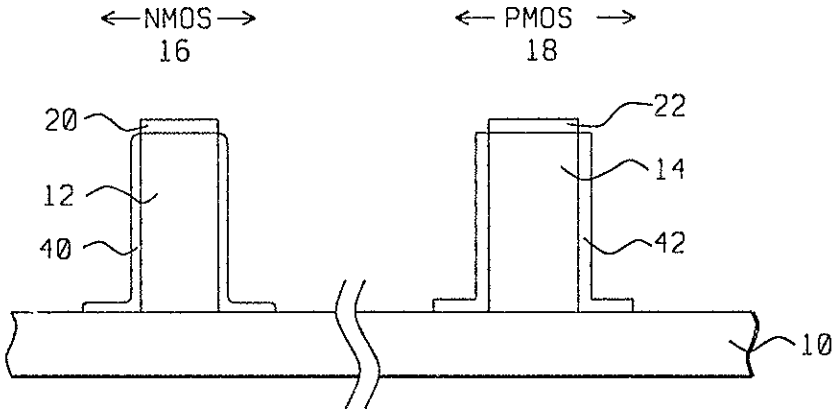

←NMOS→
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← PMOS →
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42
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FIG. 7

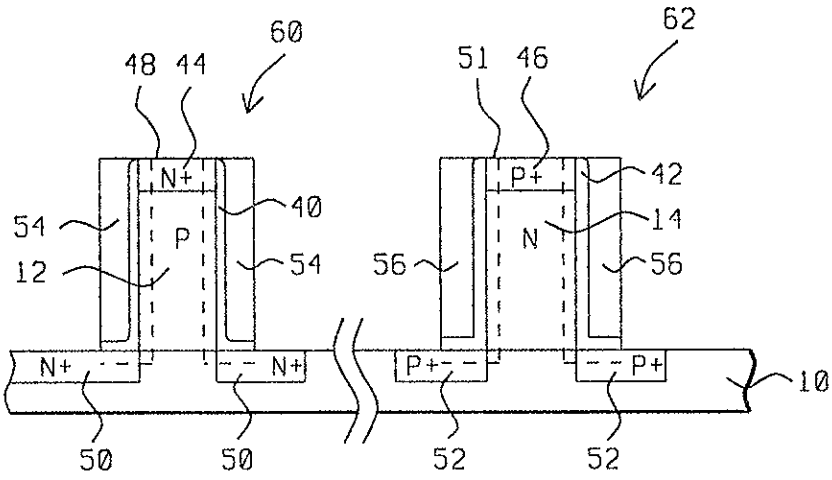

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P
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P+
P+
10
50
50
52
52

FIG. 8