ABSTRACT OF THE DISCLOSURE

METHODS FOR MAKING SEMICONDUCTOR PACKAGES WITH LEADFRAME GRID ARRAYS

Methods of forming a semiconductor assembly includes a leadframe with leads having offset portions exposed at an outer surface of a material package to form a grid array. An electrically conductive compound, such as solder, may be disposed or formed on the exposed lead portions to form a grid array such as a ball grid array ("BGA") or other similar array-type structure of dielectric conductive elements. The leads may have inner bond ends including a contact pad thermocompressively bonded to a bond pad of the semiconductor chip to enable electrical communication therewith and a lead section with increased flexibility to improve the thermocompressive bond. The inner bond ends may also be wirebonded to the bond pads.

[FIG. 1]