

FIG. 1

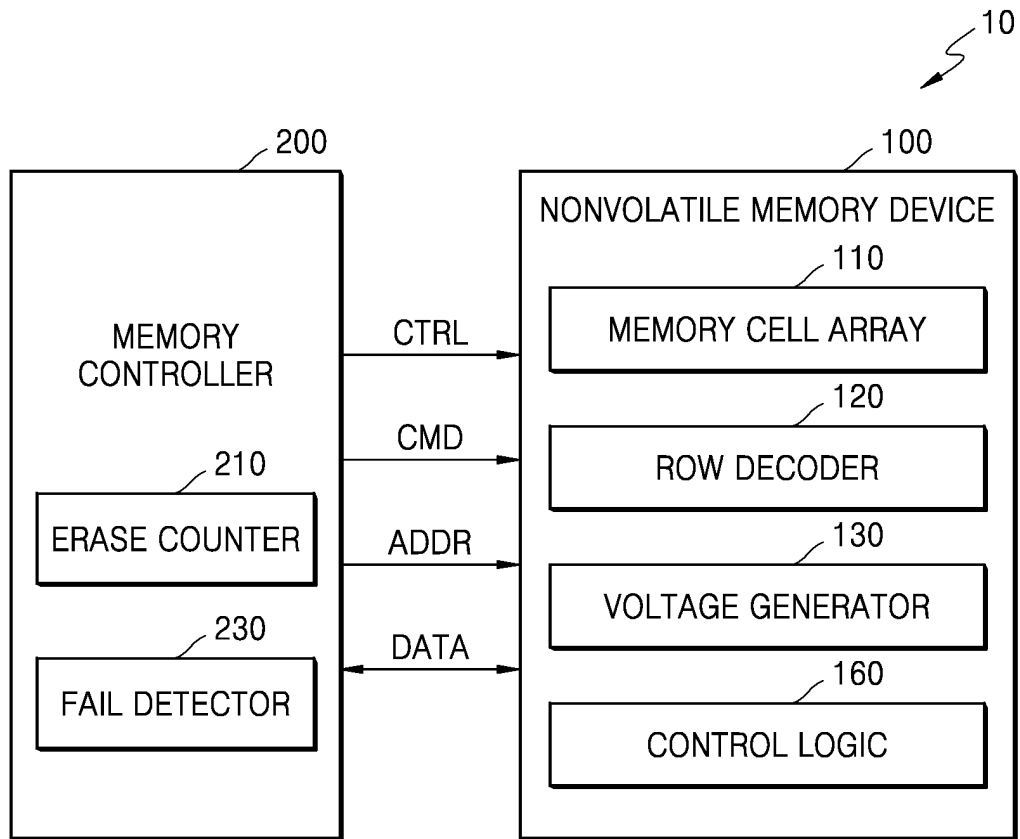


FIG. 2

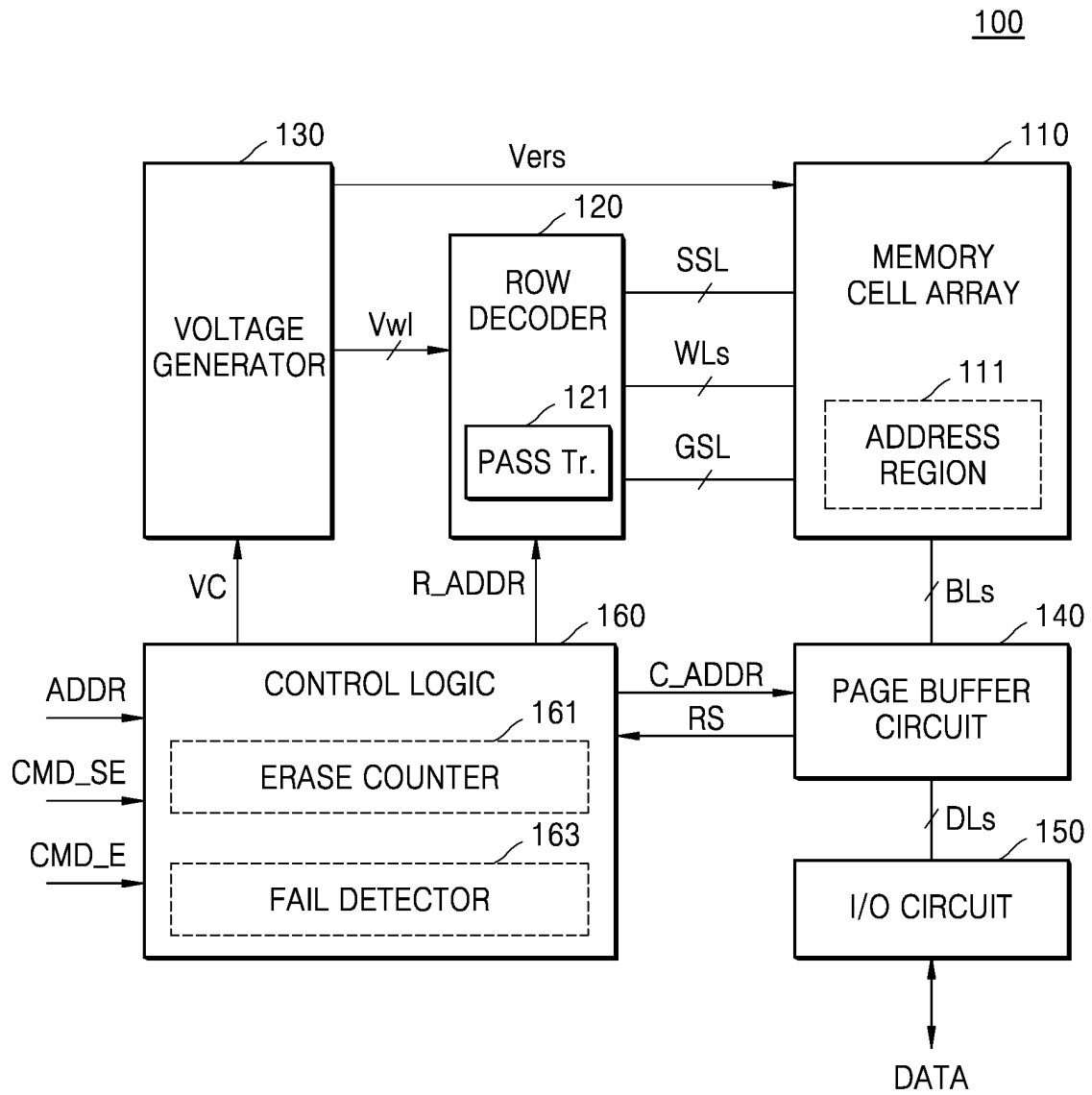


FIG. 3

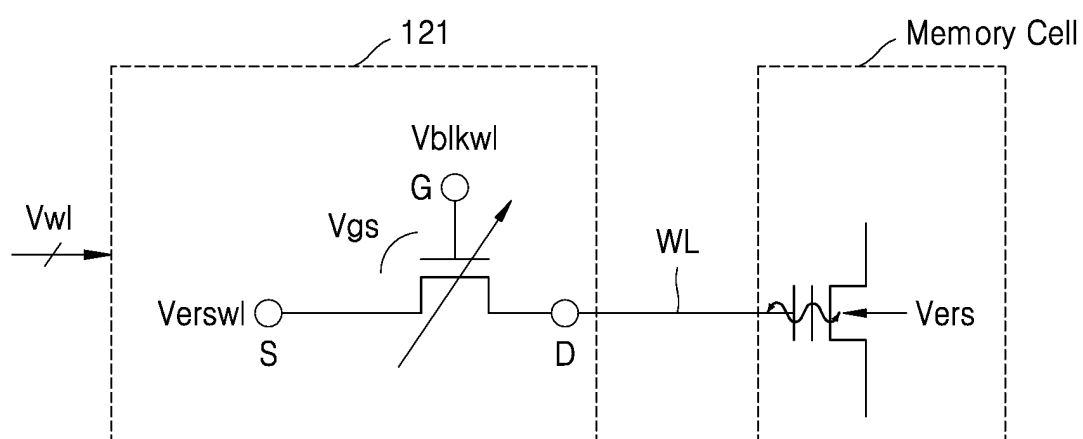


FIG. 4A

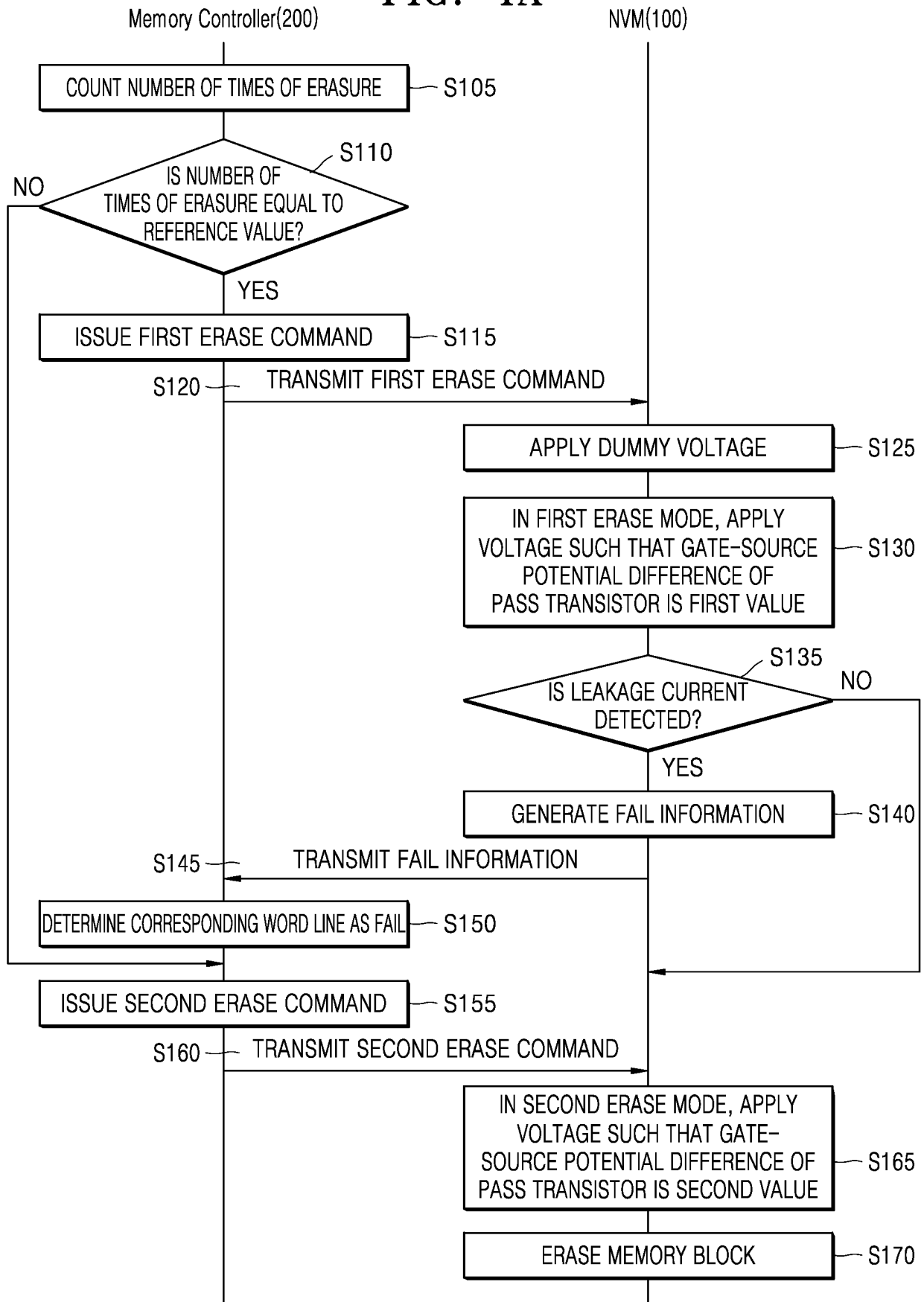


FIG. 4B

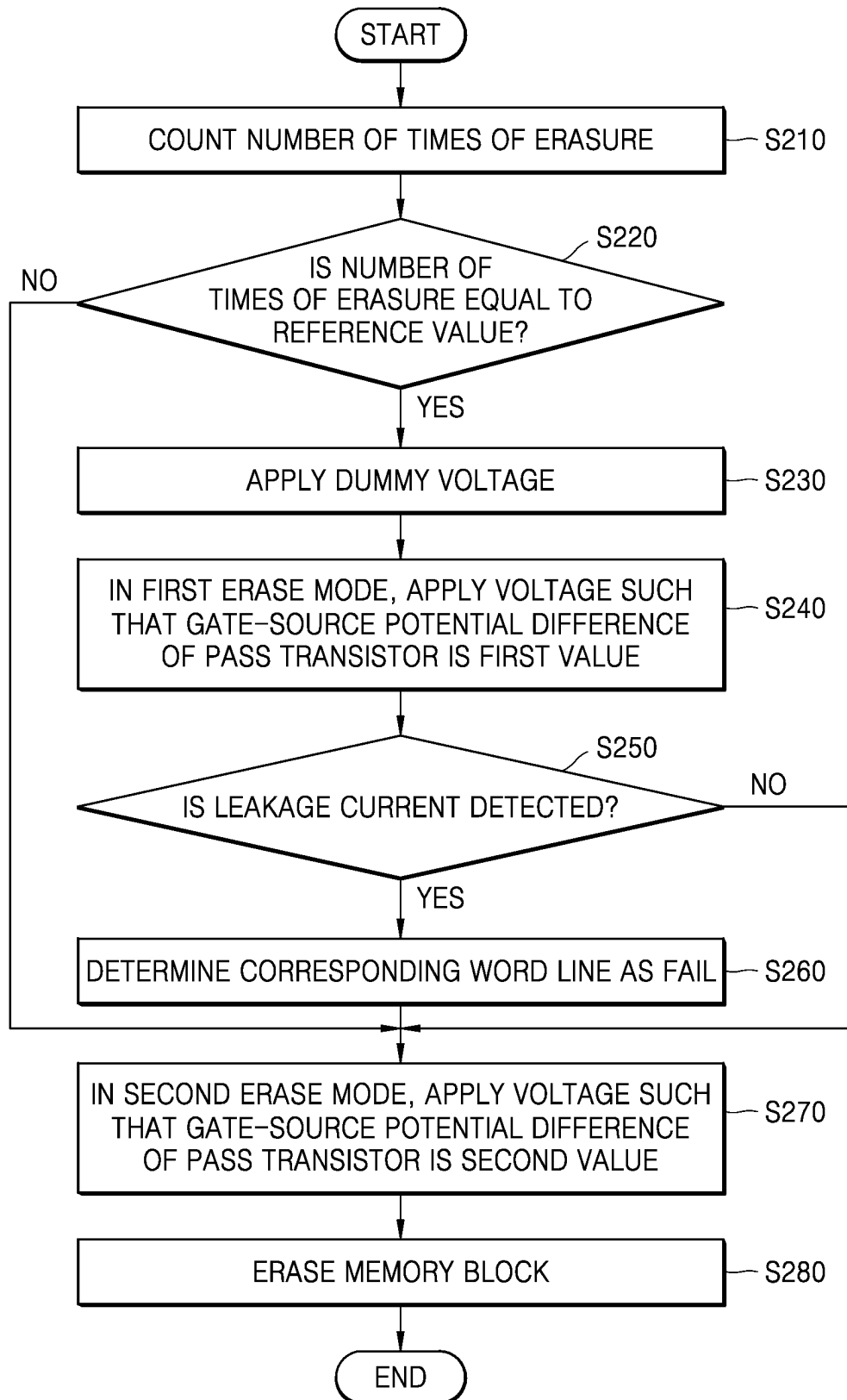


FIG. 5

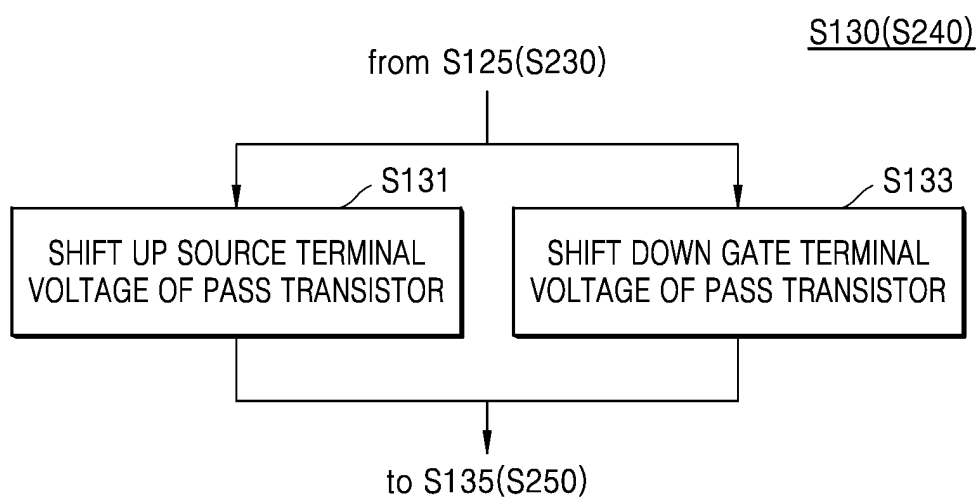


FIG. 6

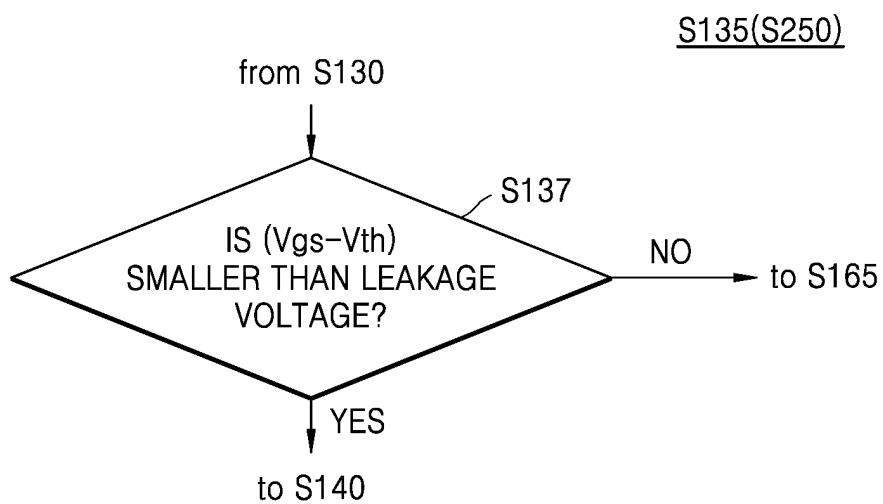


FIG. 7

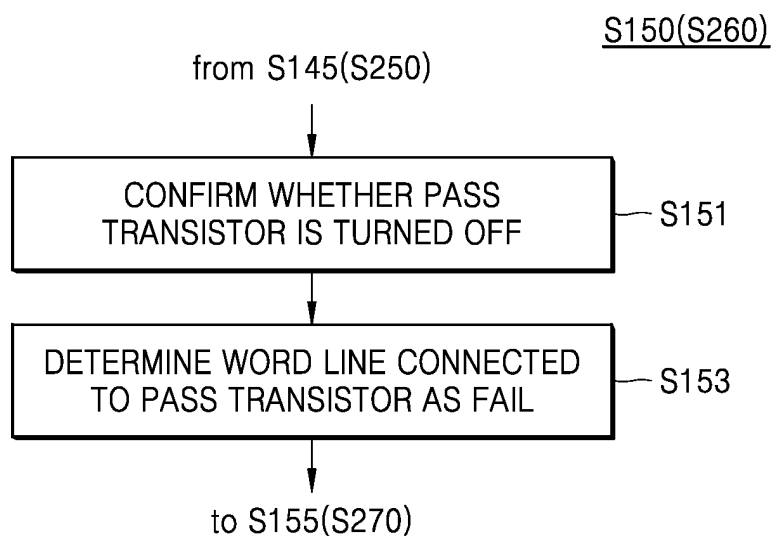


FIG. 8

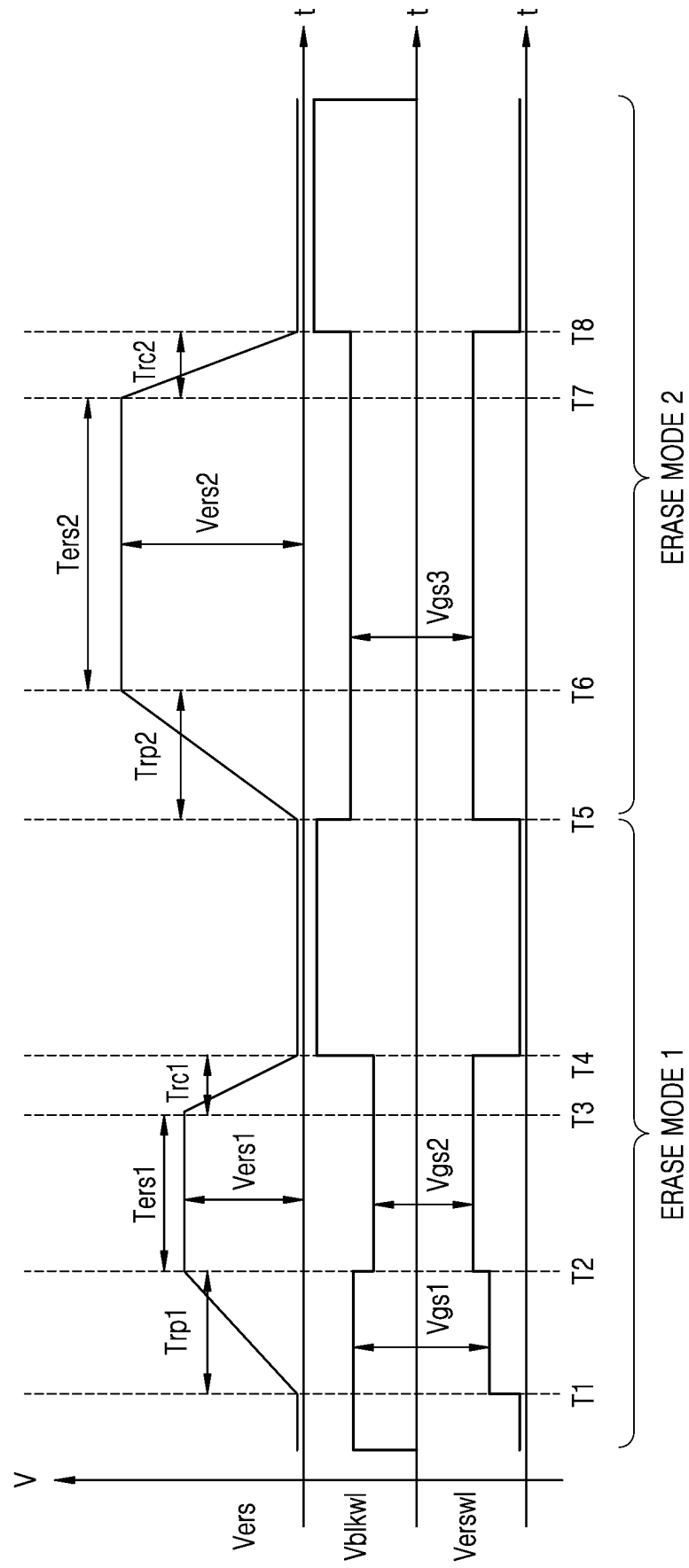


FIG. 9

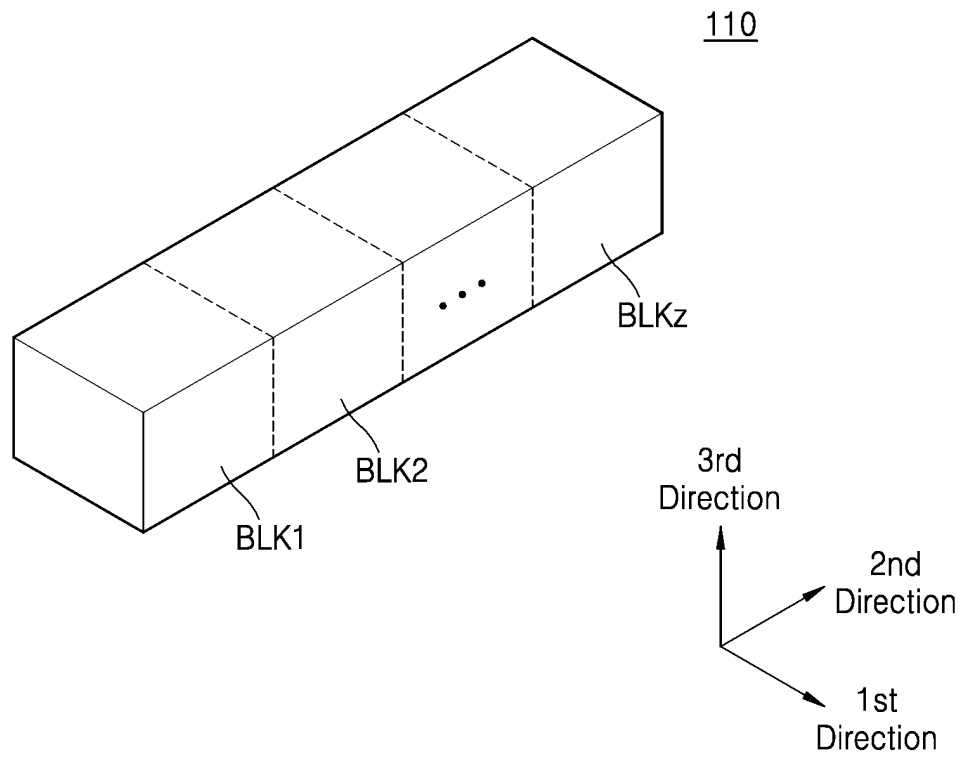


FIG. 10A

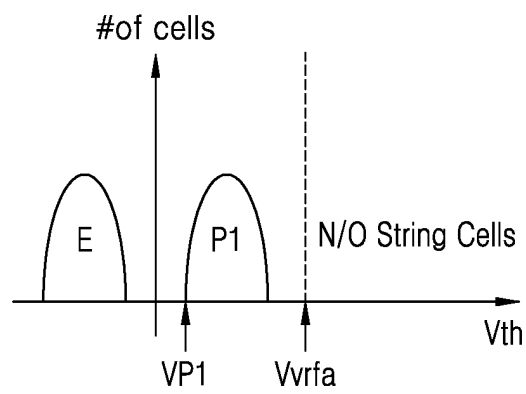


FIG. 10B

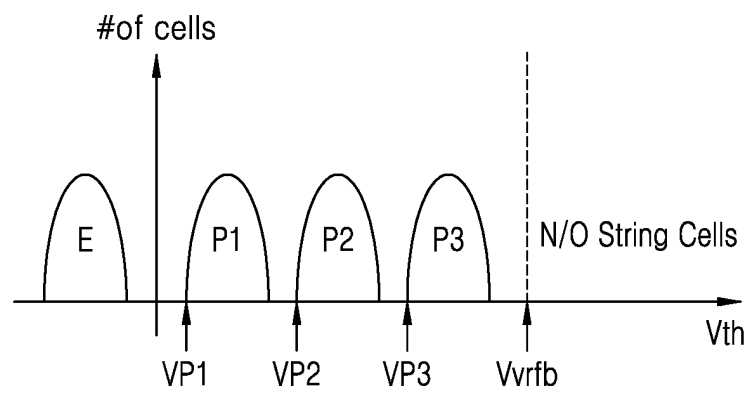


FIG. 10C

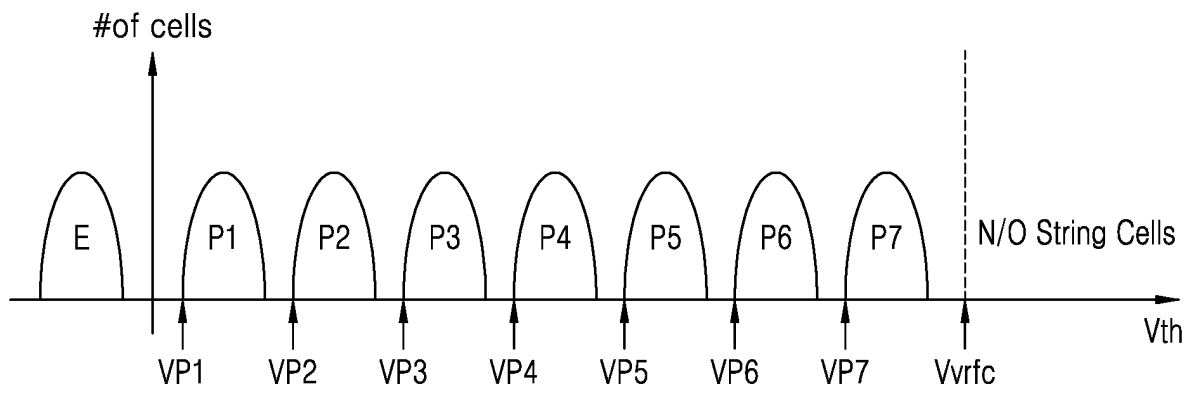


FIG. 11

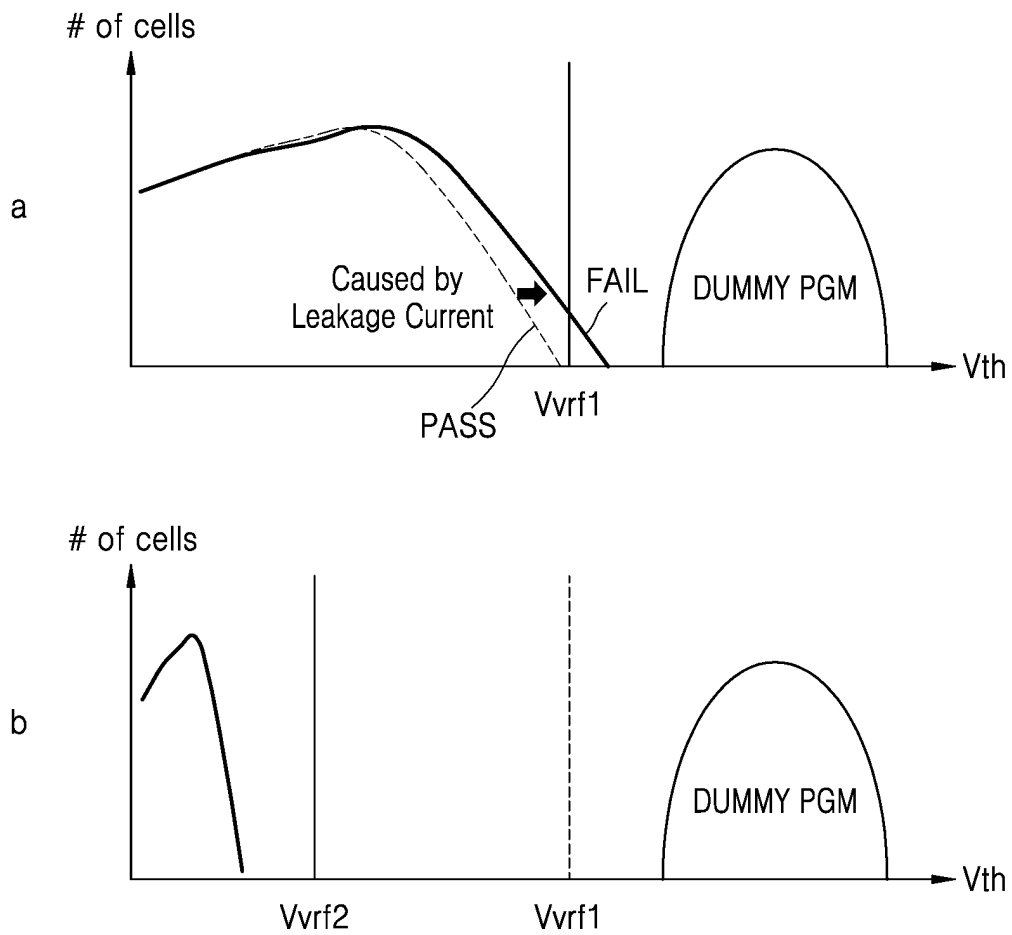


FIG. 12

	1 st Erase mode	2 nd Erase mode	DECISION
Case1	PASS	PASS	PASS
Case2	PASS	FAIL	UECC
Case3	FAIL	–	BAD BLOCK

FIG. 13A

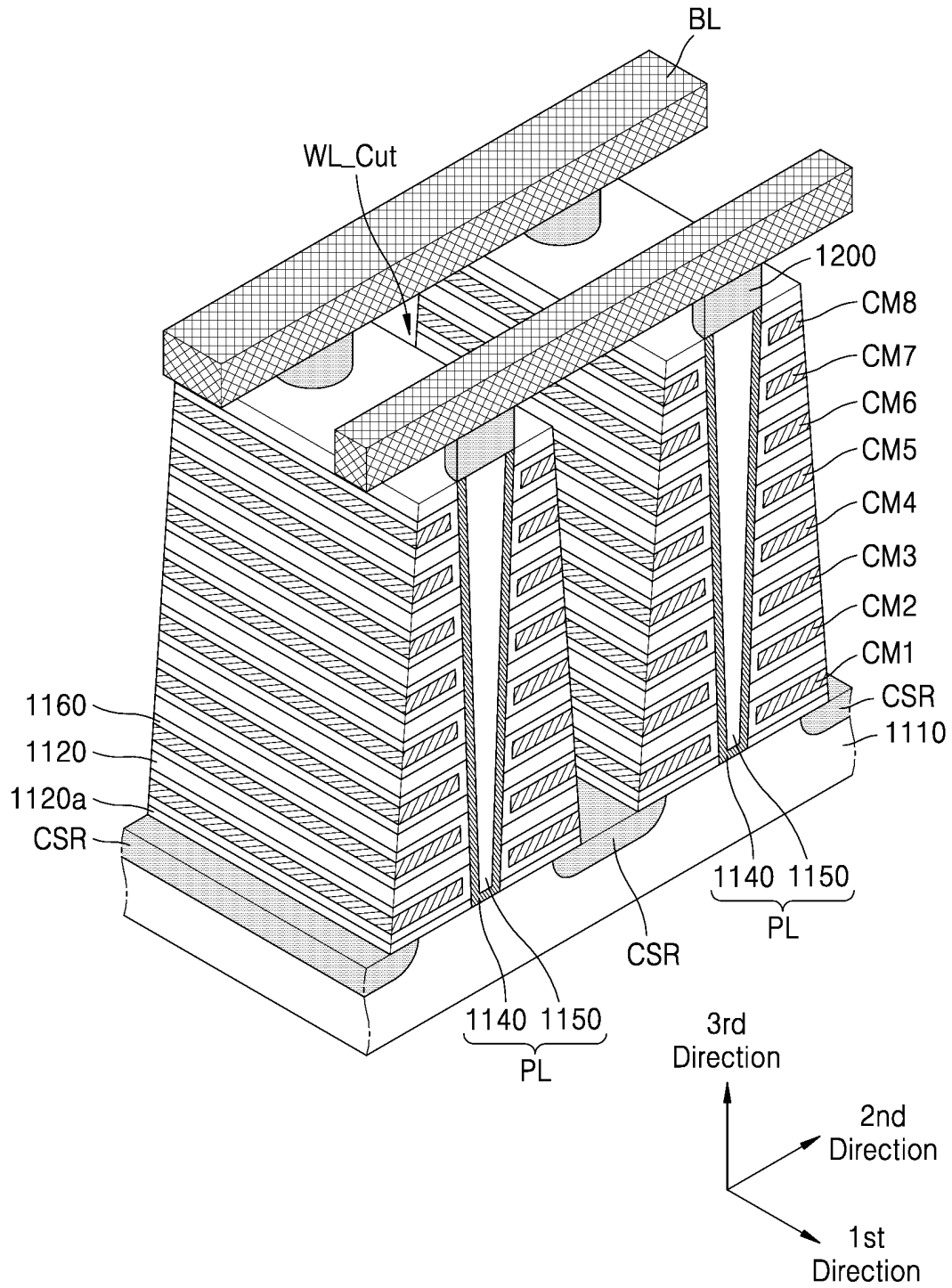


FIG. 13B

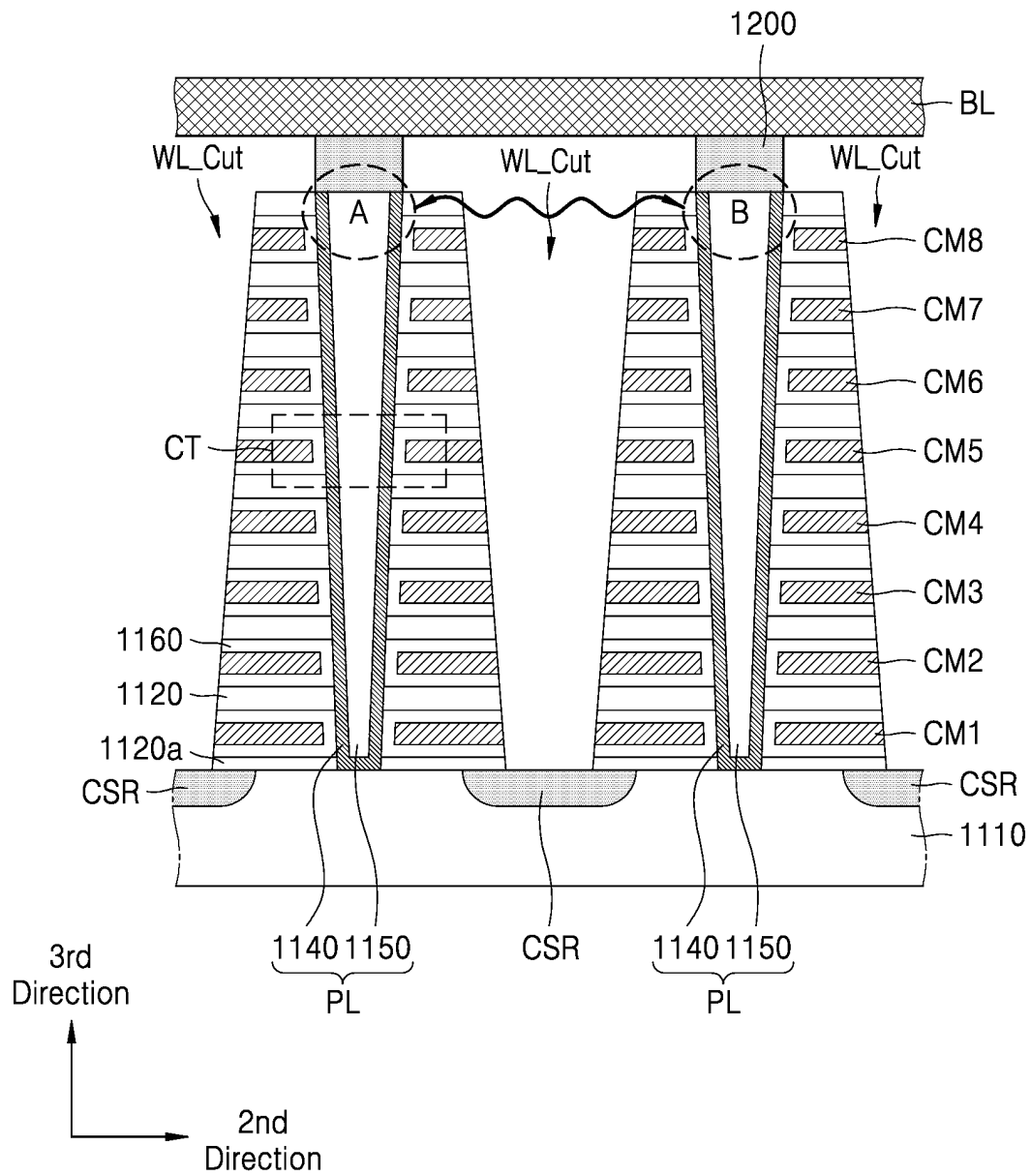


FIG. 14

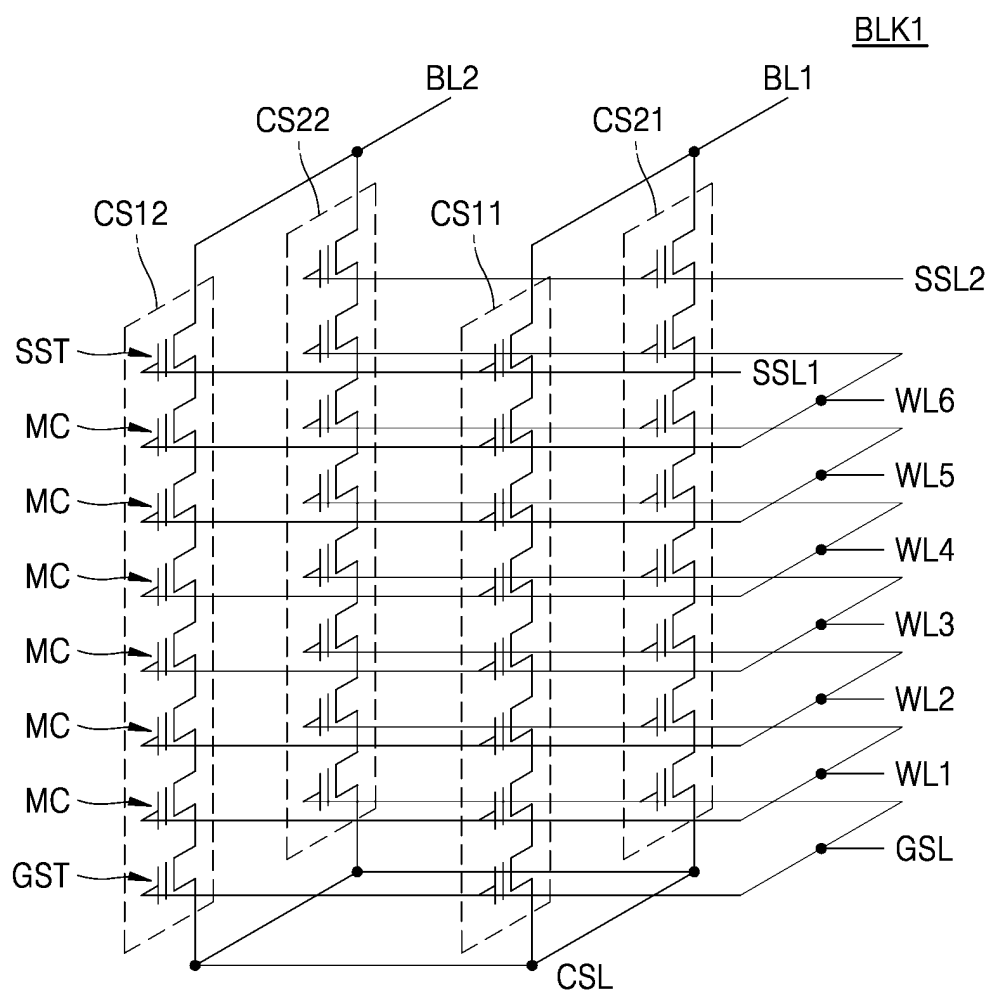


FIG. 15

