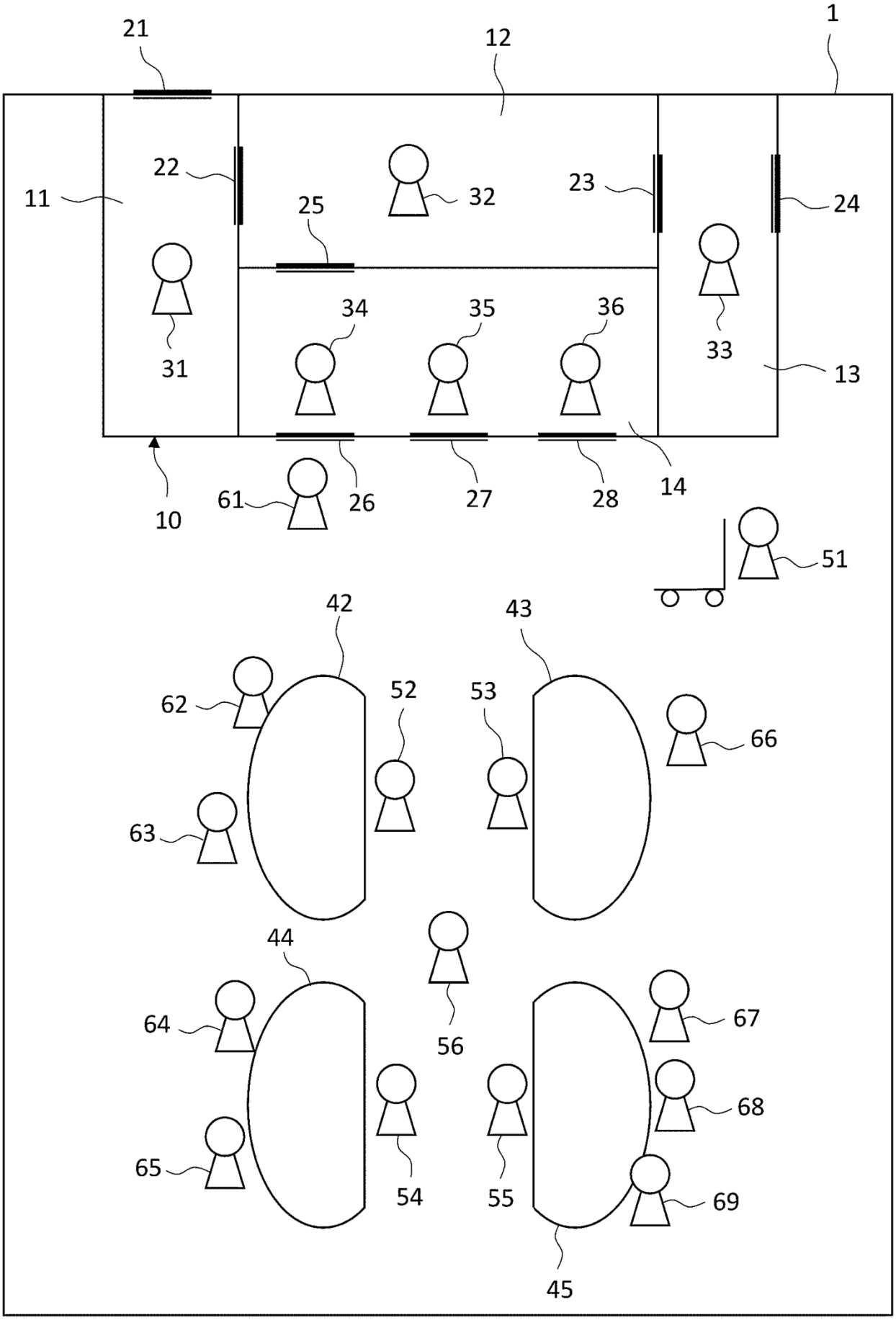

21
12
1
22
11
25
32
23
24
31
34
35
36
33
13
10
61
26
27
28
14
51
42
43
62
52
53
66
63
44
56
64
67
68
65
54
55
69
45

FIG.1

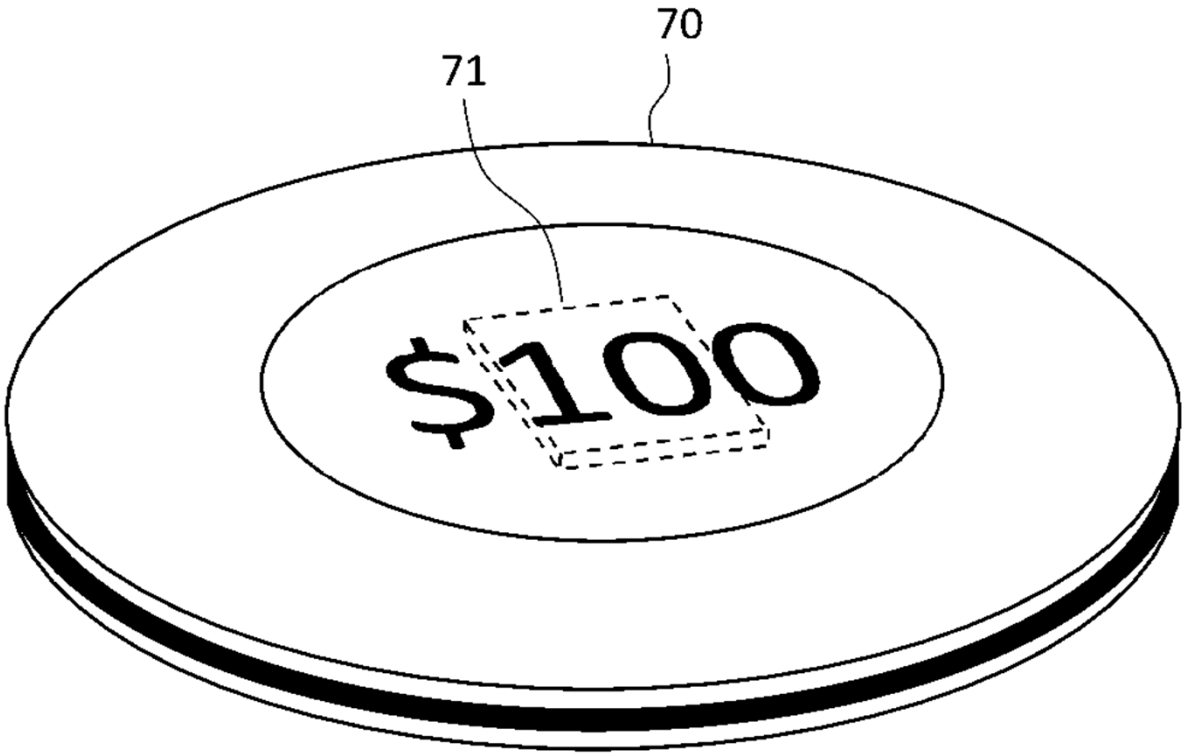


FIG.2

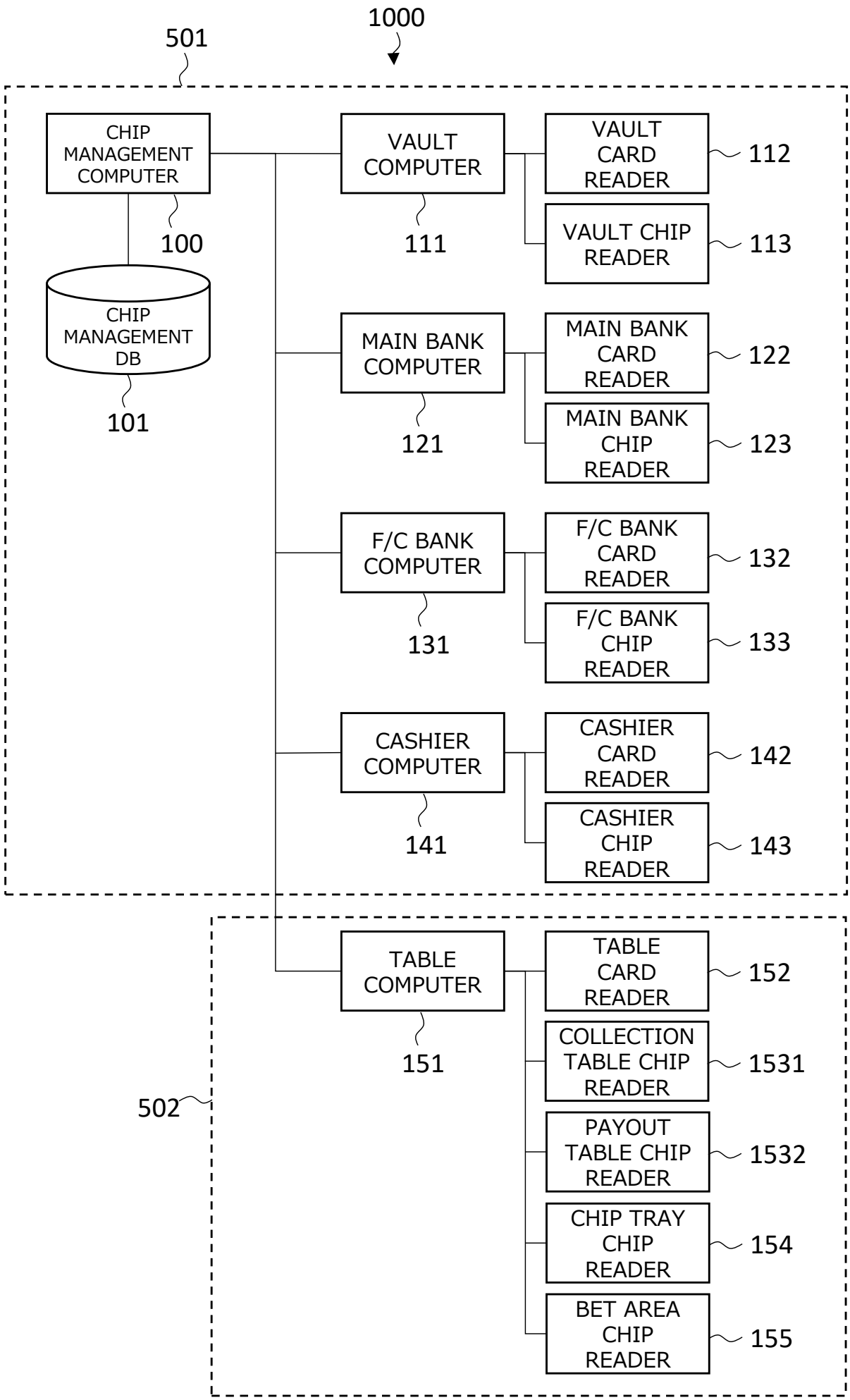

1000
501
CHIP MANAGEMENT COMPUTER
100
CHIP MANAGEMENT DB
101
VAULT COMPUTER
111
VAULT CARD READER
112
VAULT CHIP READER
113
MAIN BANK COMPUTER
121
MAIN BANK CARD READER
122
MAIN BANK CHIP READER
123
F/C BANK COMPUTER
131
F/C BANK CARD READER
132
F/C BANK CHIP READER
133
CASHIER COMPUTER
141
CASHIER CARD READER
142
CASHIER CHIP READER
143
502
TABLE COMPUTER
151
TABLE CARD READER
152
COLLECTION TABLE CHIP READER
1531
PAYOUT TABLE CHIP READER
1532
CHIP TRAY CHIP READER
154
BET AREA CHIP READER
155

FIG.3

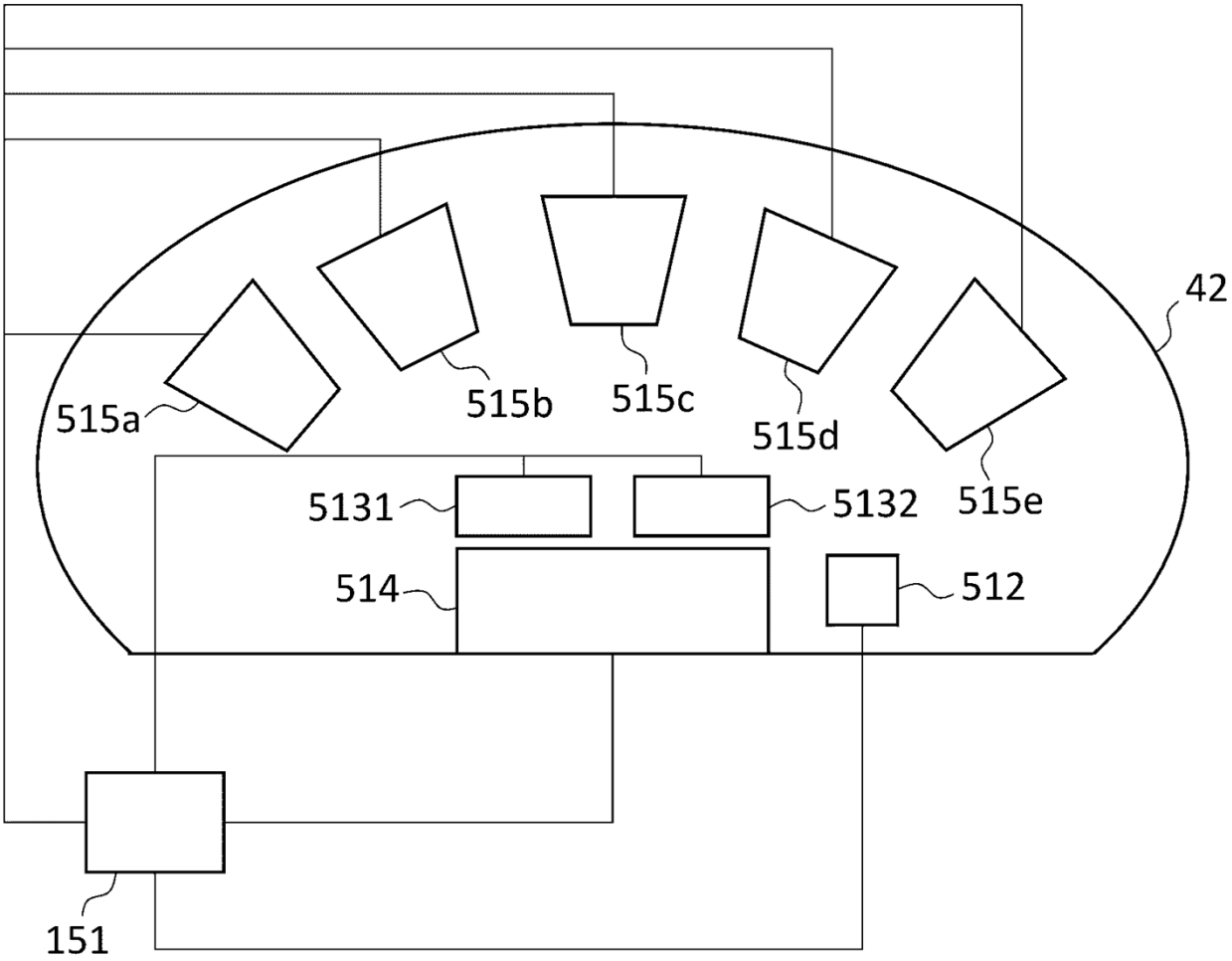


FIG.4

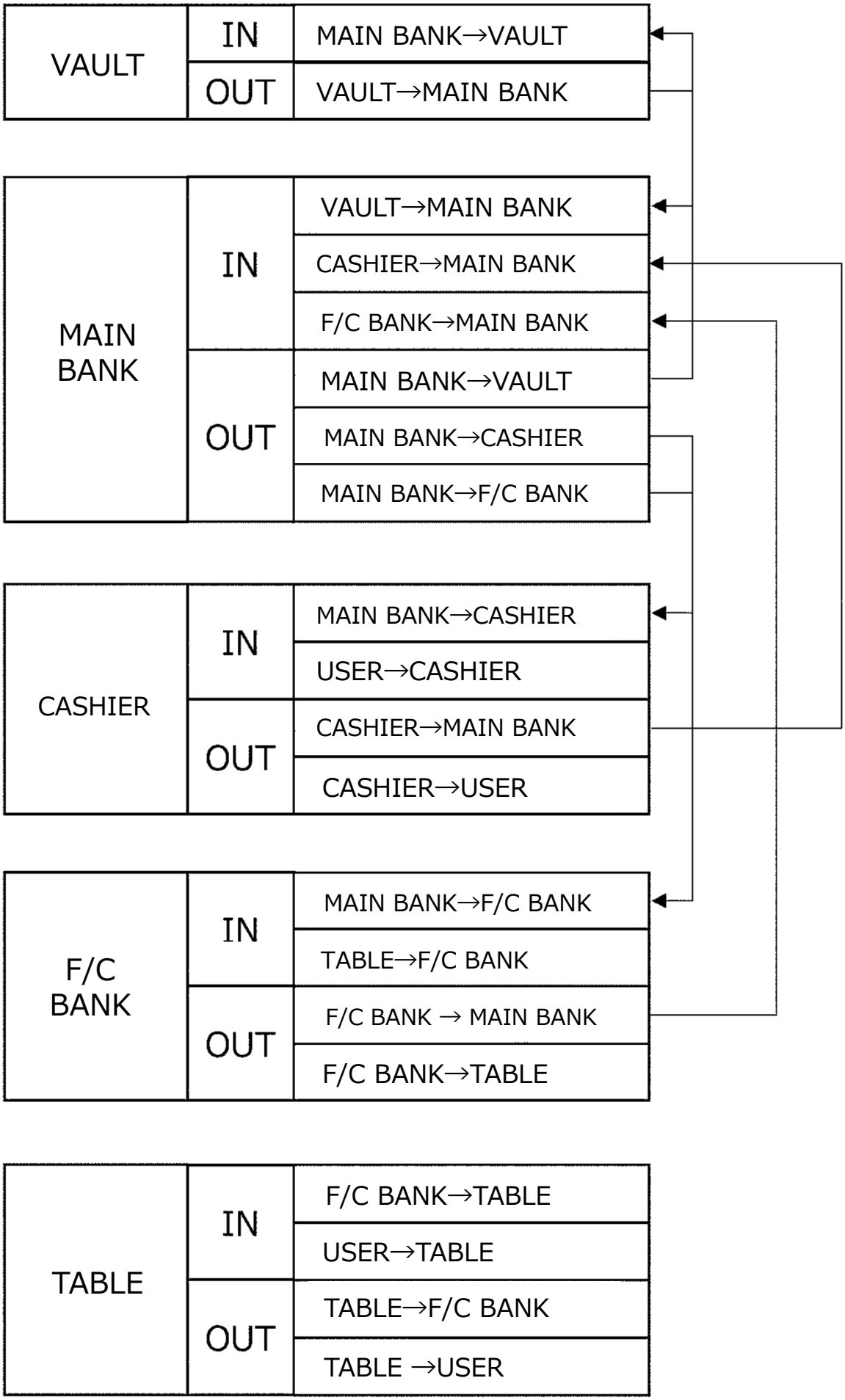

VAULT
IN
MAIN BANK→VAULT
OUT
VAULT→MAIN BANK
MAIN BANK
IN
VAULT→MAIN BANK
CASHIER→MAIN BANK
F/C BANK→MAIN BANK
OUT
MAIN BANK→VAULT
MAIN BANK→CASHIER
MAIN BANK→F/C BANK
CASHIER
IN
MAIN BANK→CASHIER
USER→CASHIER
OUT
CASHIER→MAIN BANK
CASHIER→USER
F/C BANK
IN
MAIN BANK→F/C BANK
TABLE→F/C BANK
OUT
F/C BANK → MAIN BANK
F/C BANK→TABLE
TABLE
IN
F/C BANK→TABLE
USER→TABLE
OUT
TABLE→F/C BANK
TABLE →USER

FIG.5

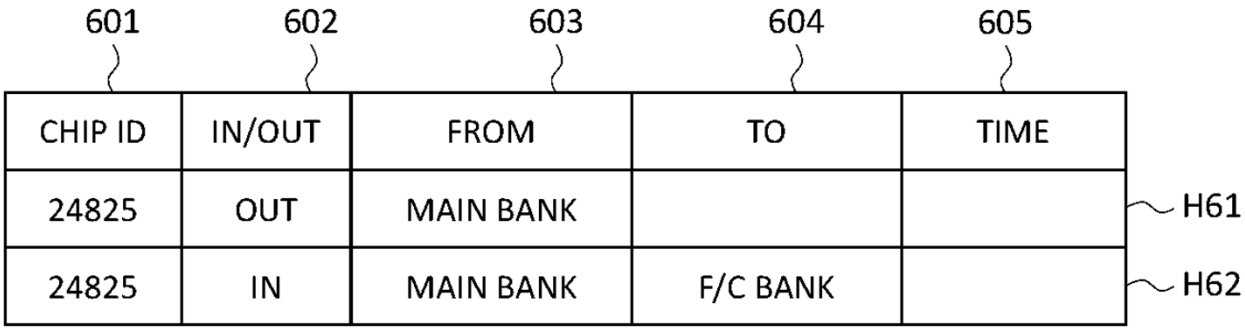


FIG.6

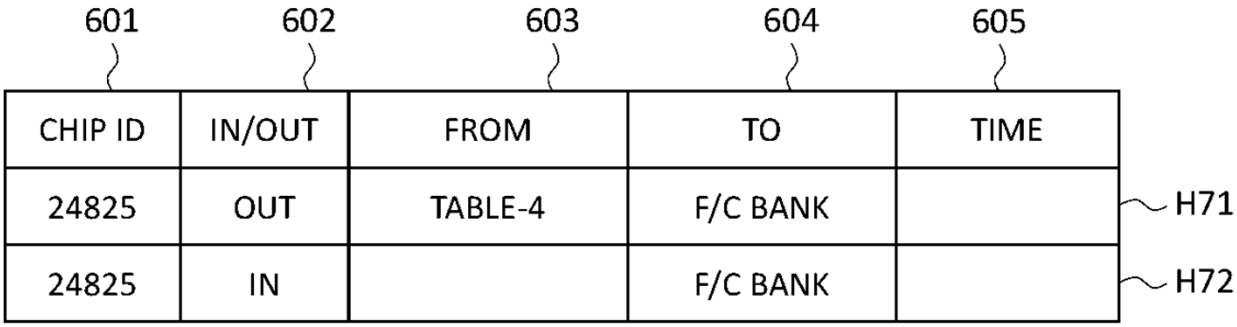


FIG.7

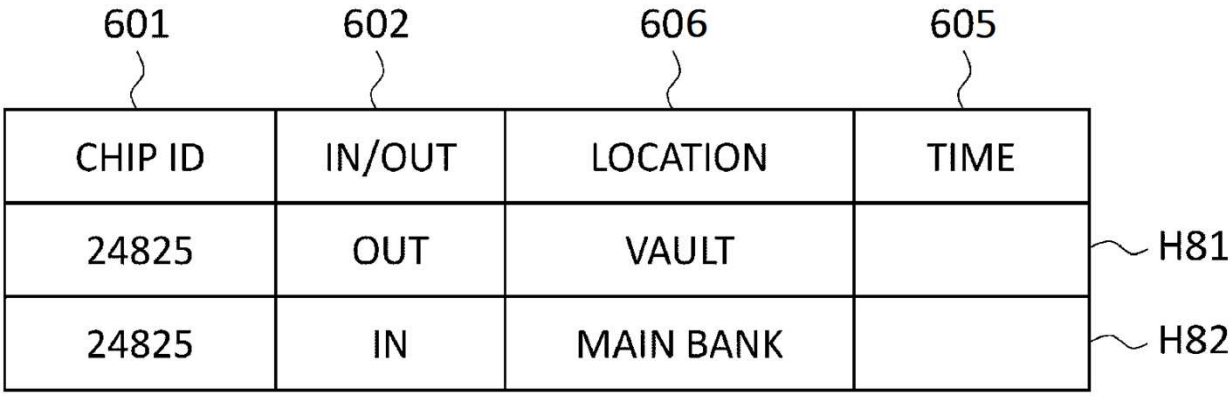


FIG.8

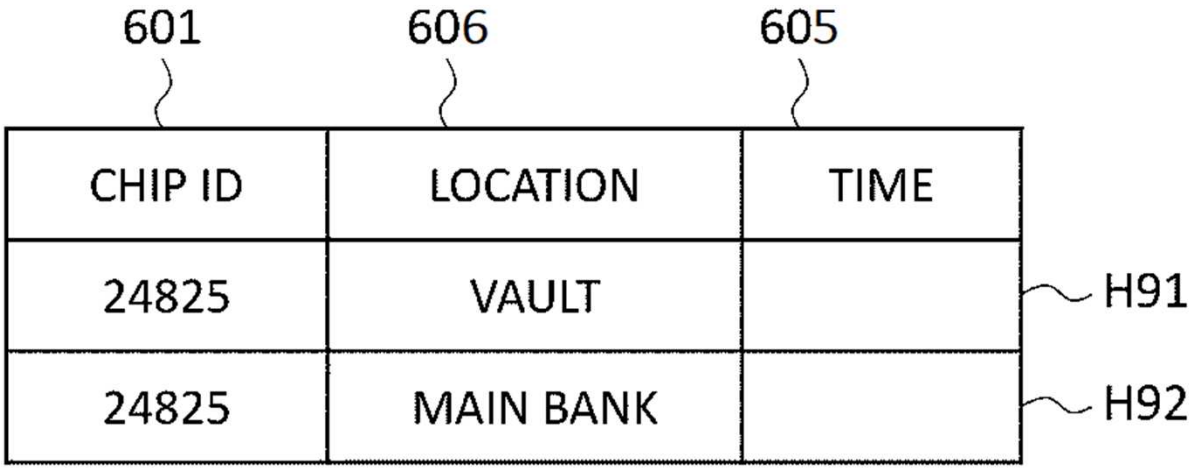


FIG.9

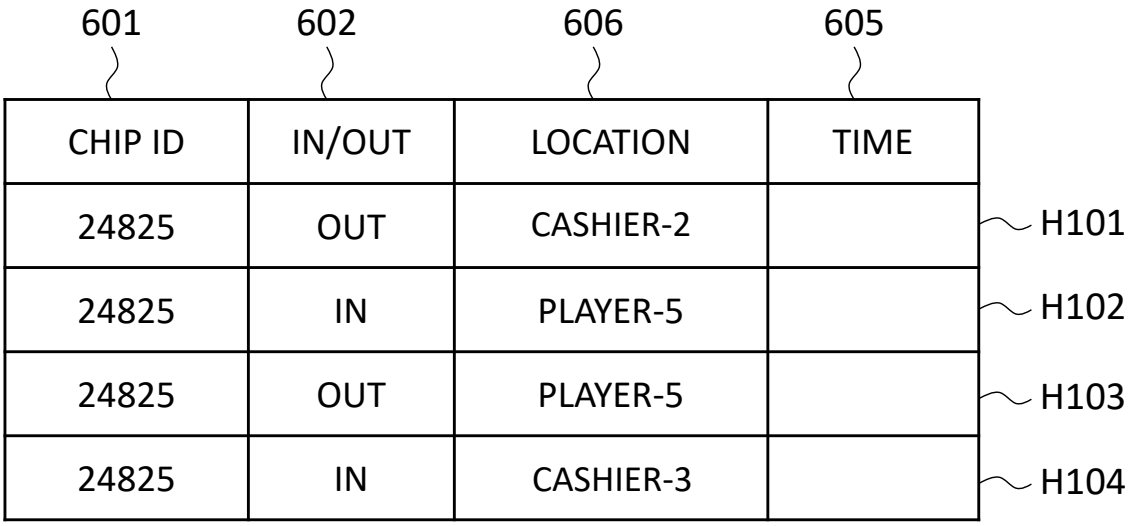

601
602
606
605
CHIP ID
IN/OUT
LOCATION
TIME
24825
OUT
CASHIER-2
H101
24825
IN
PLAYER-5
H102
24825
OUT
PLAYER-5
H103
24825
IN
CASHIER-3
H104

FIG.10

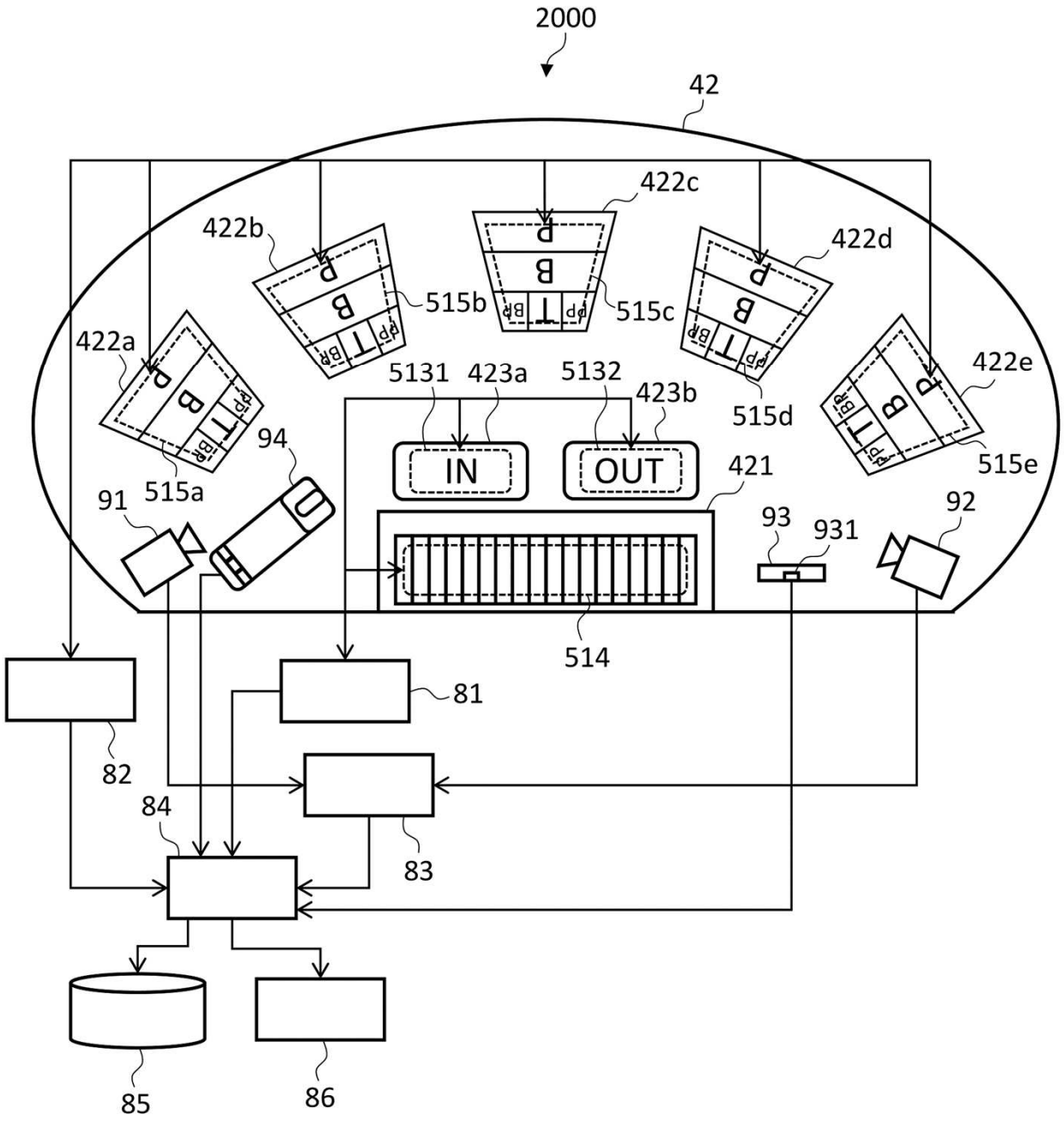

2000
42
422a
422b
422c
422d
422e
515a
515b
515c
515d
515e
5131
423a
5132
423b
IN
OUT
421
514
91
92
93
931
94
81
82
83
84
85
86

FIG.11

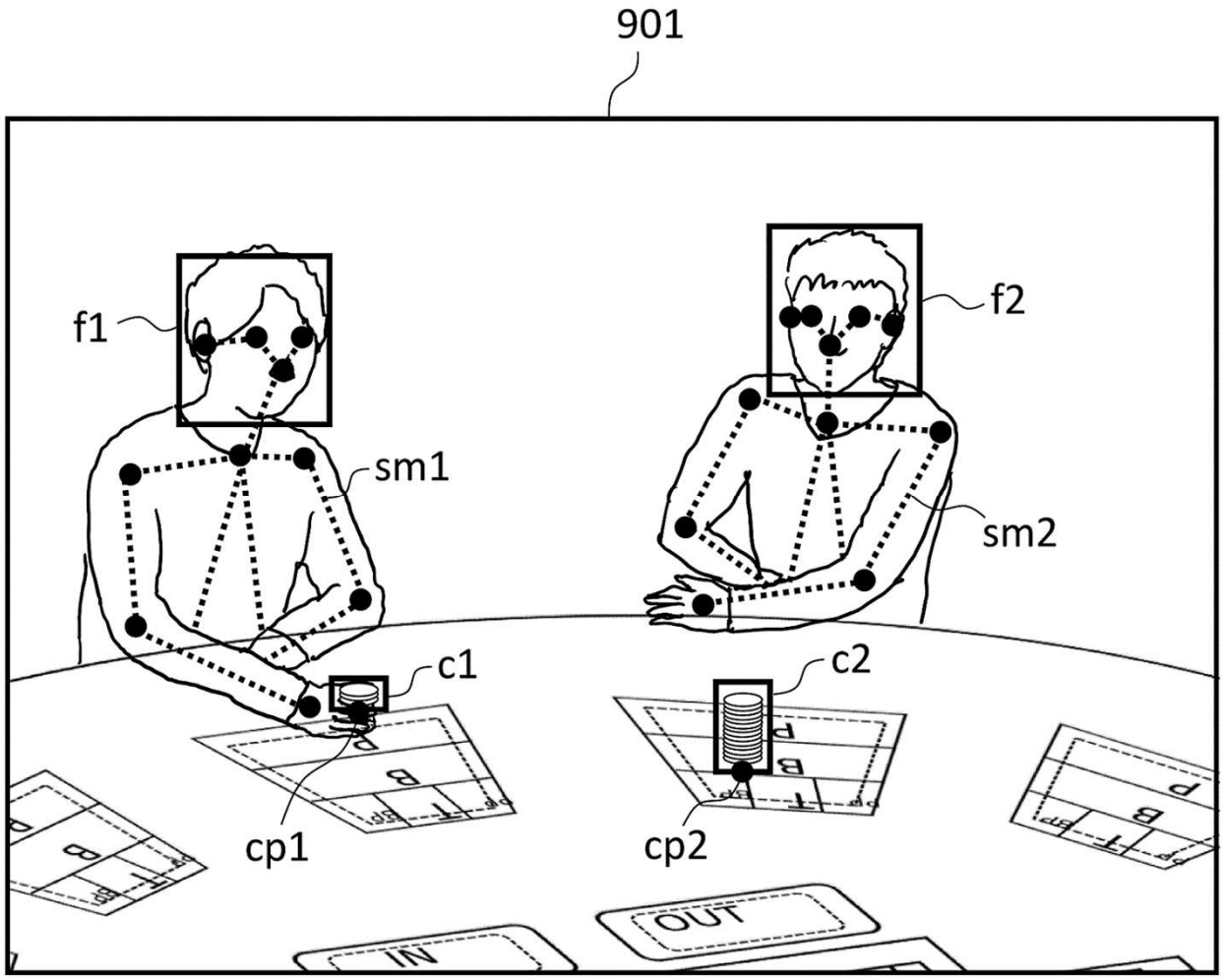


FIG.12

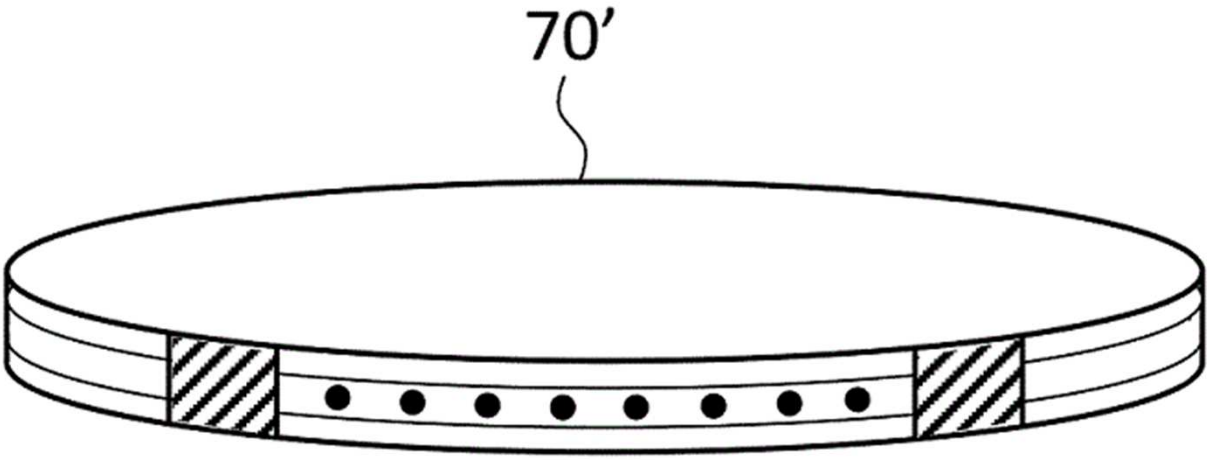


FIG.13A

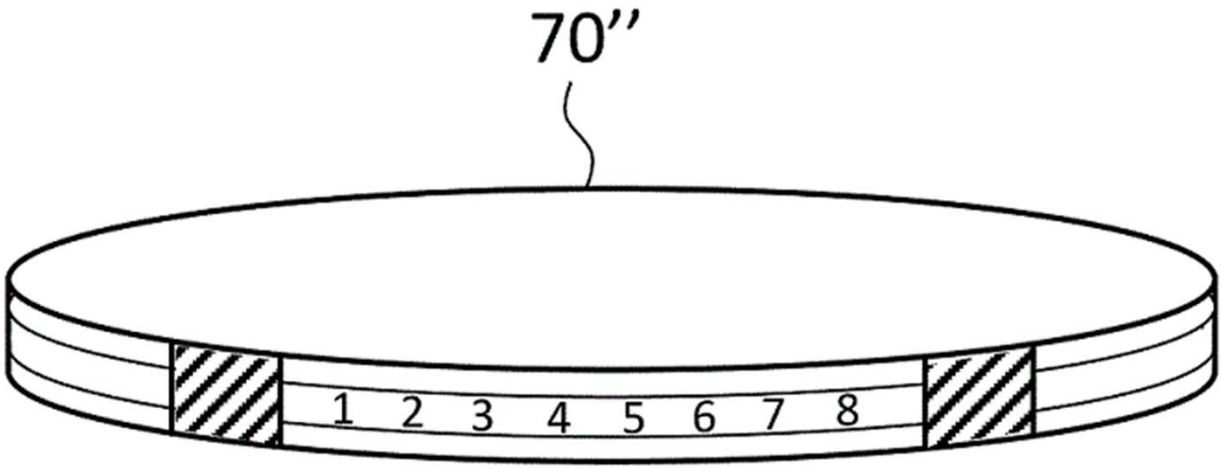


FIG.13B

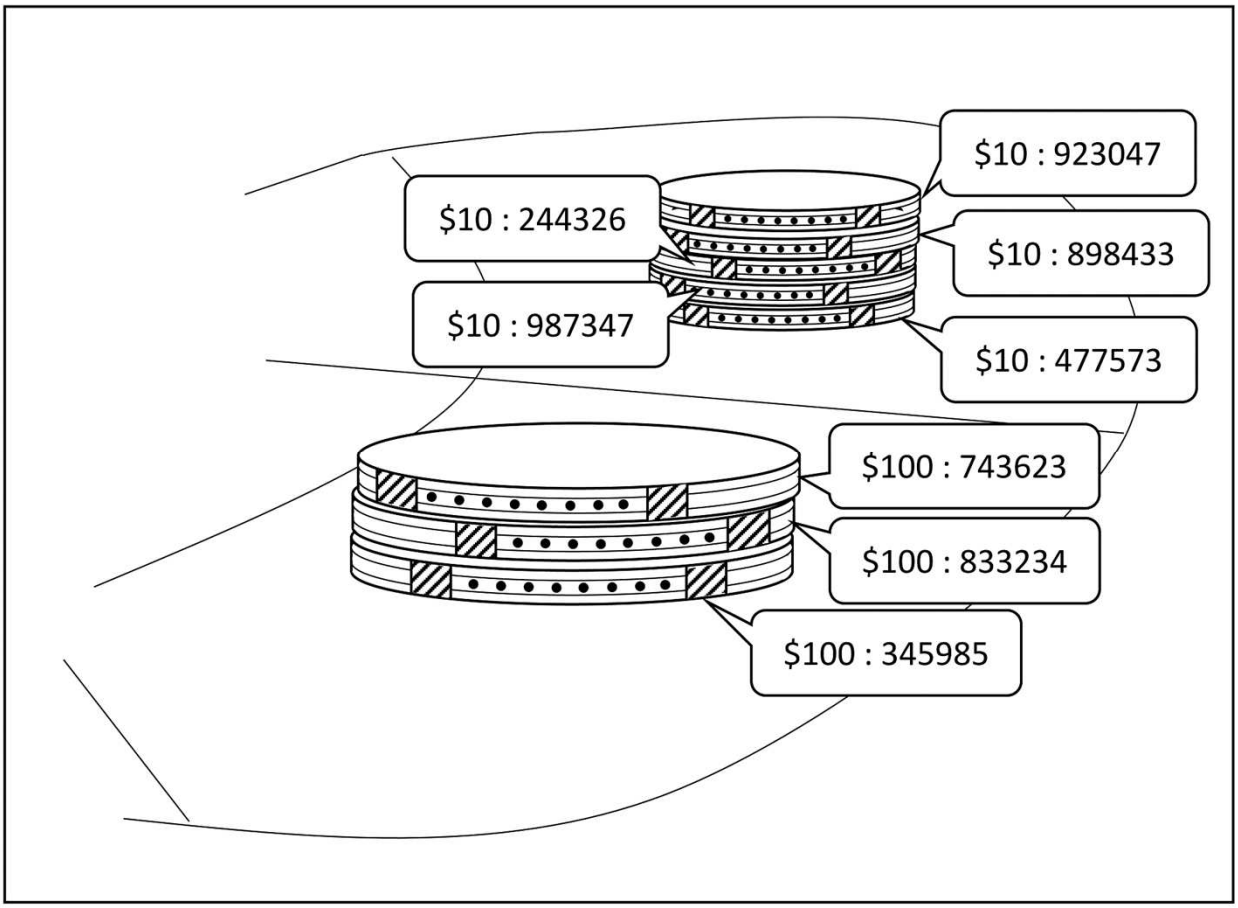

$10 : 923047
$10 : 244326
$10 : 898433
$10 : 987347
$10 : 477573
$100 : 743623
$100 : 833234
$100 : 345985

FIG.14

CHIP ID	FACE VALUE	OWNER	STATUS
923047	$10	Mr. A	Valid
898433	$10	Mr. A	Valid
743623	$10	Mr. A	Valid
833234	$100	Mr. B	Valid
⋮	⋮	⋮	⋮

FIG.15

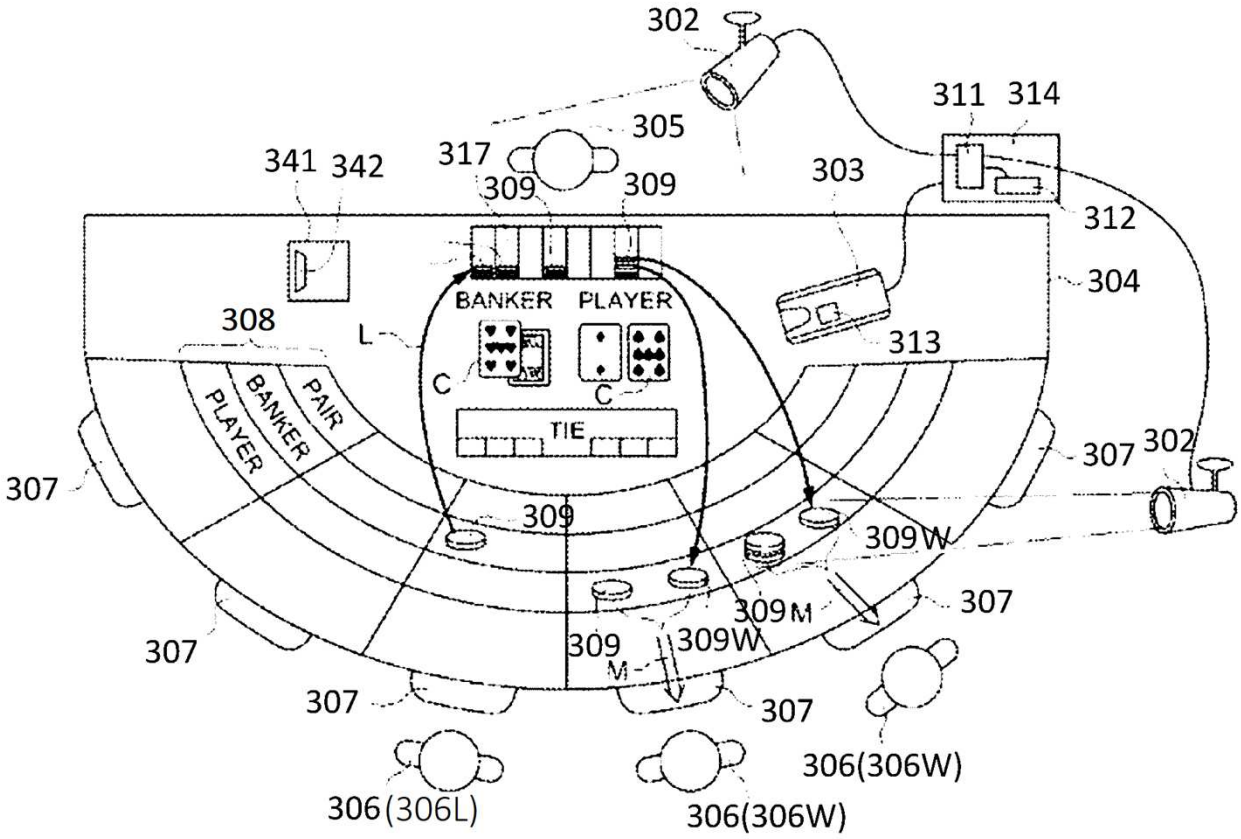

302
311
314
305
341
342
317
309
309
303
312
304
BANKER
PLAYER
308
L
313
C
C
PAIR
BANKER
PLAYER
TIE
307
302
307
309
309W
309M
307
307
309
M
309W
307
307
306(306W)
306(306L)
306(306W)

FIG.16

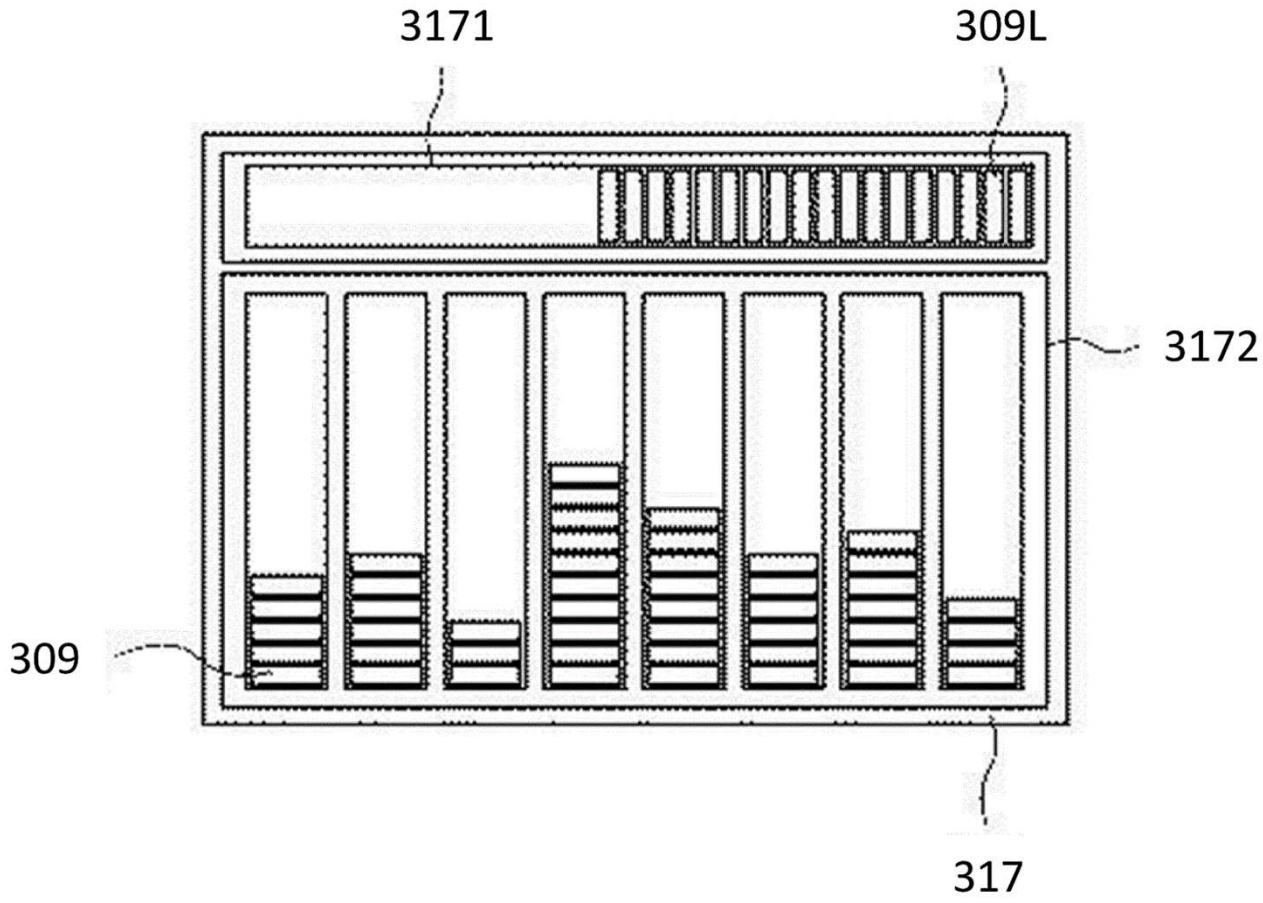


FIG.17A

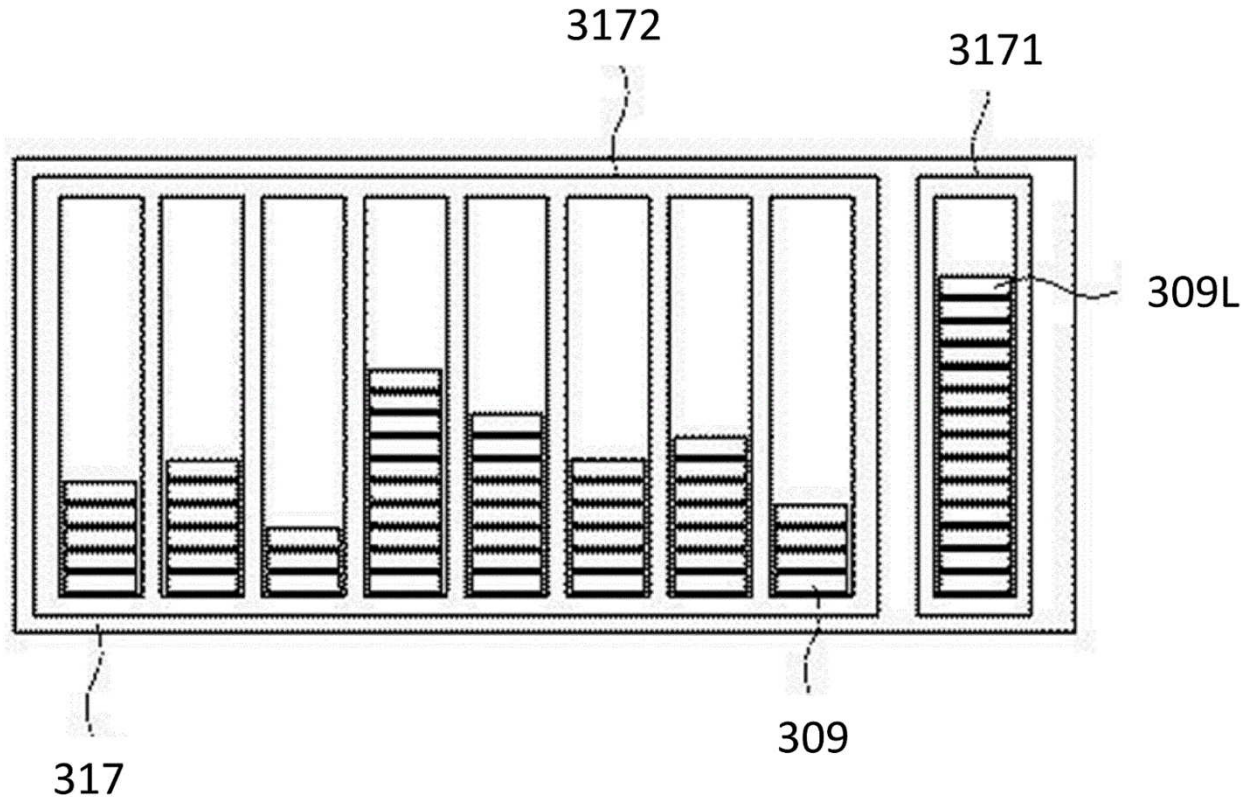


FIG.17B



FIG.18

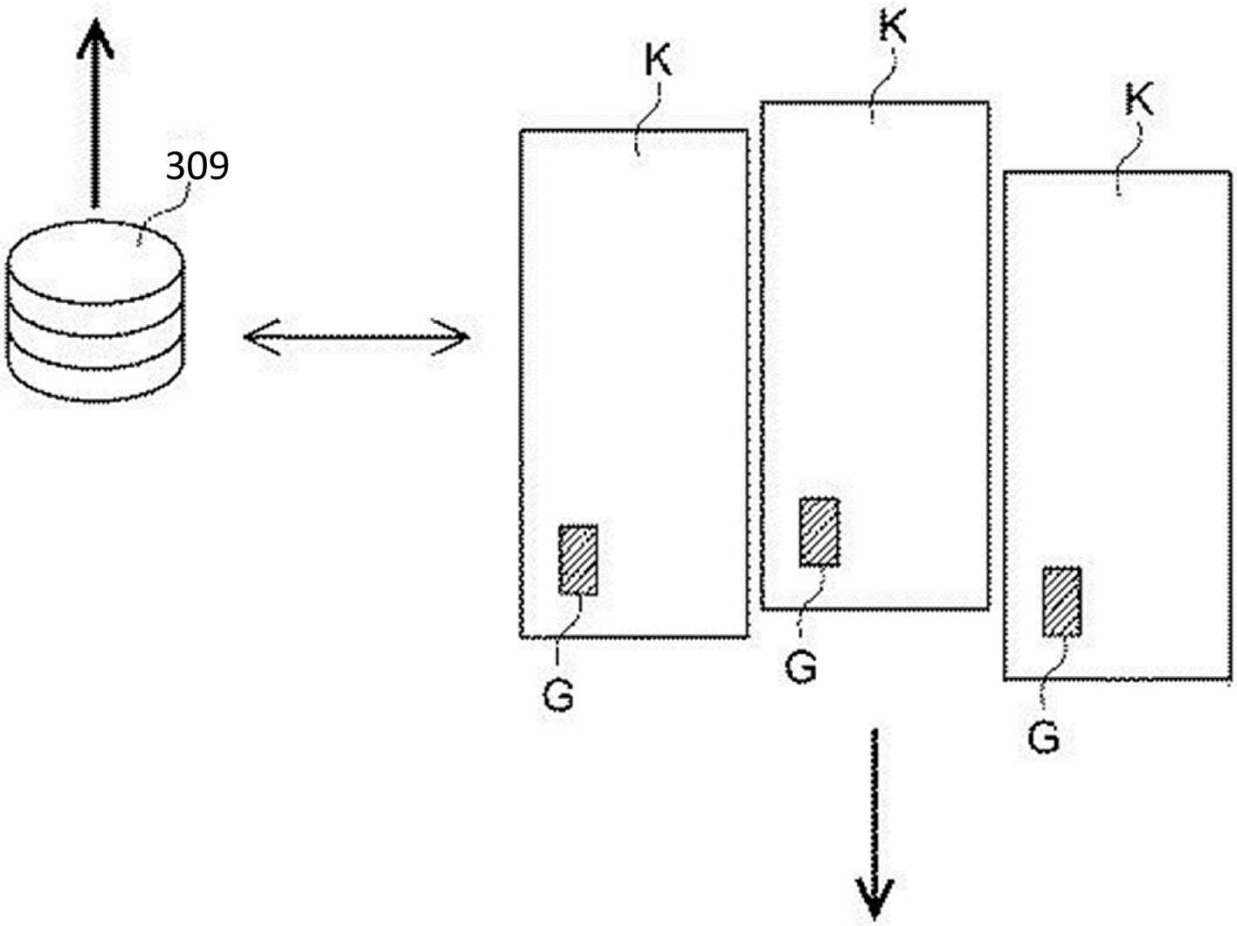


FIG.19

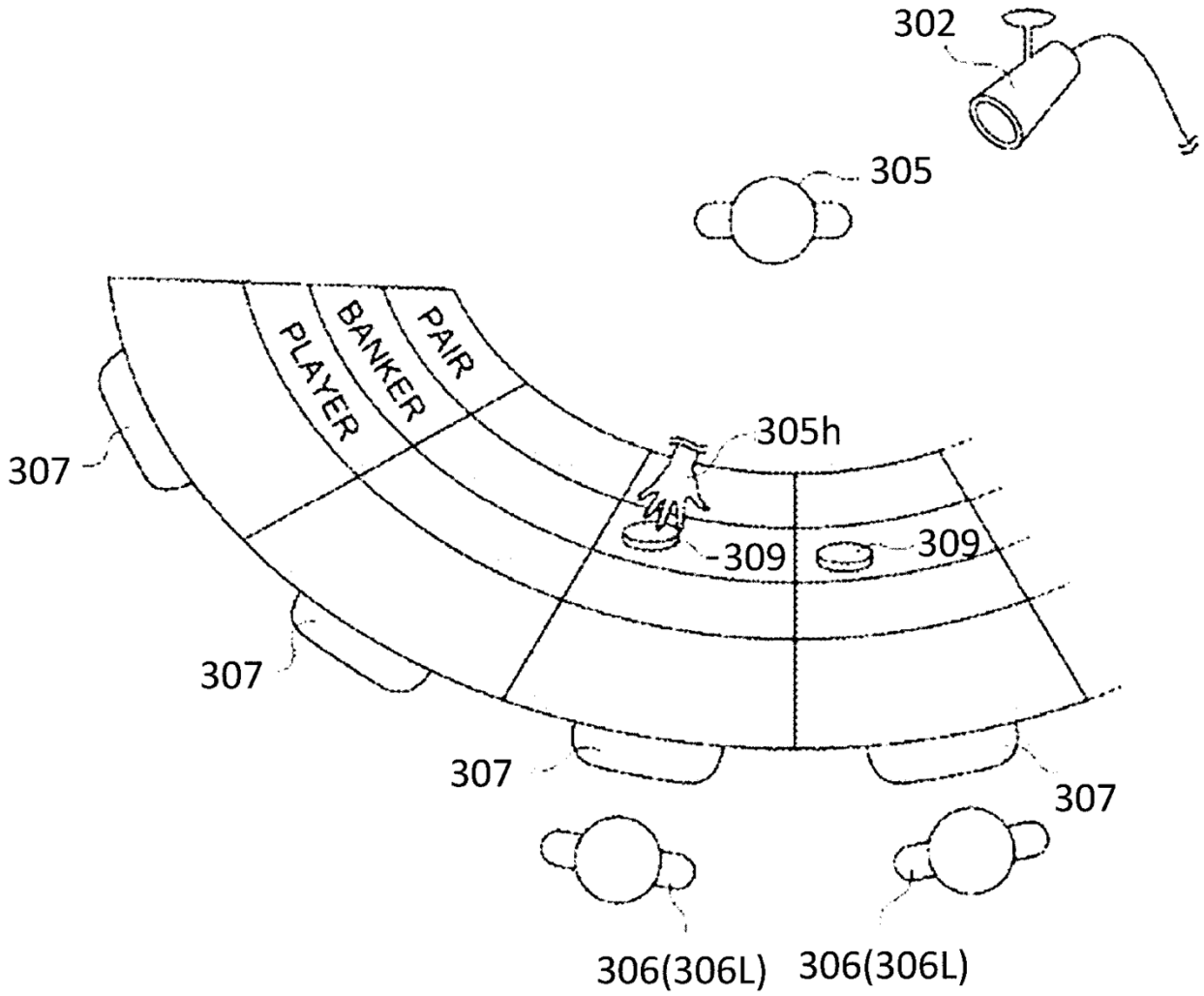

302
305
PAIR
BANKER
PLAYER
305h
307
309
309
307
307
307
306(306L)
306(306L)

FIG.20

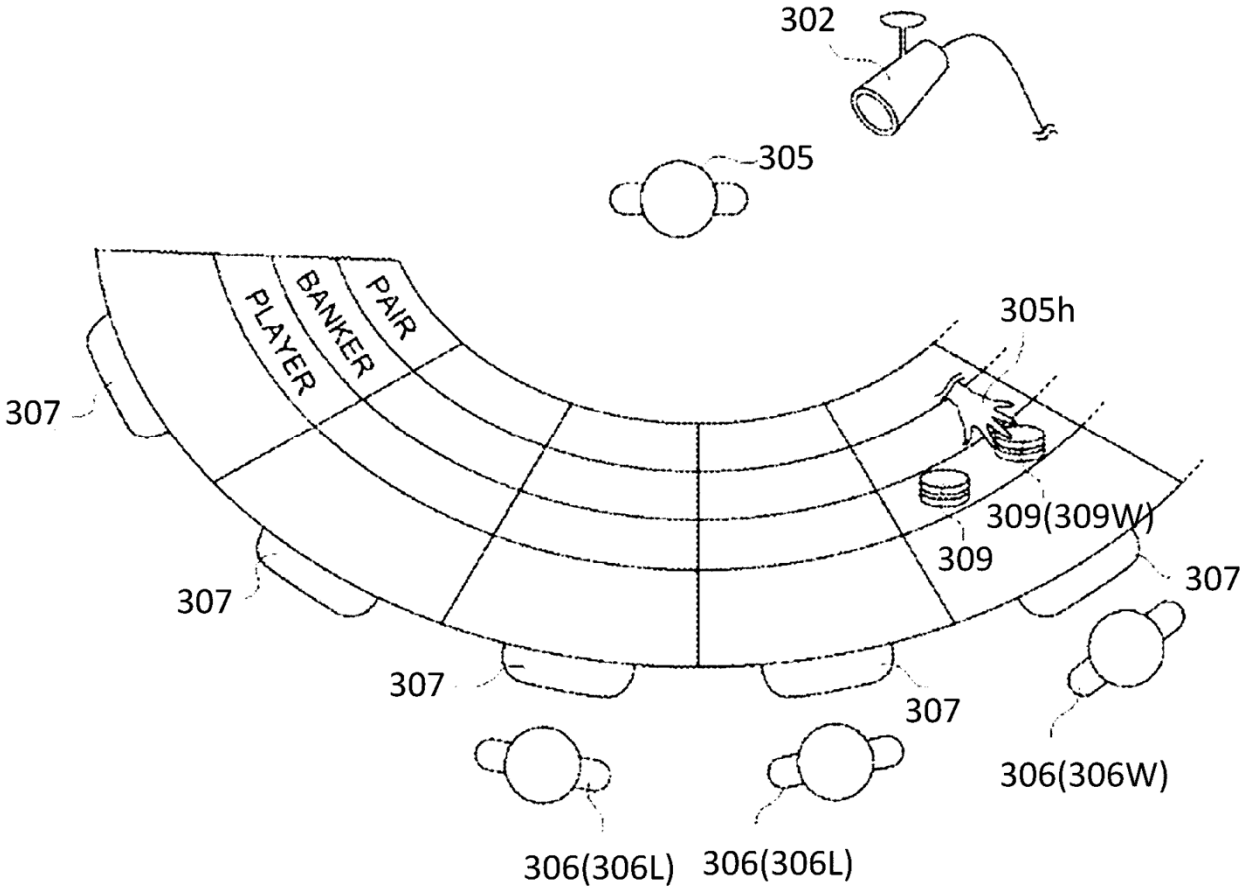

302
305
PAIR
BANKER
PLAYER
305h
307
309(309W)
309
307
307
307
307
306(306W)
306(306L)
306(306L)

FIG.21

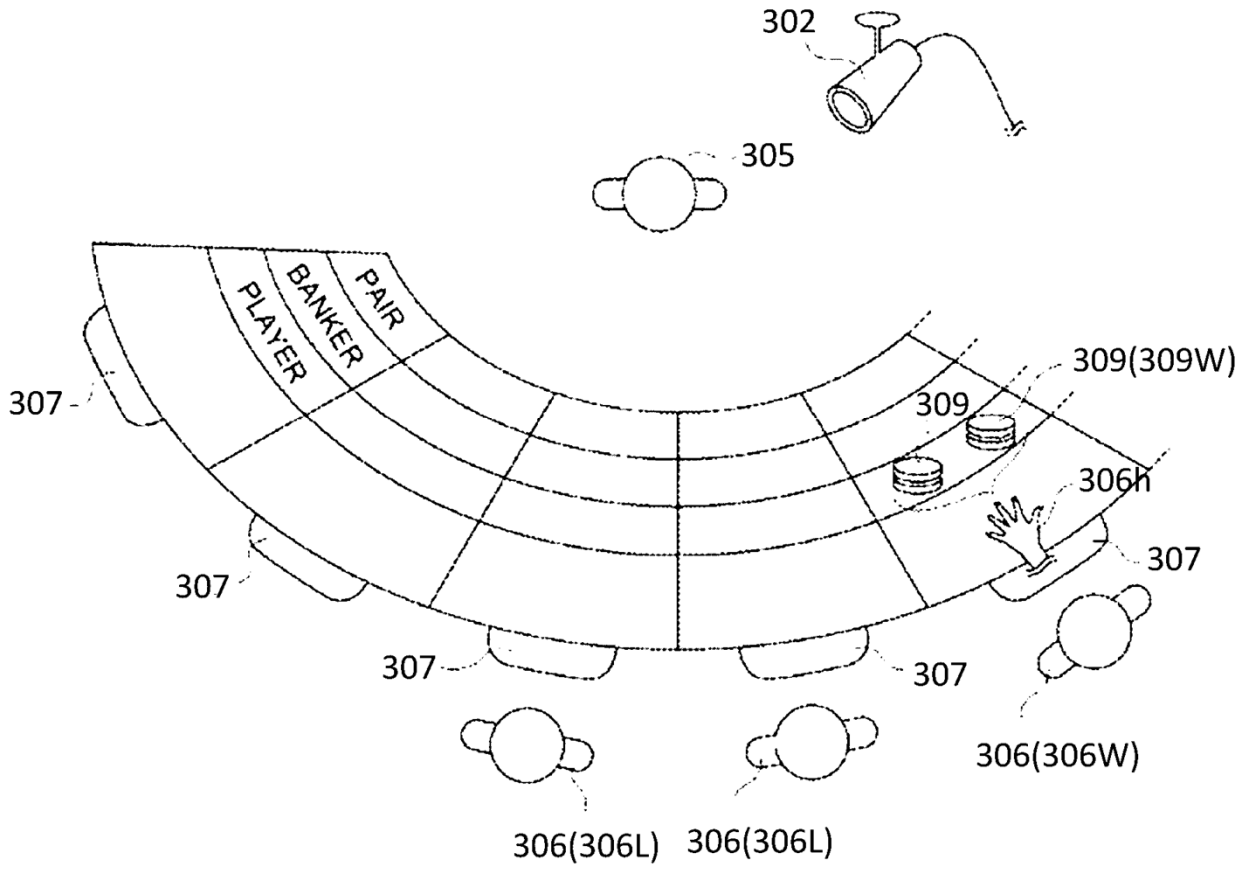

302
305
PAIR
BANKER
PLAYER
307
309(309W)
309
306h
307
307
307
307
306(306W)
306(306L)
306(306L)

FIG.22

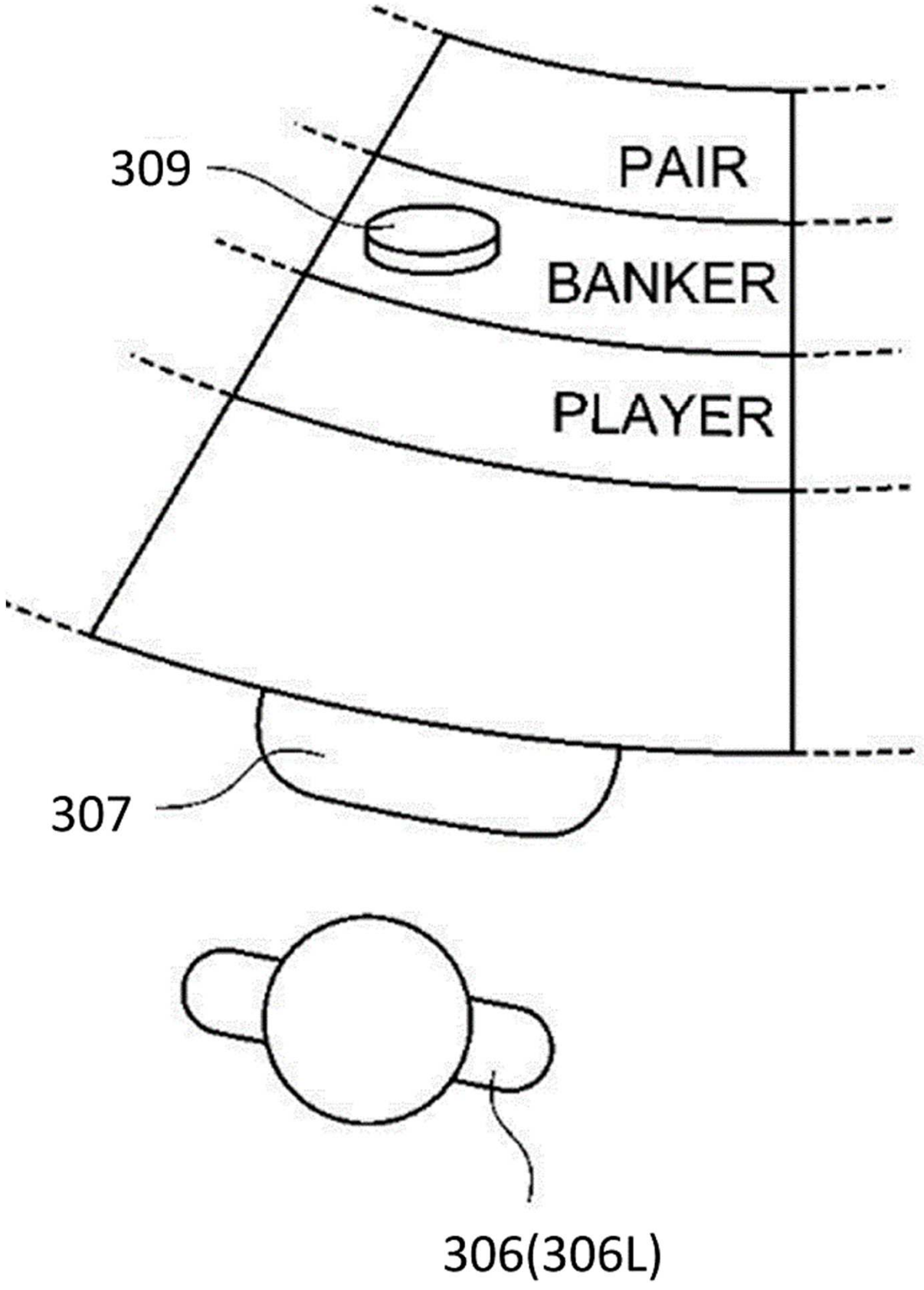


FIG.23A

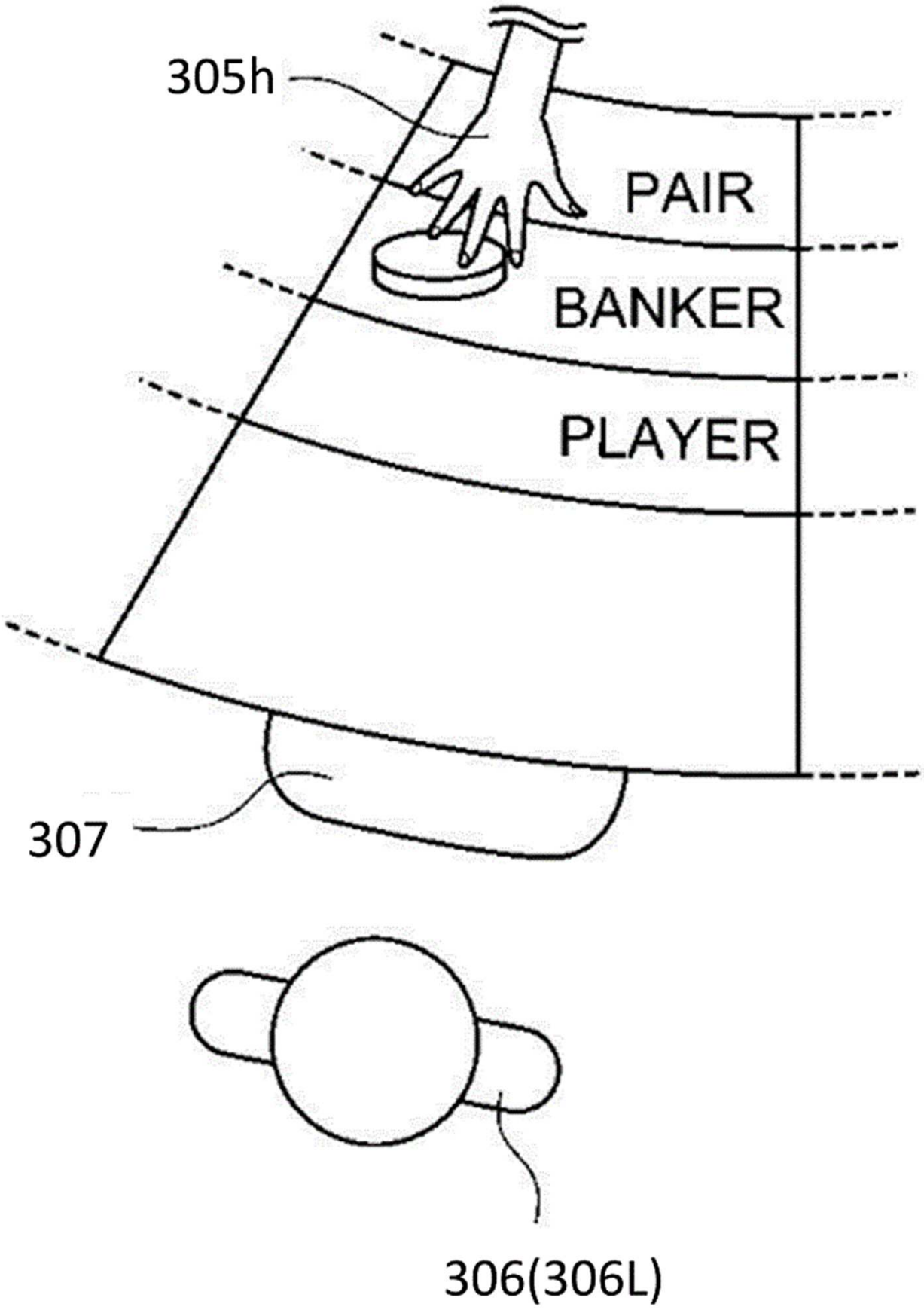


FIG.23B

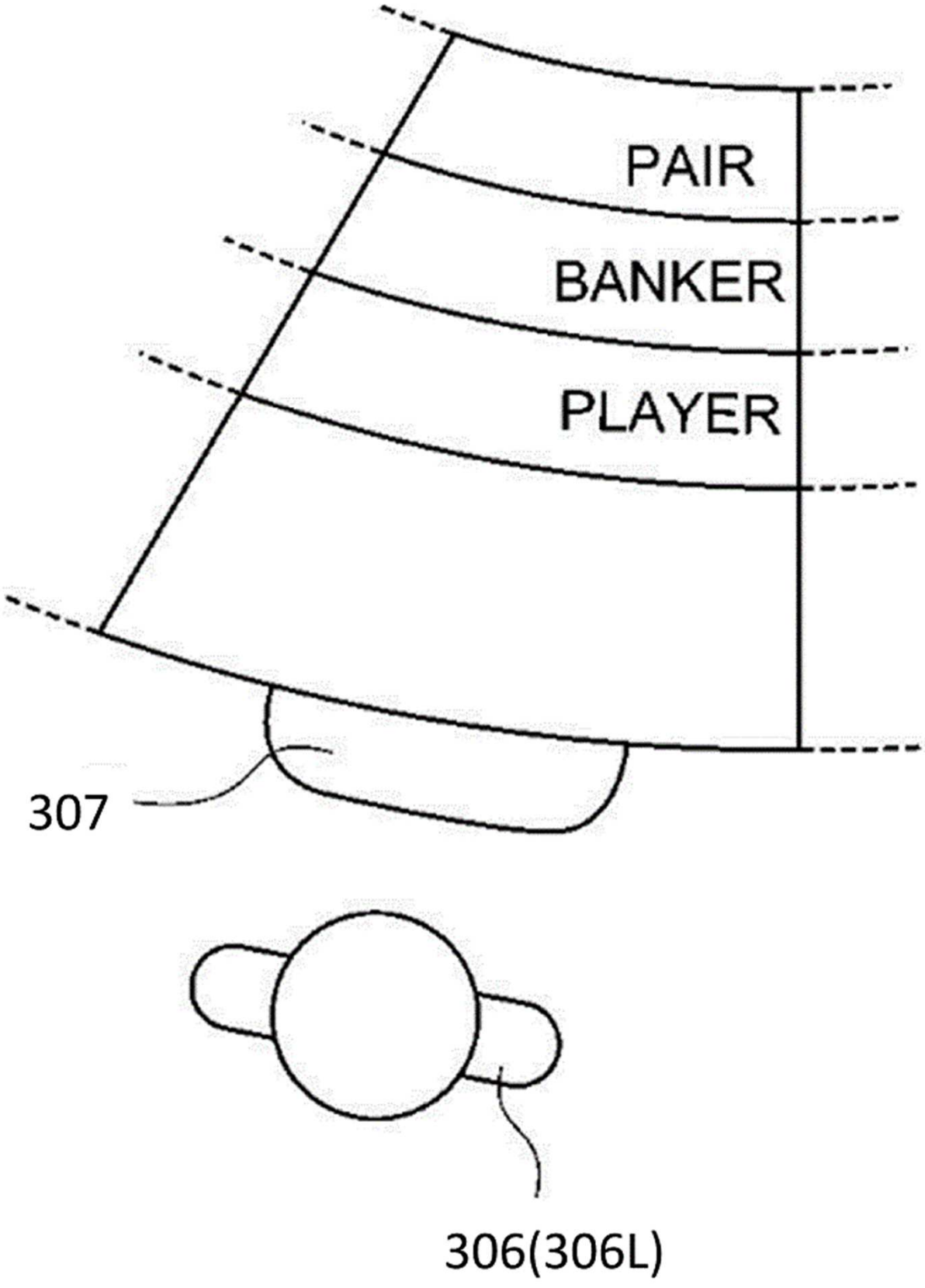


FIG.23C

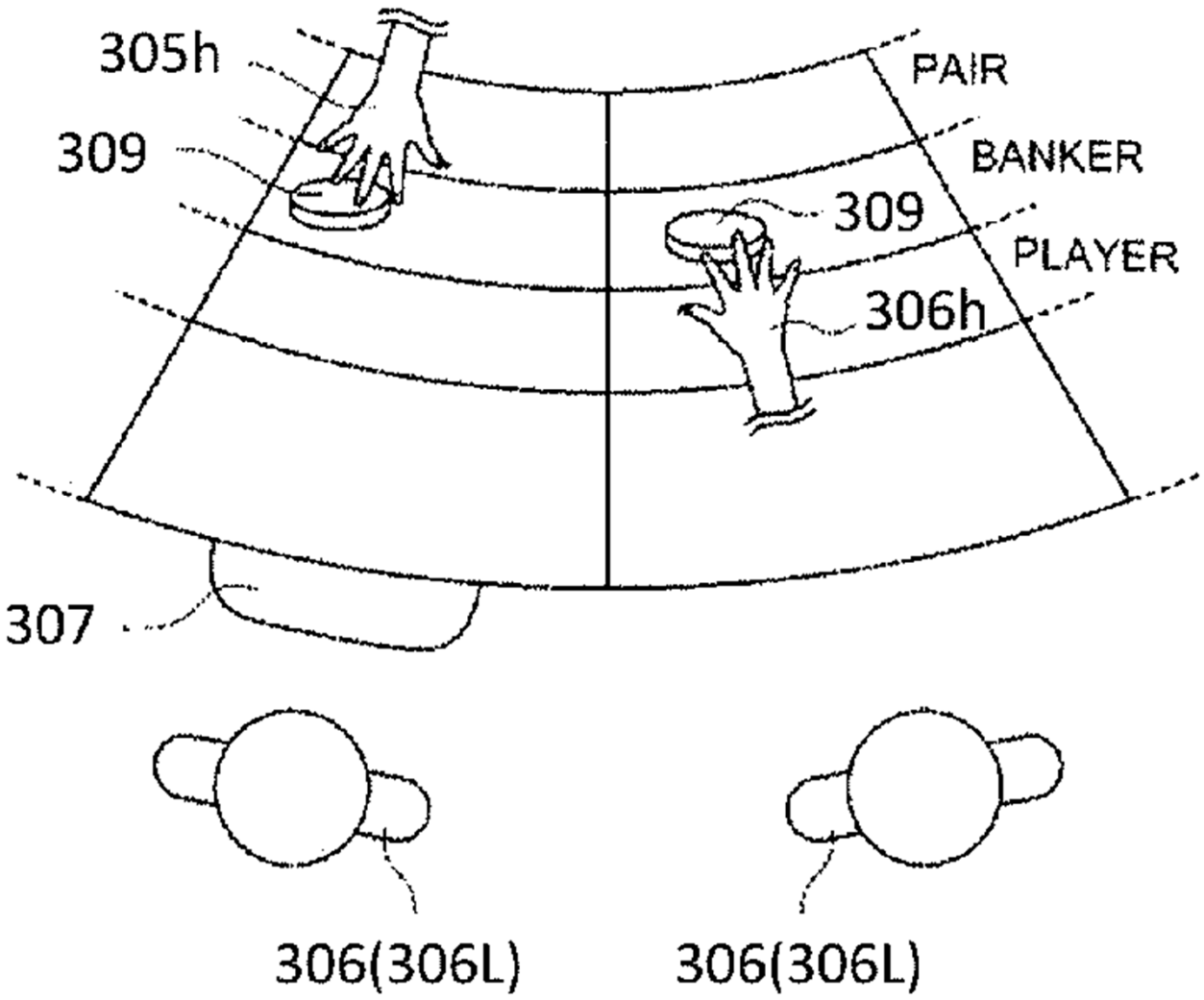


FIG.24

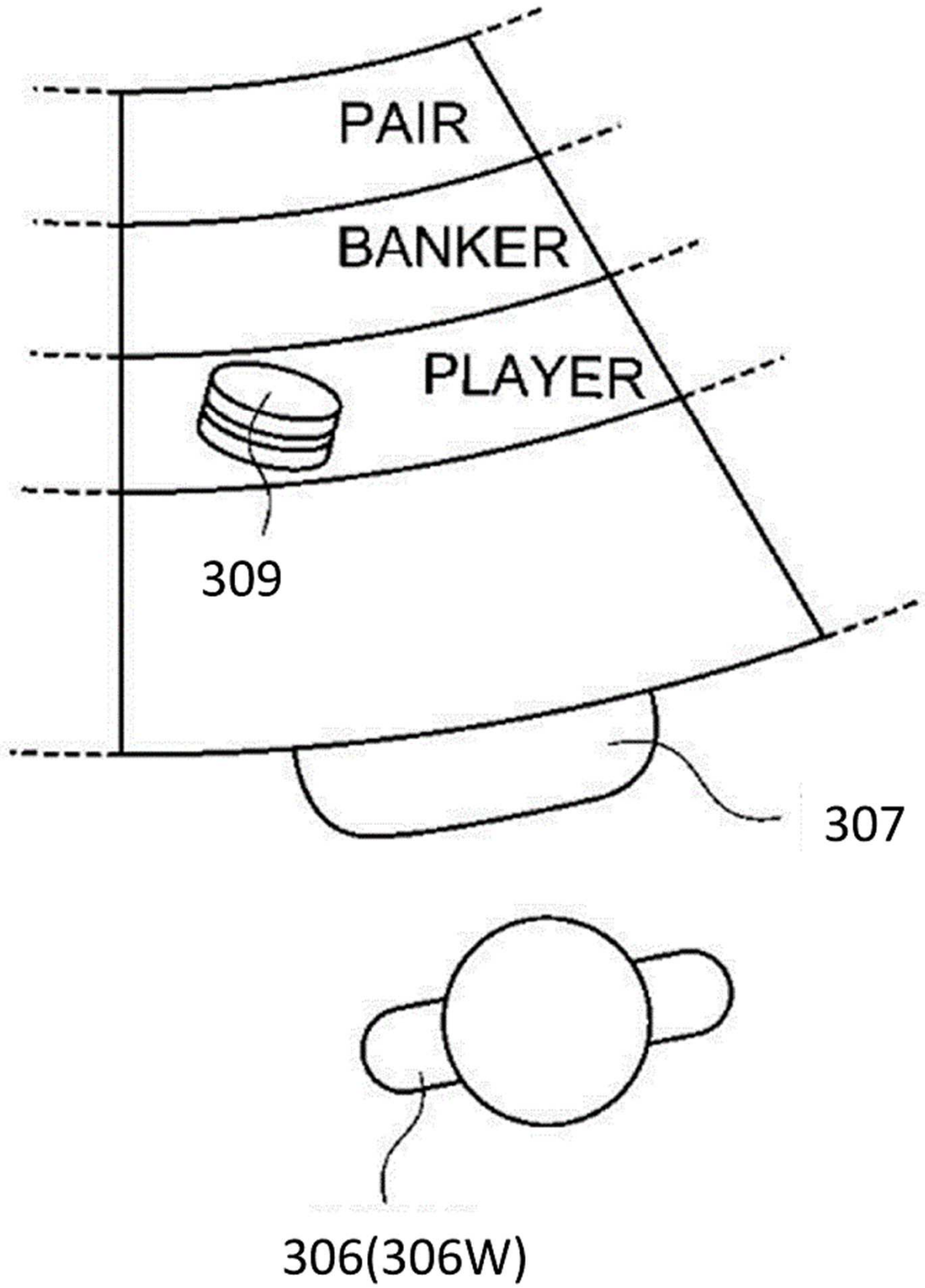


FIG.25A

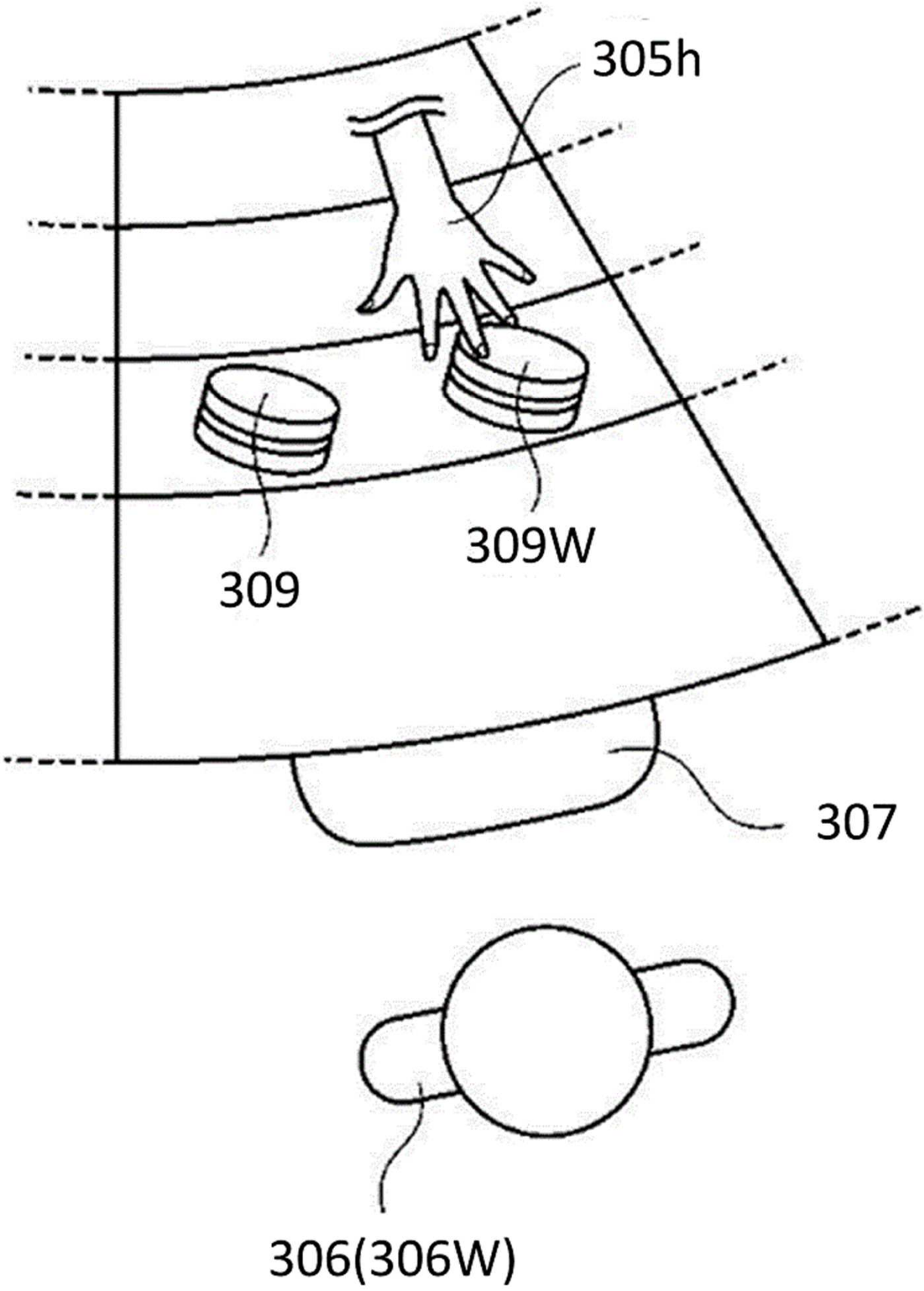


FIG.25B

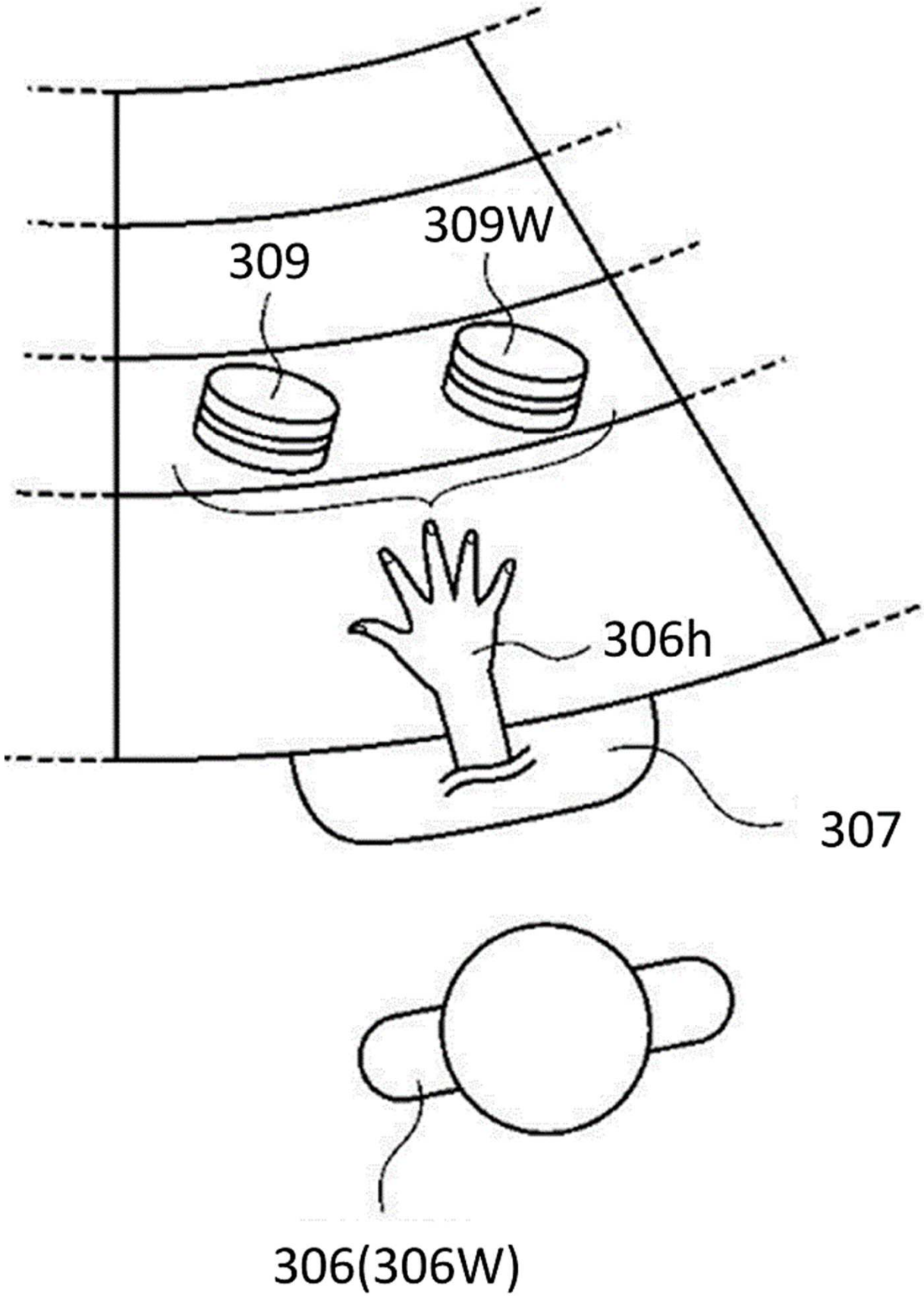


FIG.25C

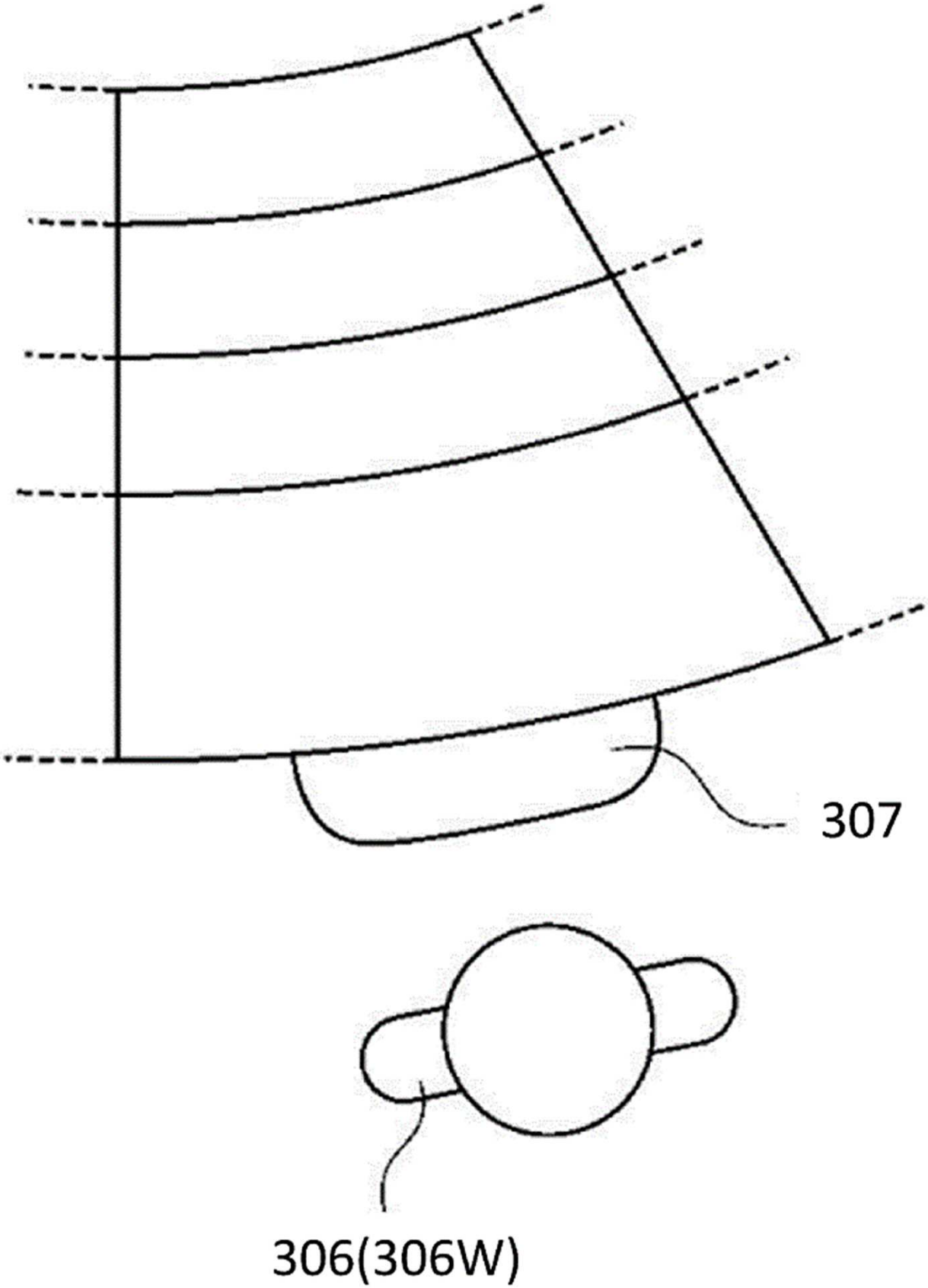


FIG.25D

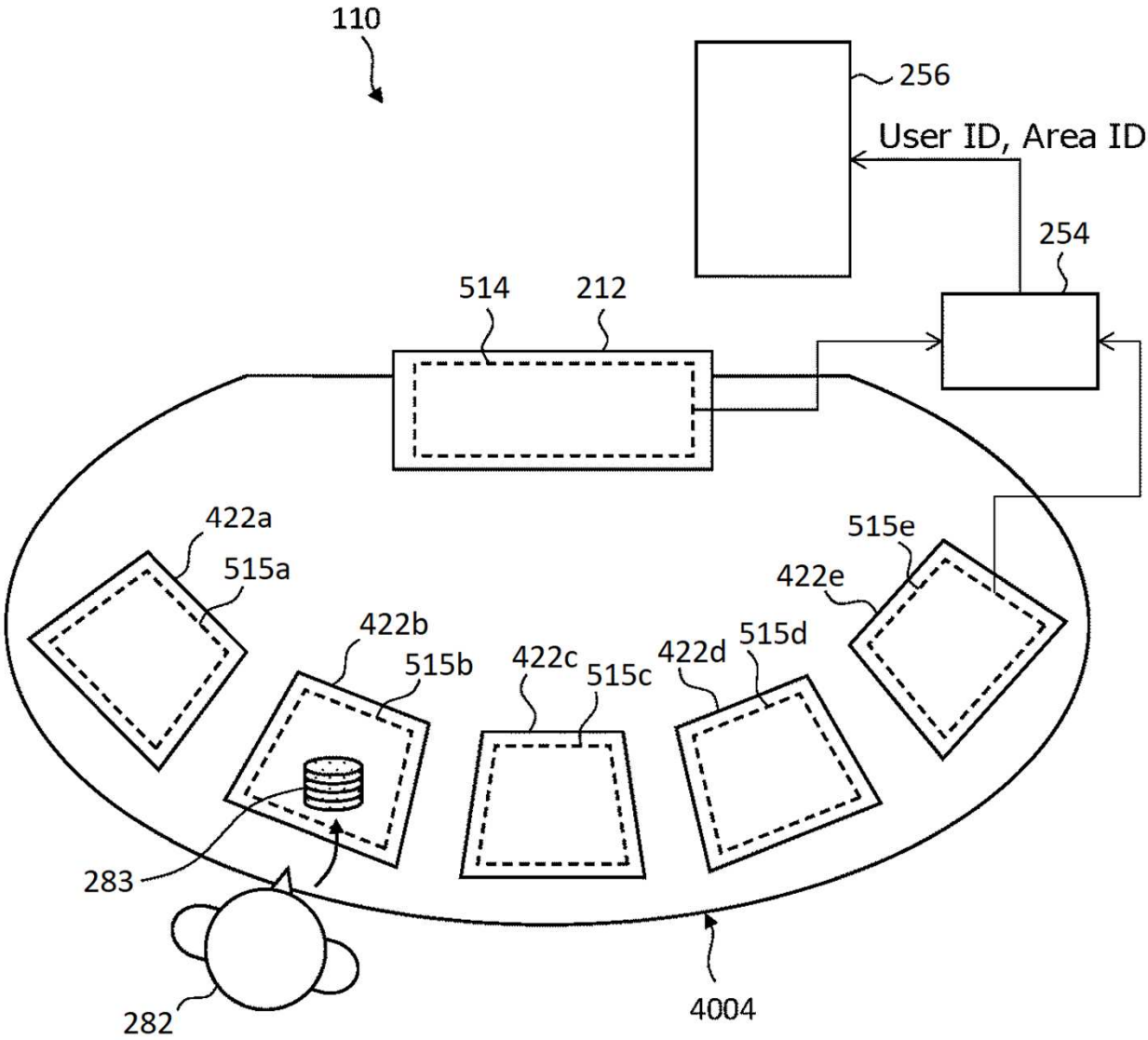

110
256
User ID, Area ID
254
514
212
422a
515a
422b
515b
422c
515c
422d
515d
422e
515e
283
282
4004

FIG.26

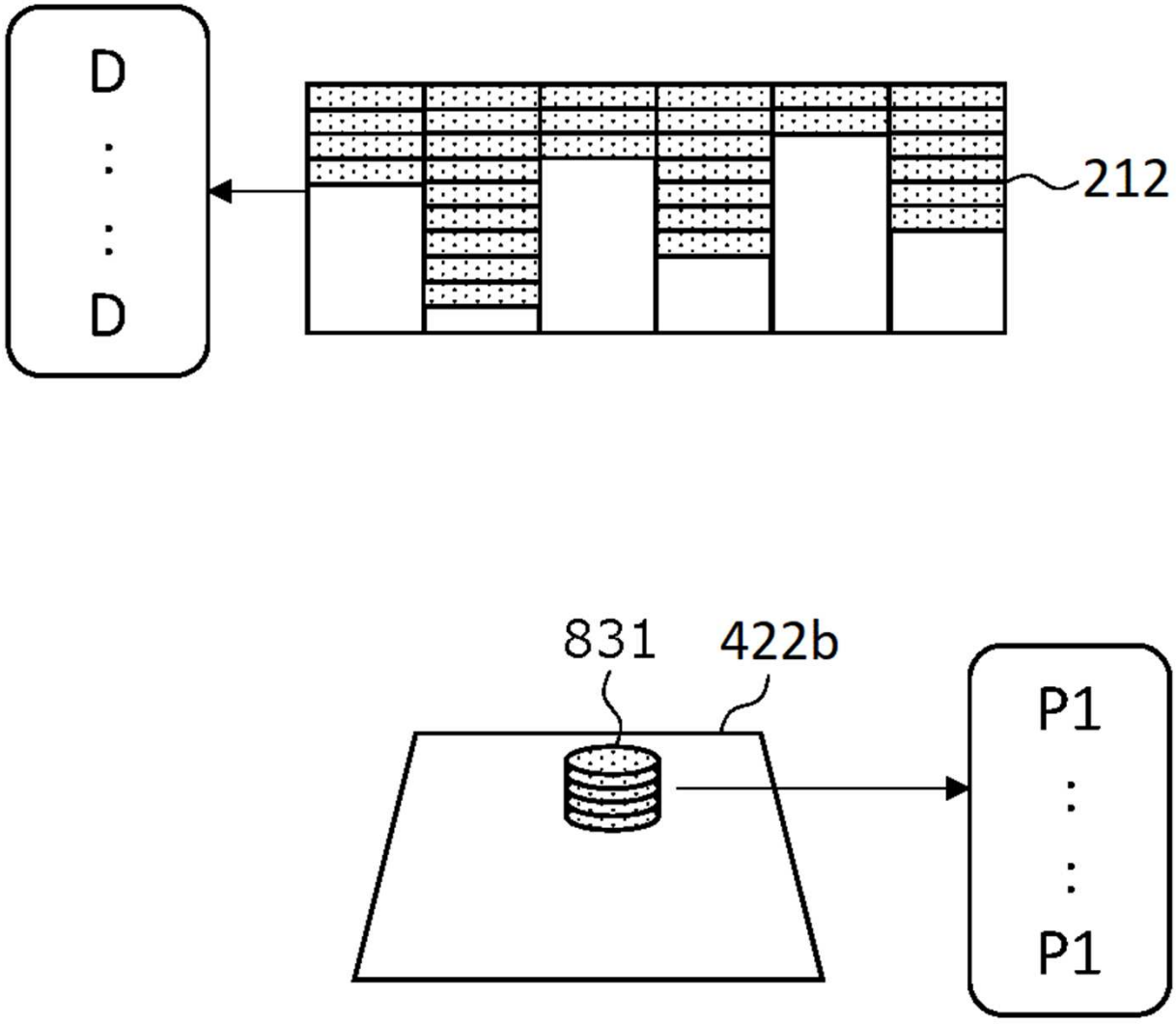


FIG.27

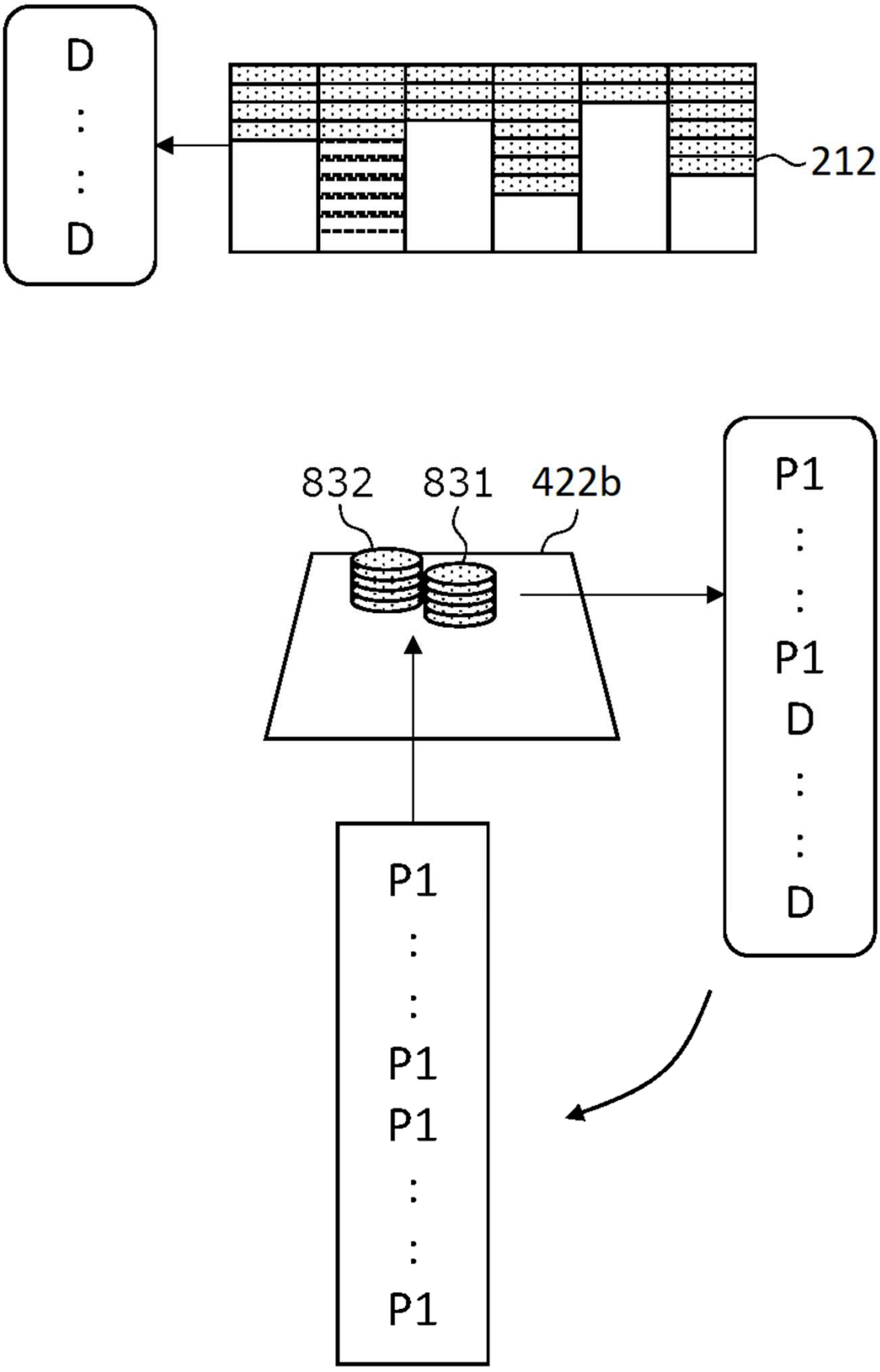


FIG.28

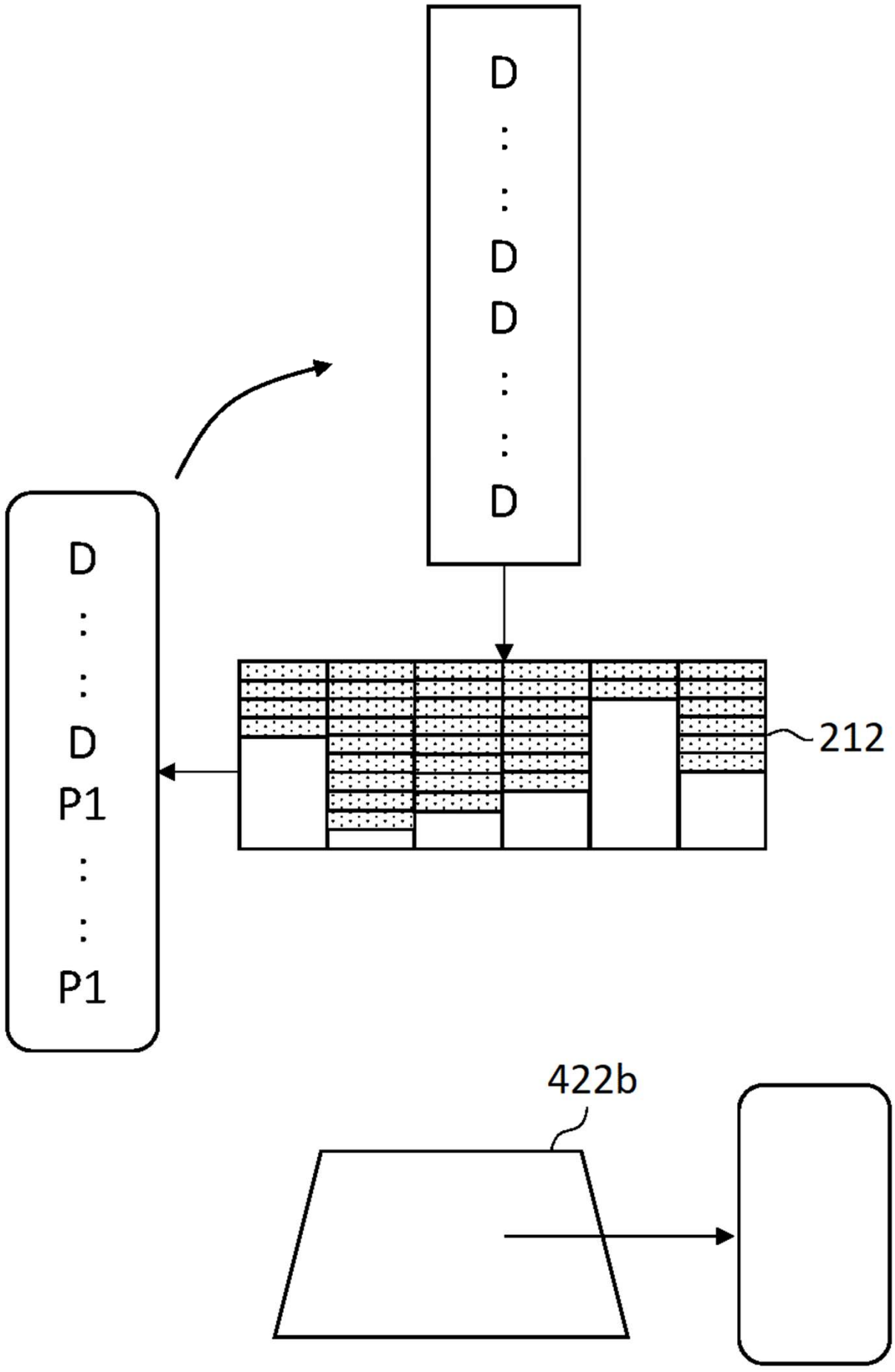


FIG.29

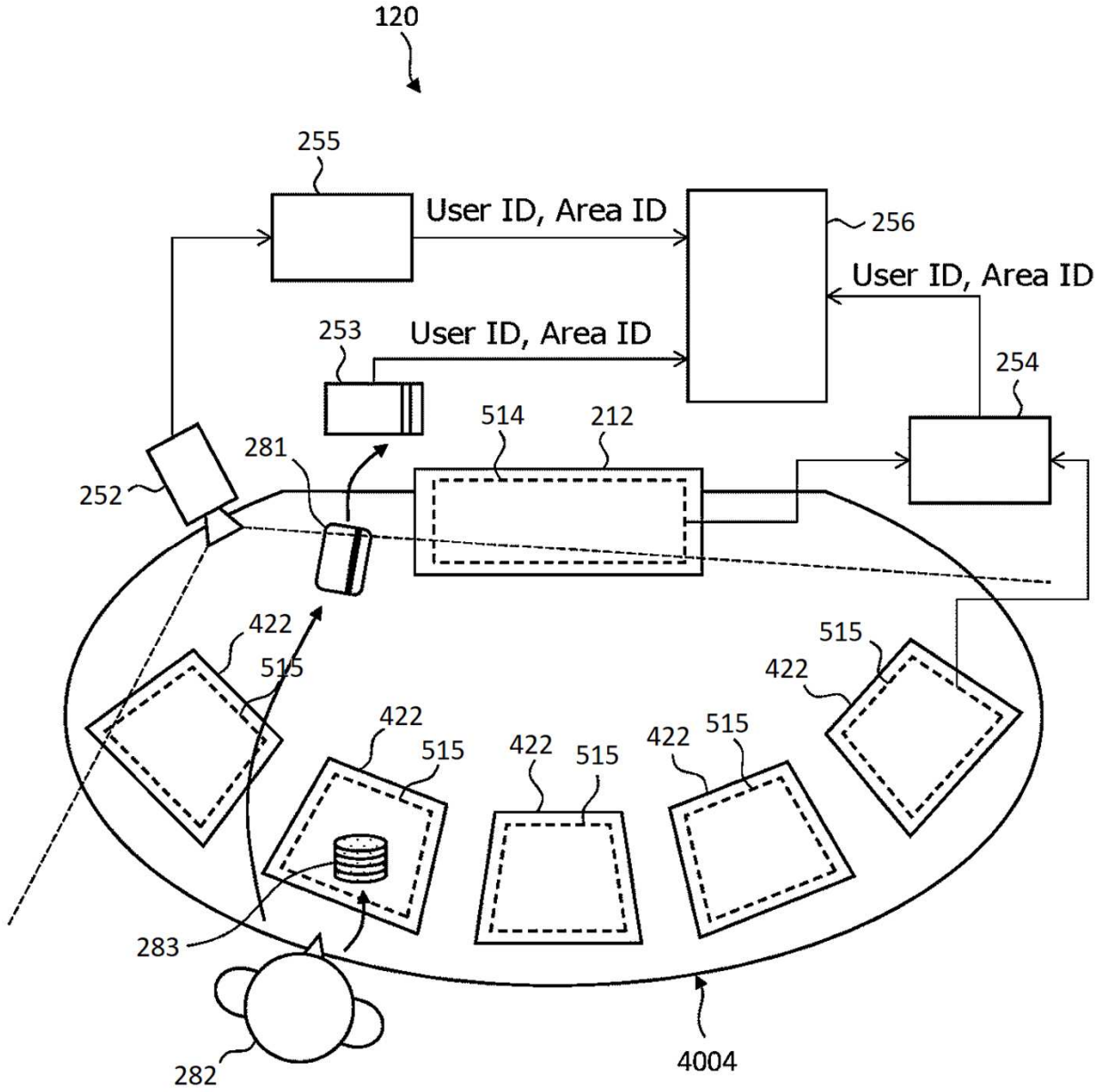

120
255
User ID, Area ID
256
User ID, Area ID
253
User ID, Area ID
254
514
212
281
252
422
515
422
515
422
515
422
515
515
422
283
282
4004

FIG.30

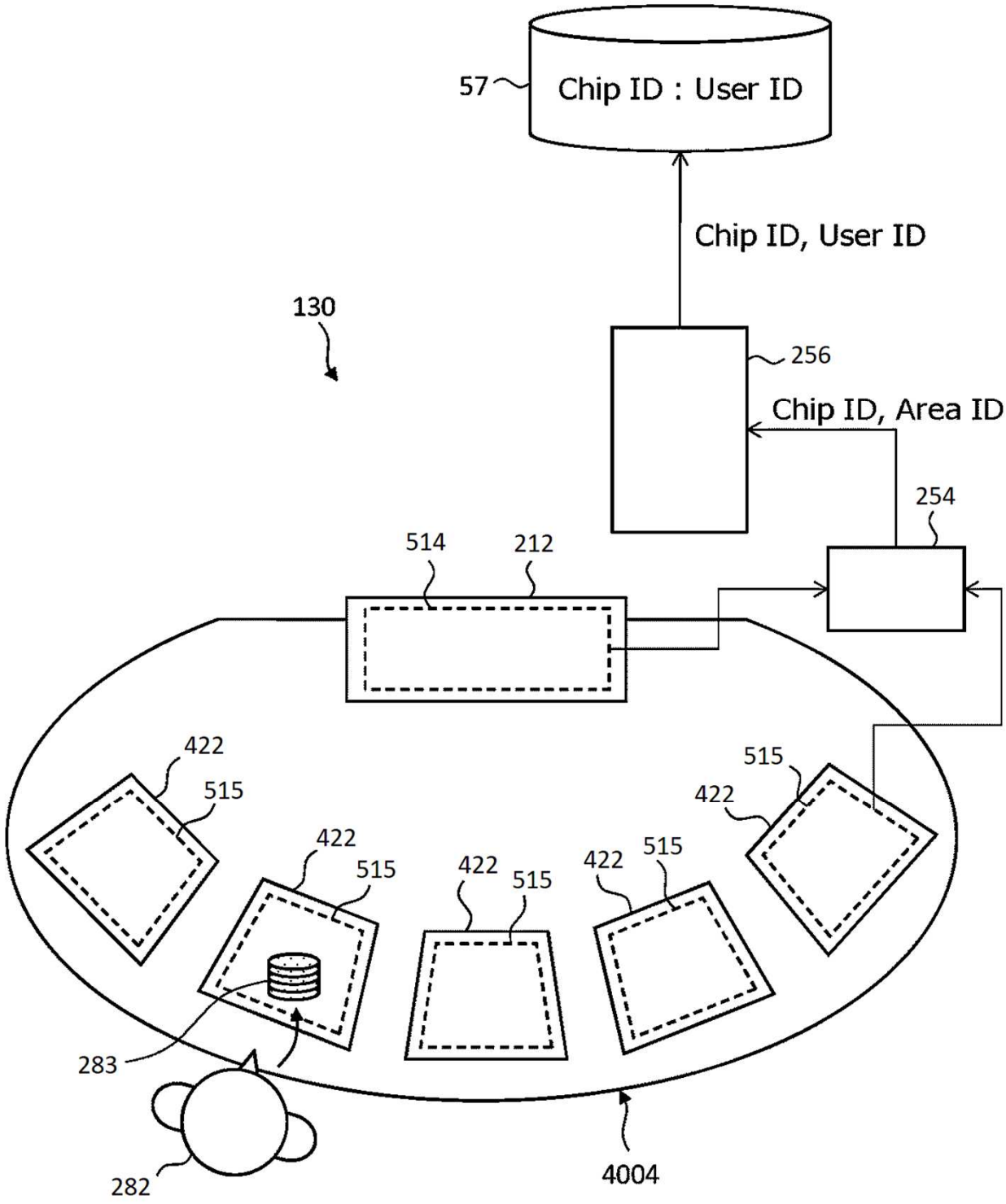

57
Chip ID : User ID
Chip ID, User ID
130
256
Chip ID, Area ID
254
514
212
422
515
422
515
422
515
422
515
515
422
283
282
4004

FIG.31

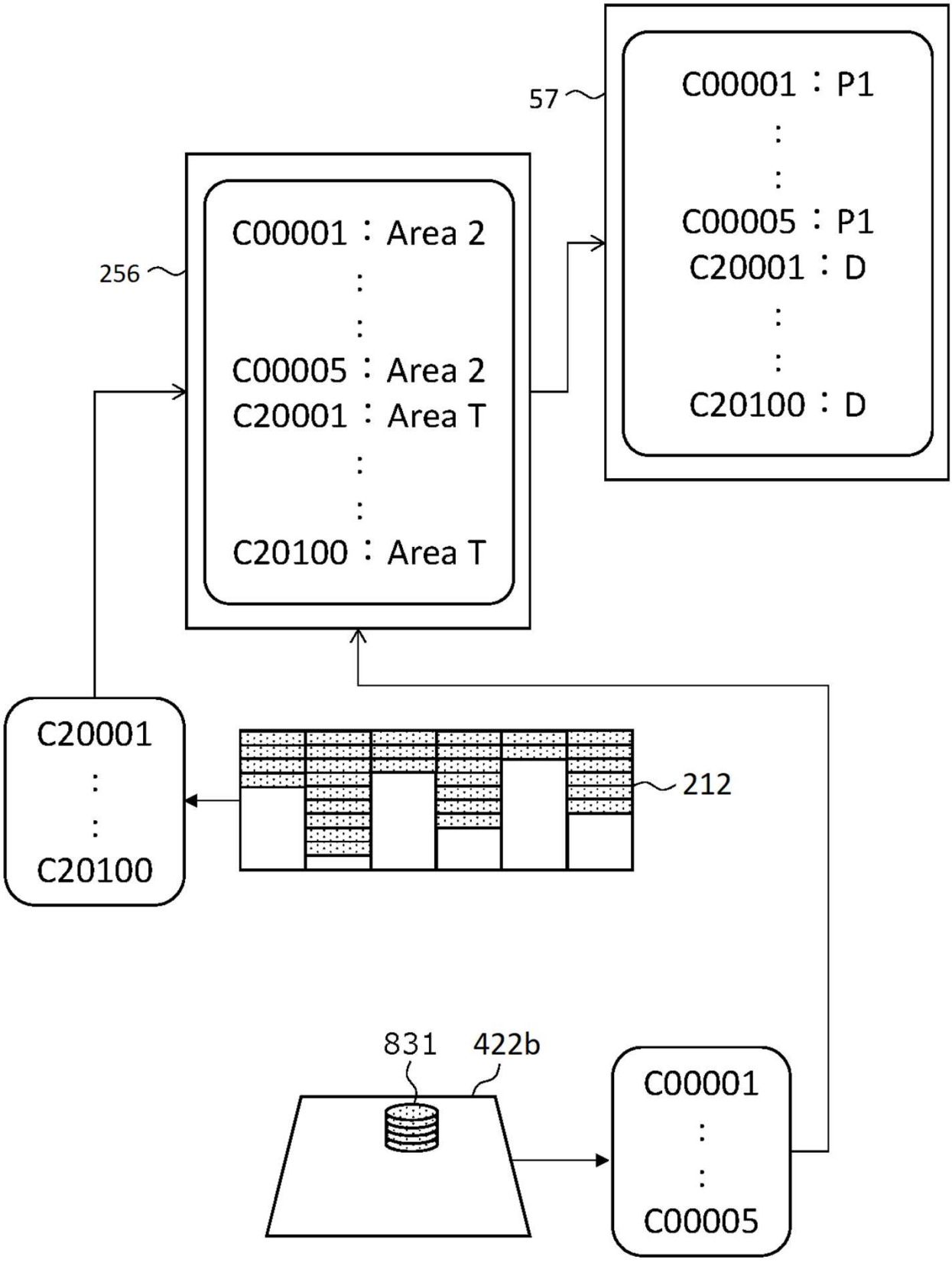

57
C00001 : P1
:
:
C00005 : P1
C20001 : D
:
:
C20100 : D
256
C00001 : Area 2
:
:
C00005 : Area 2
C20001 : Area T
:
:
C20100 : Area T
C20001
:
:
C20100
212
831
422b
C00001
:
:
C00005

FIG.32

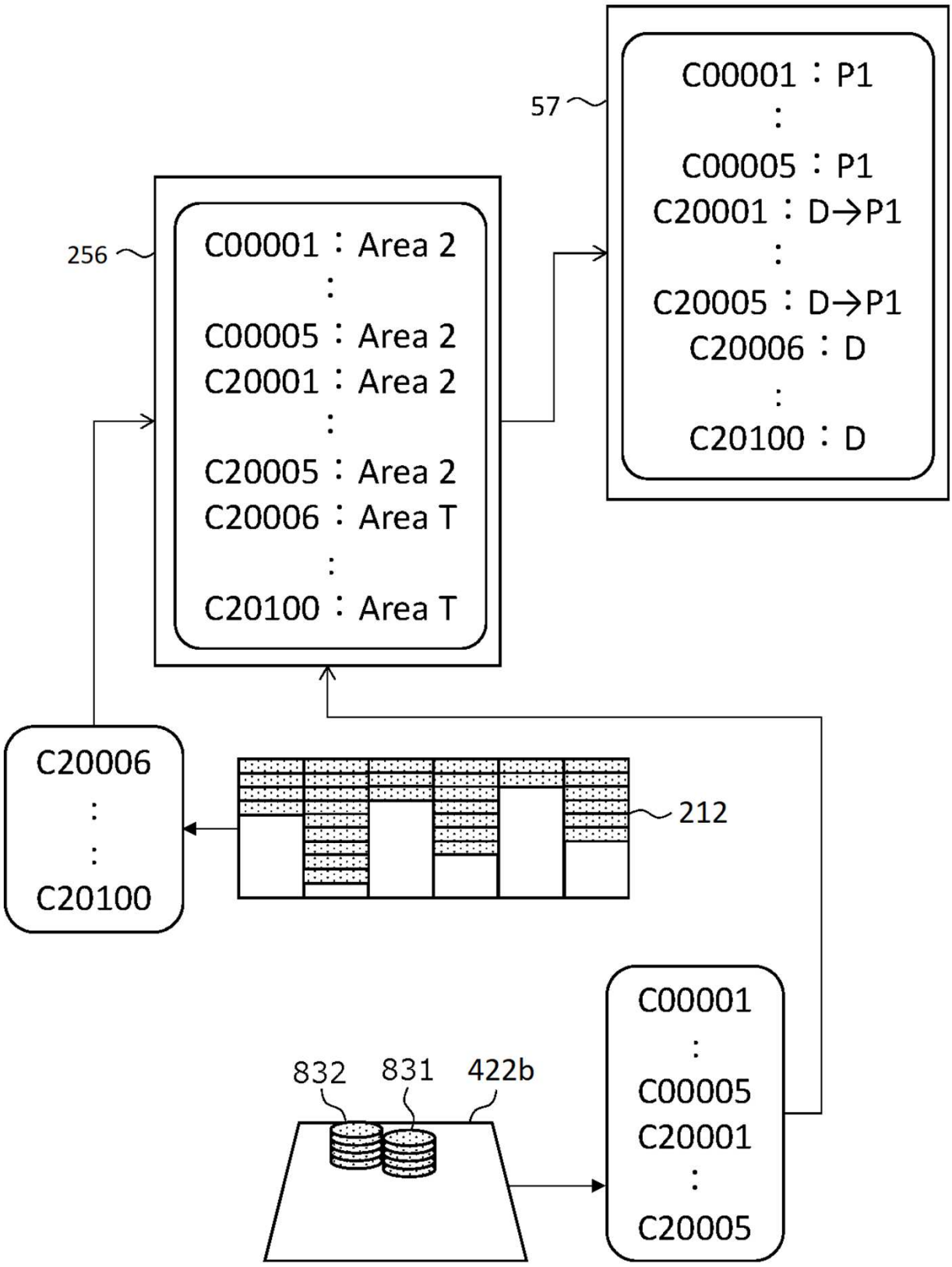

57
C00001 : P1
:
C00005 : P1
C20001 : D→P1
:
C20005 : D→P1
C20006 : D
:
C20100 : D
256
C00001 : Area 2
:
C00005 : Area 2
C20001 : Area 2
:
C20005 : Area 2
C20006 : Area T
:
C20100 : Area T
C20006
:
:
C20100
212
832
831
422b
C00001
:
C00005
C20001
:
C20005

FIG.33

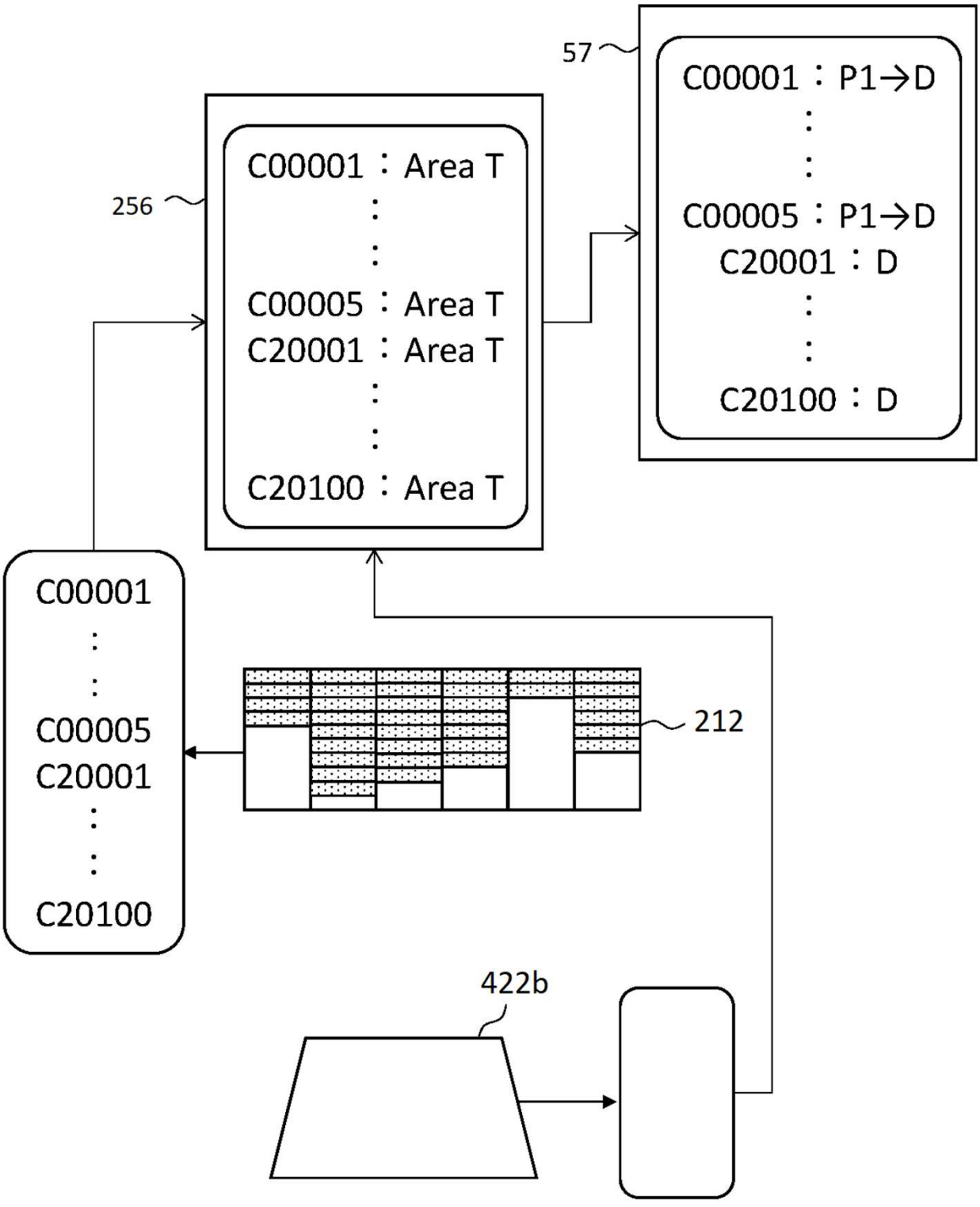

57
C00001 : P1→D
:
:
C00005 : P1→D
C20001 : D
:
:
C20100 : D
256
C00001 : Area T
:
:
C00005 : Area T
C20001 : Area T
:
:
C20100 : Area T
C00001
:
:
C00005
C20001
:
:
C20100
212
422b

FIG.34

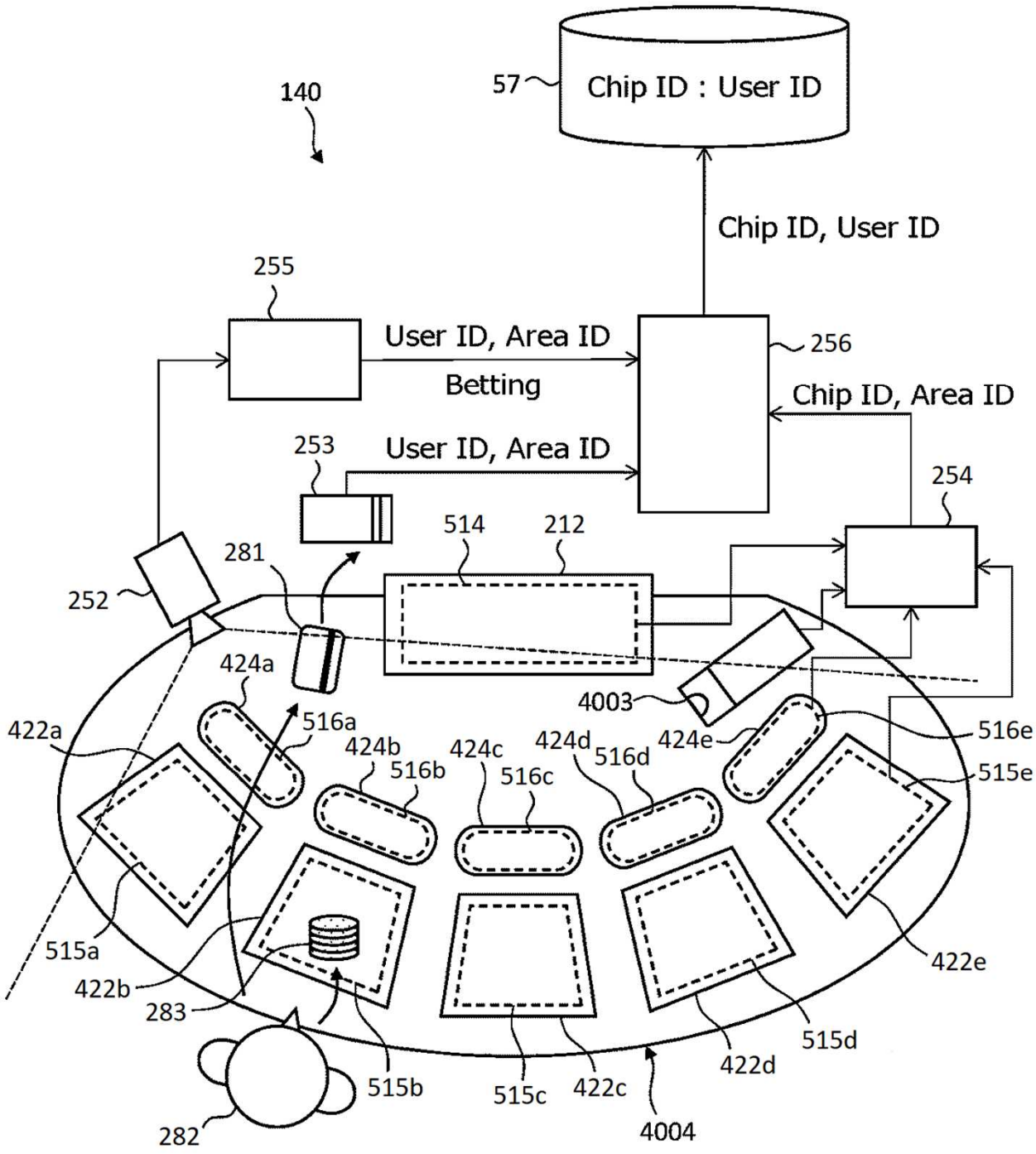

140
57
Chip ID : User ID
Chip ID, User ID
255
User ID, Area ID
Betting
256
Chip ID, Area ID
253
User ID, Area ID
254
514
212
281
252
424a
516a
422a
424b
516b
424c
516c
424d
516d
424e
4003
516e
515e
515a
422b
283
515b
515c
422c
4004
422d
515d
422e
282

FIG.35

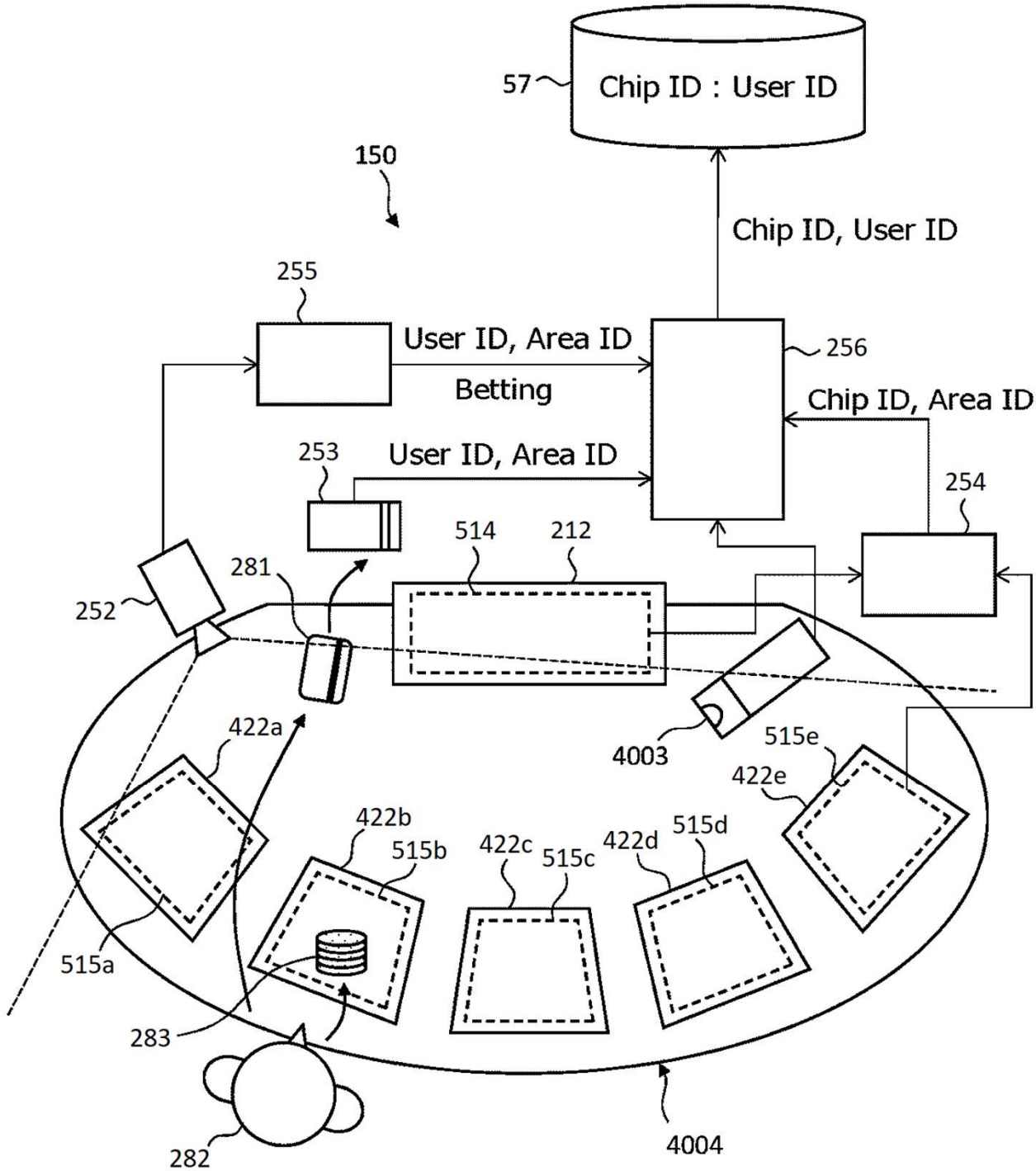

57
Chip ID : User ID
150
Chip ID, User ID
255
User ID, Area ID
Betting
256
Chip ID, Area ID
253
User ID, Area ID
254
514
212
281
252
422a
4003
515e
422e
422b
515b
422c
515c
422d
515d
515a
283
282
4004

FIG.36

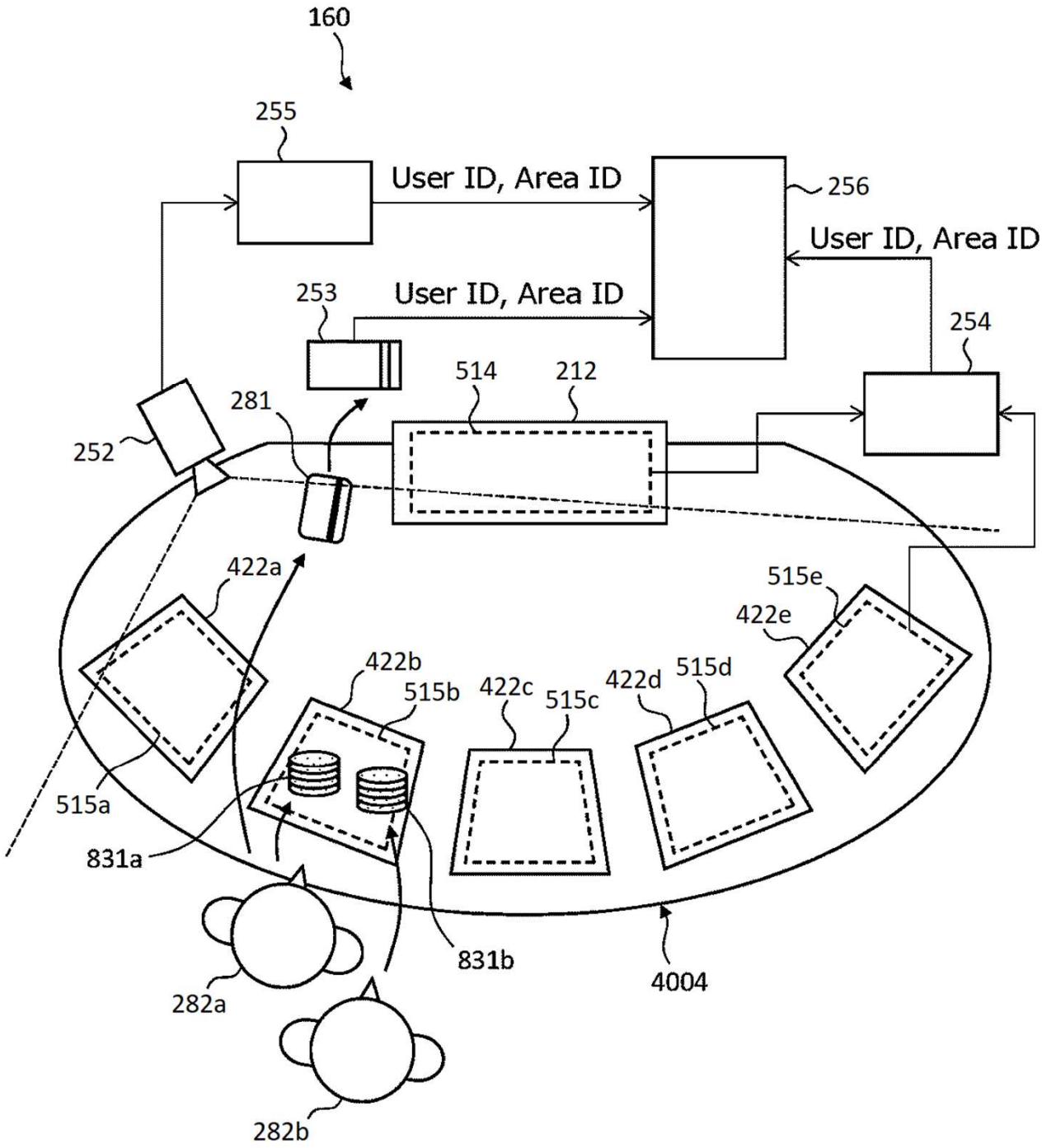

160
255
User ID, Area ID
256
User ID, Area ID
253
User ID, Area ID
254
514
212
281
252
422a
515e
422e
422b
515b
422c
515c
422d
515d
515a
831a
831b
4004
282a
282b

FIG.37

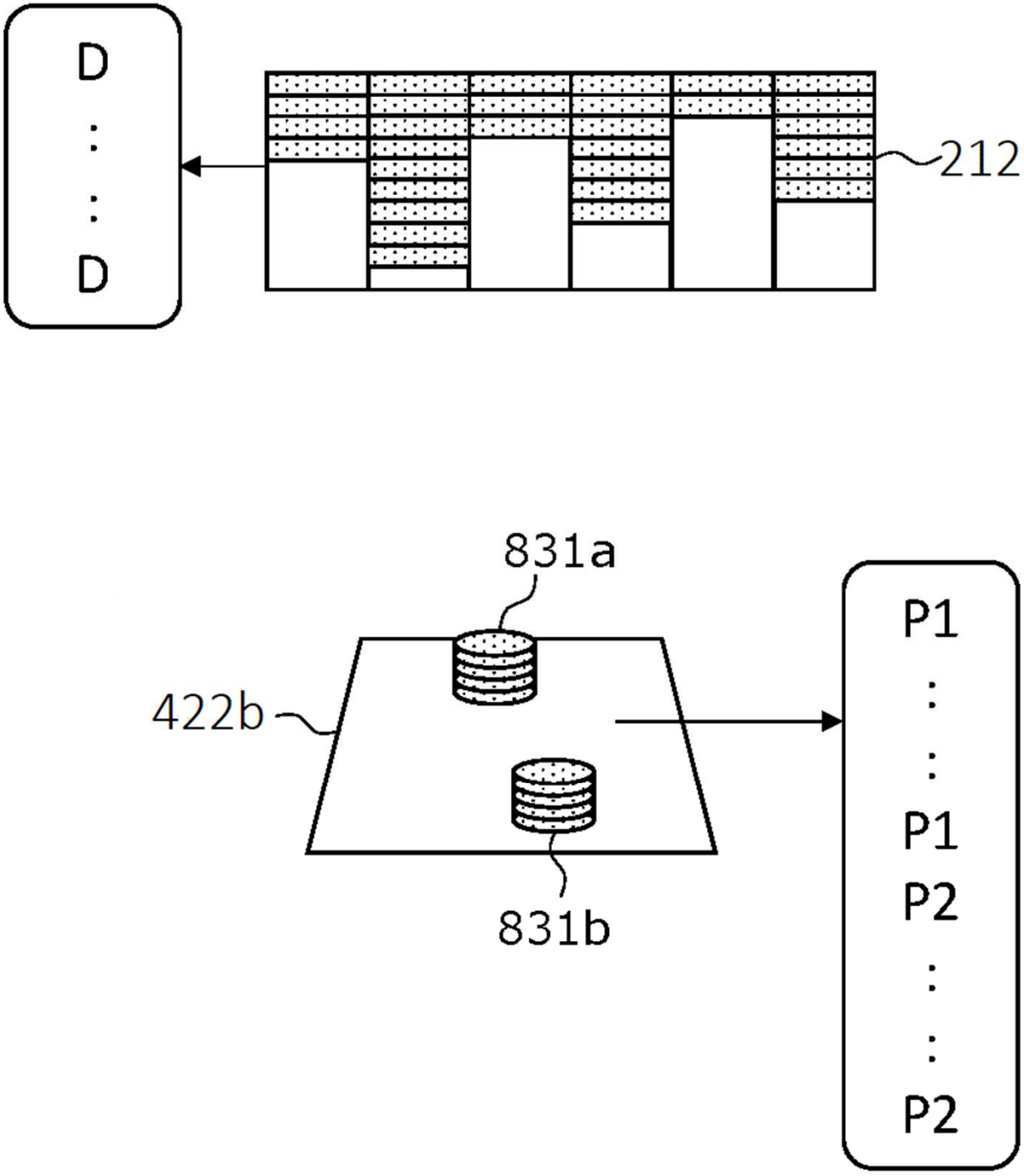


FIG.38

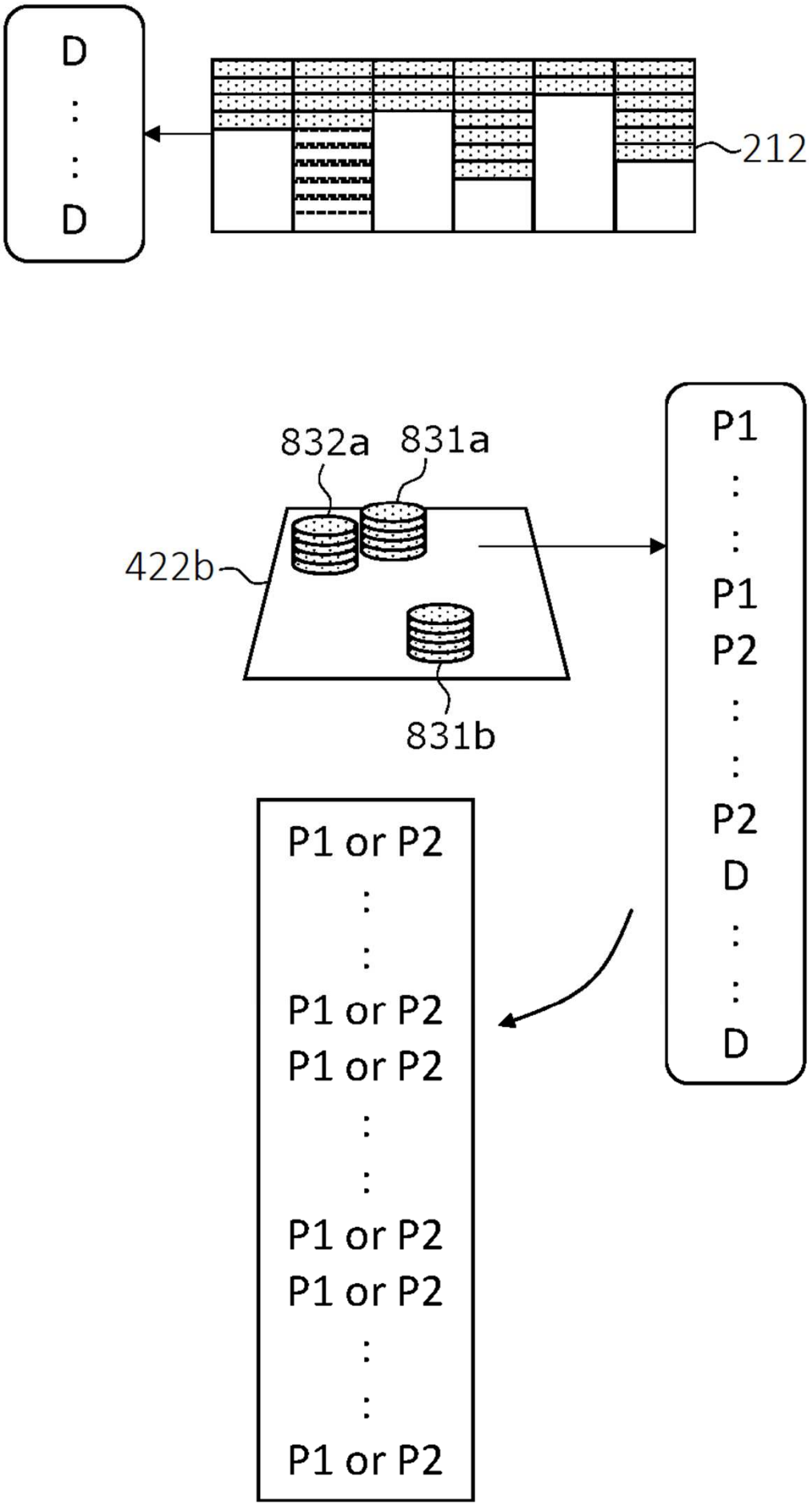

D
D
212
832a
831a
422b
831b
P1
P1
P2
P2
D
D
P1 or P2
P1 or P2
P1 or P2
P1 or P2
P1 or P2
P1 or P2

FIG.39

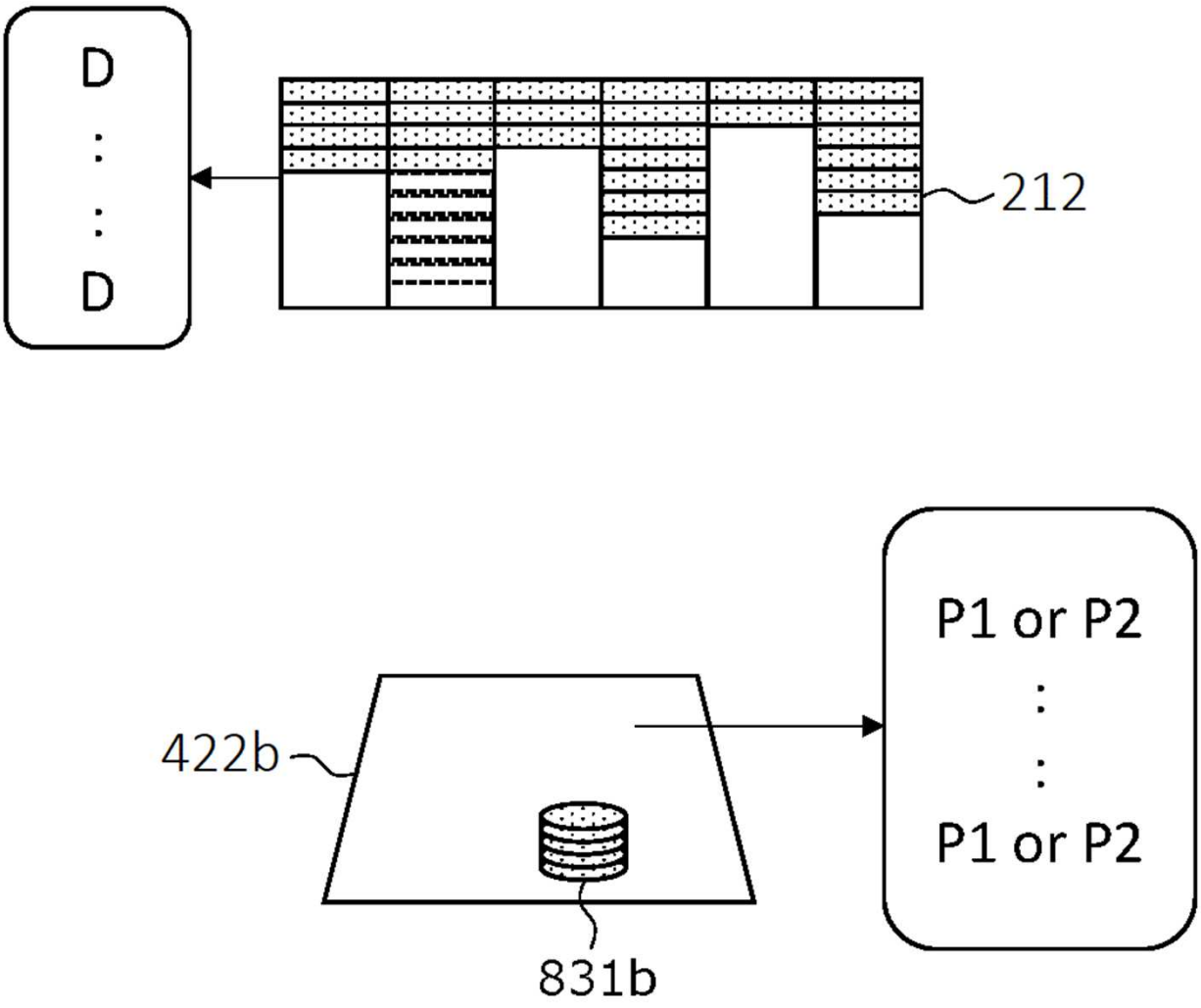


FIG.40

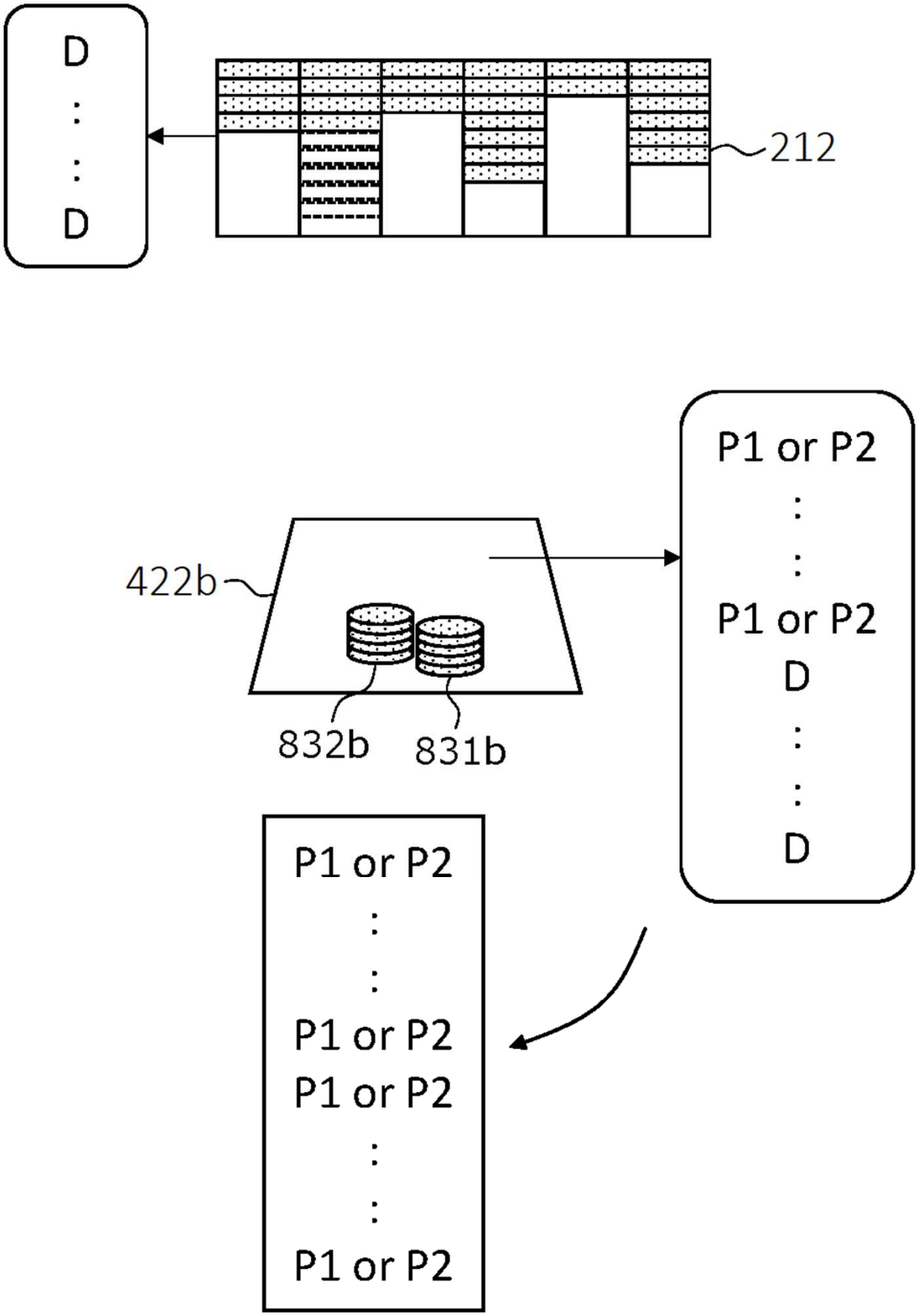

D
:
:
D
212
422b
832b
831b
P1 or P2
:
:
P1 or P2
D
:
:
D
P1 or P2
:
:
P1 or P2
P1 or P2
:
:
P1 or P2

FIG.41

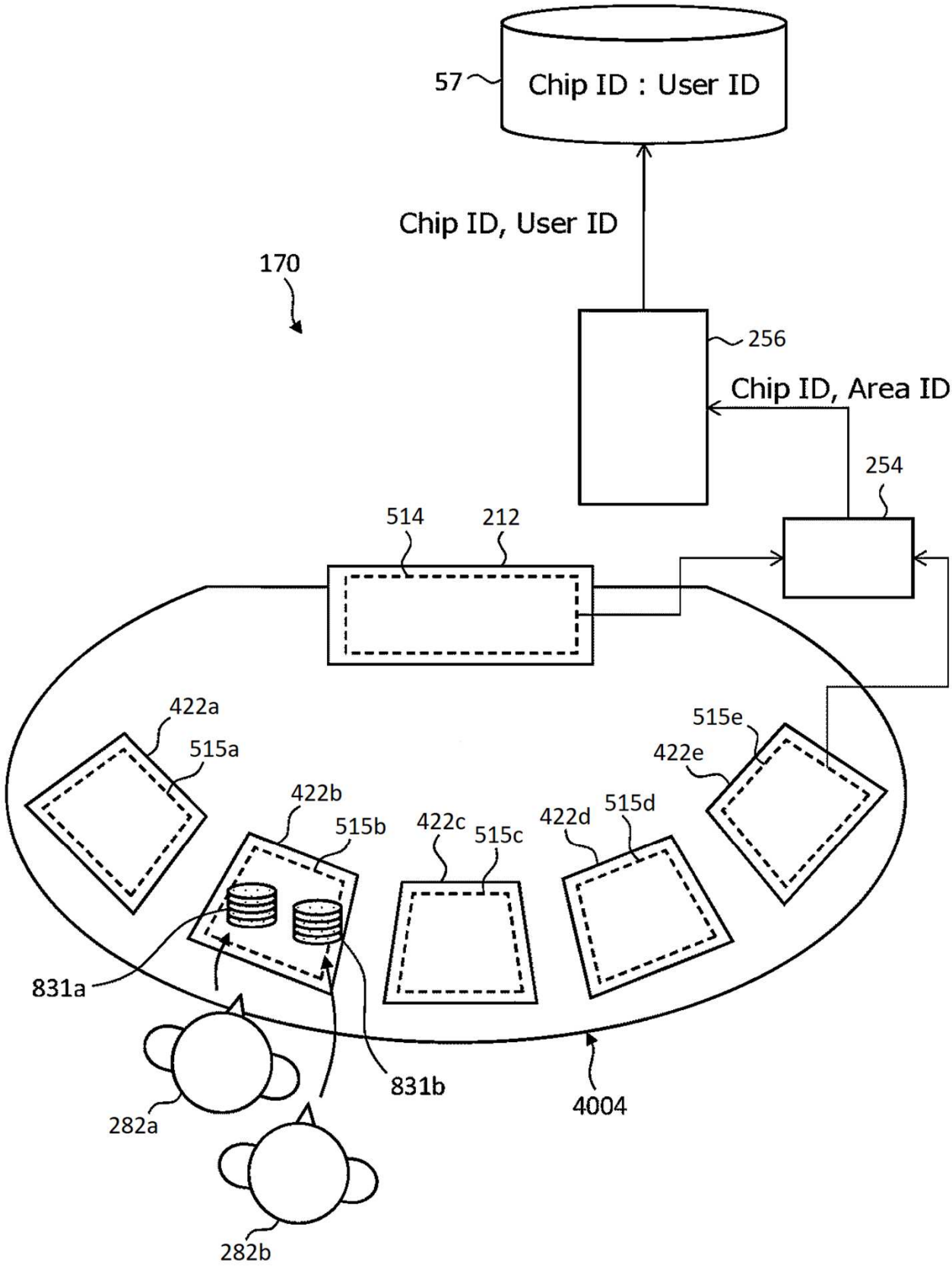

57
Chip ID : User ID
Chip ID, User ID
170
256
Chip ID, Area ID
254
514
212
422a
515a
422b
515b
422c
515c
422d
515d
515e
422e
831a
831b
4004
282a
282b

FIG.42

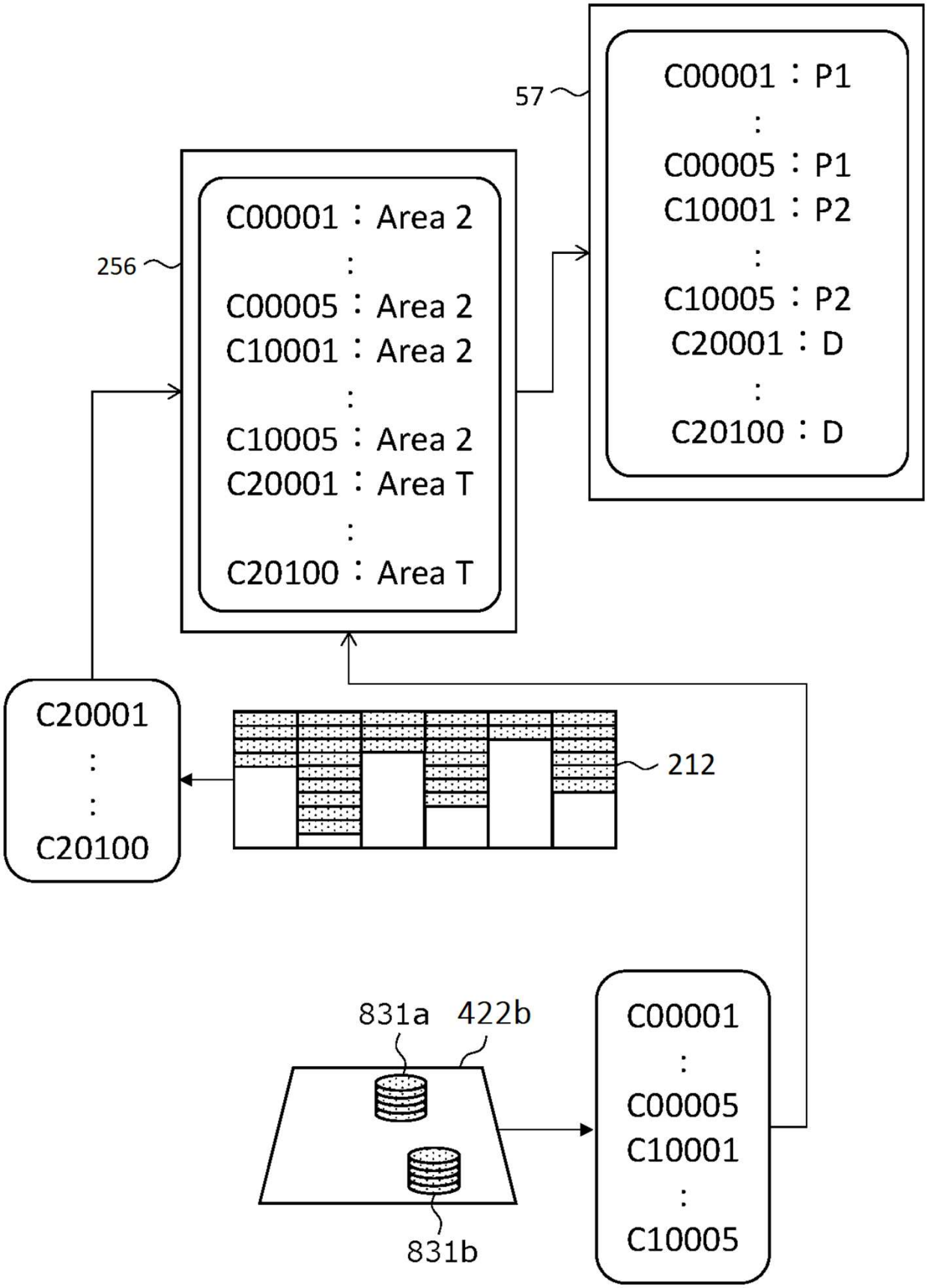

57
C00001 : P1
:
C00005 : P1
C10001 : P2
:
C10005 : P2
C20001 : D
:
C20100 : D
256
C00001 : Area 2
:
C00005 : Area 2
C10001 : Area 2
:
C10005 : Area 2
C20001 : Area T
:
C20100 : Area T
C20001
:
:
C20100
212
831a
422b
831b
C00001
:
C00005
C10001
:
C10005

FIG.43

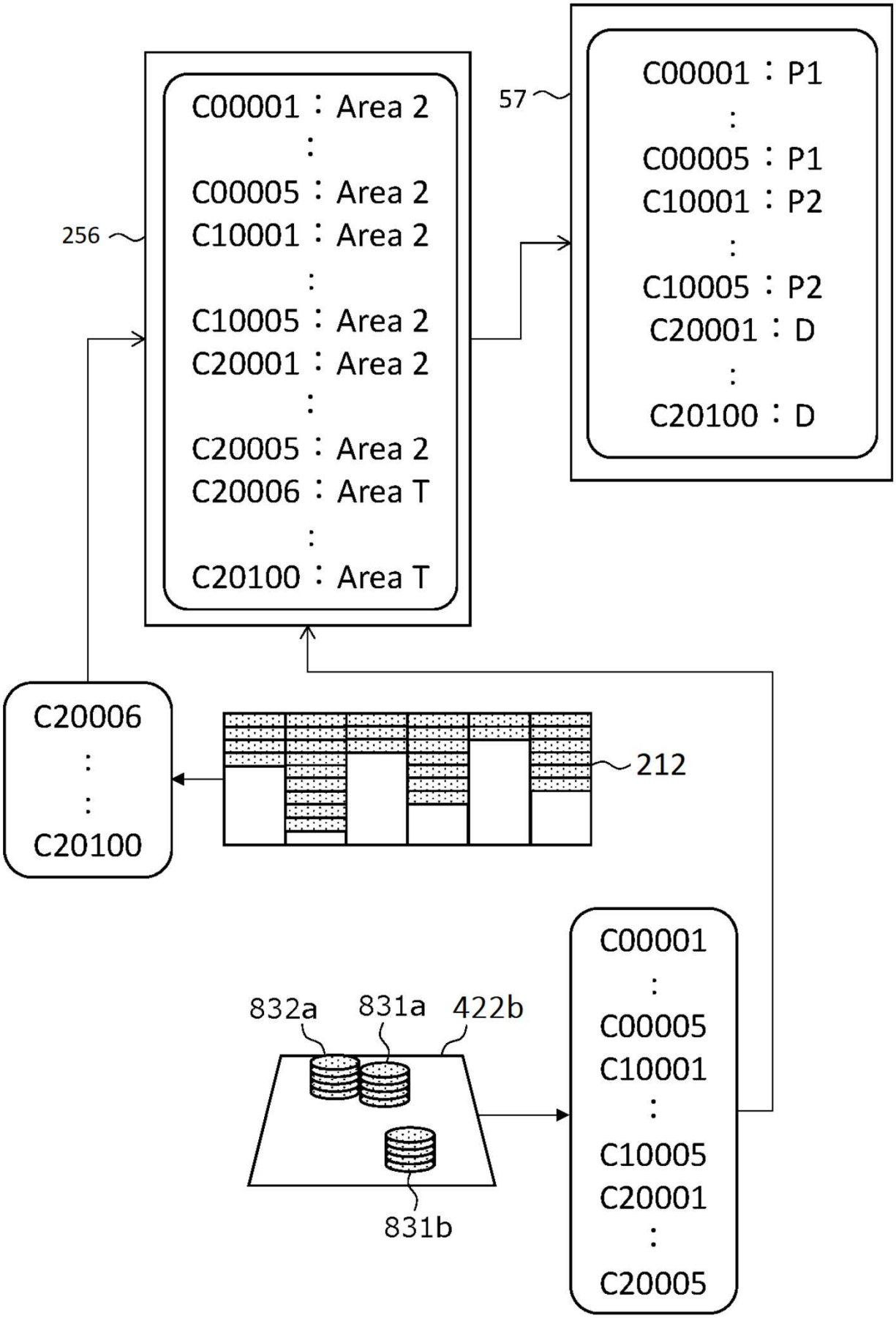

C00001 : Area 2
:
C00005 : Area 2
C10001 : Area 2
:
C10005 : Area 2
C20001 : Area 2
:
C20005 : Area 2
C20006 : Area T
:
C20100 : Area T
256
57
C00001 : P1
:
C00005 : P1
C10001 : P2
:
C10005 : P2
C20001 : D
:
C20100 : D
C20006
:
:
C20100
212
832a
831a
422b
831b
C00001
:
C00005
C10001
:
C10005
C20001
:
C20005

FIG.44

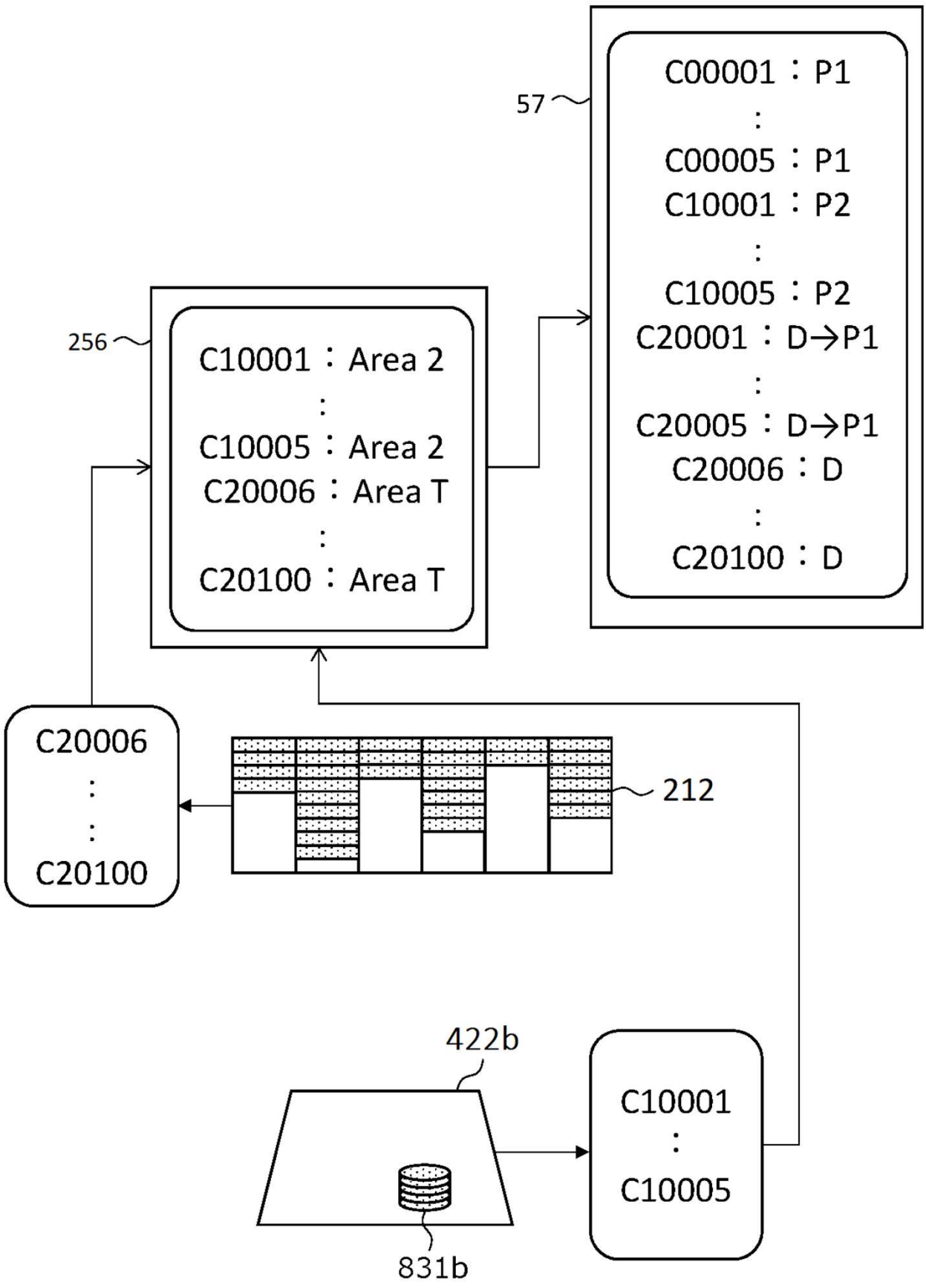

57
C00001 : P1
:
C00005 : P1
C10001 : P2
:
C10005 : P2
C20001 : D→P1
:
C20005 : D→P1
C20006 : D
:
C20100 : D
256
C10001 : Area 2
:
C10005 : Area 2
C20006 : Area T
:
C20100 : Area T
C20006
:
:
C20100
212
422b
C10001
:
C10005
831b

FIG.45

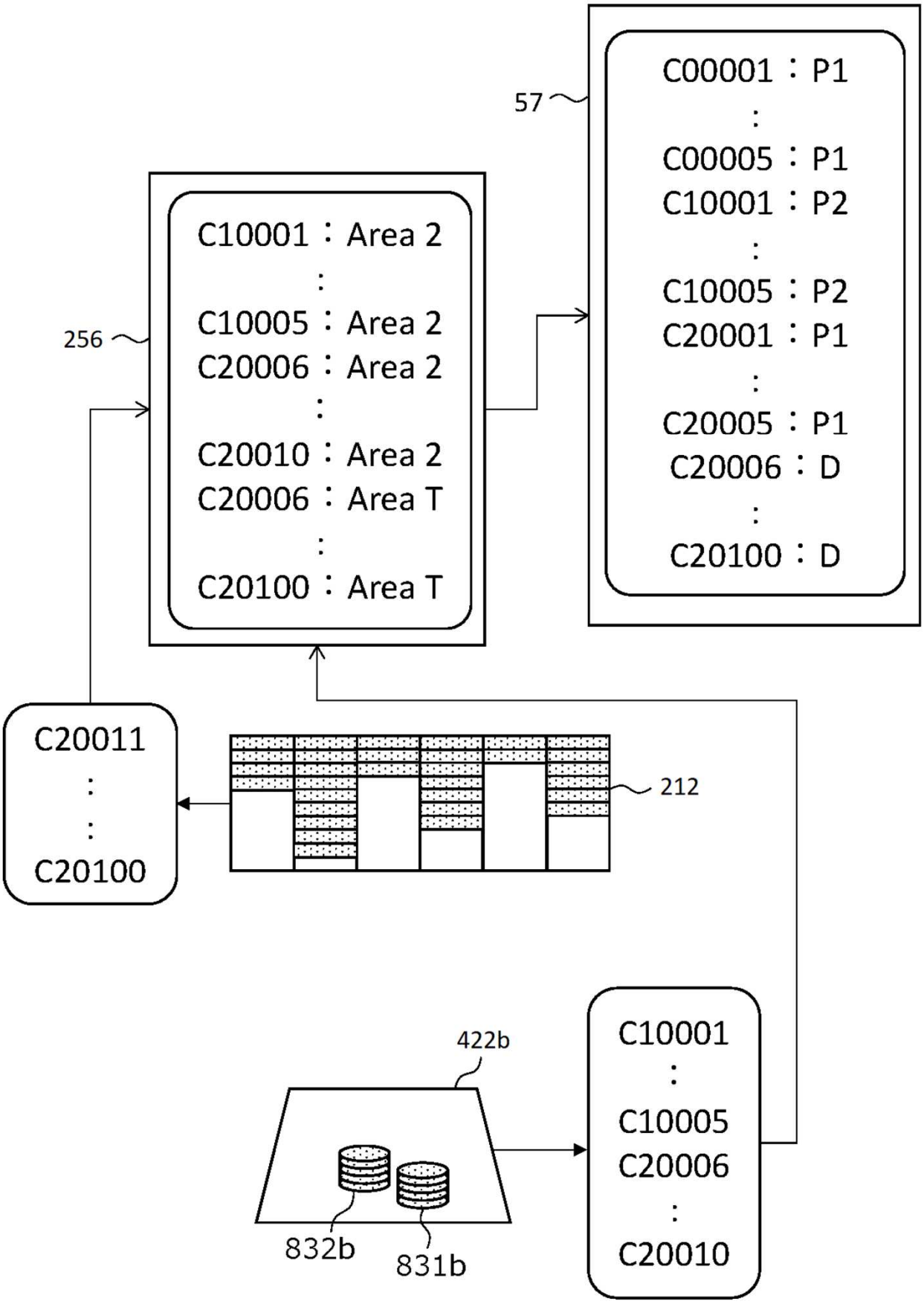

57
C00001 : P1
:
C00005 : P1
C10001 : P2
:
C10005 : P2
C20001 : P1
:
C20005 : P1
C20006 : D
:
C20100 : D
256
C10001 : Area 2
:
C10005 : Area 2
C20006 : Area 2
:
C20010 : Area 2
C20006 : Area T
:
C20100 : Area T
C20011
:
:
C20100
212
422b
832b
831b
C10001
:
C10005
C20006
:
C20010

FIG.46

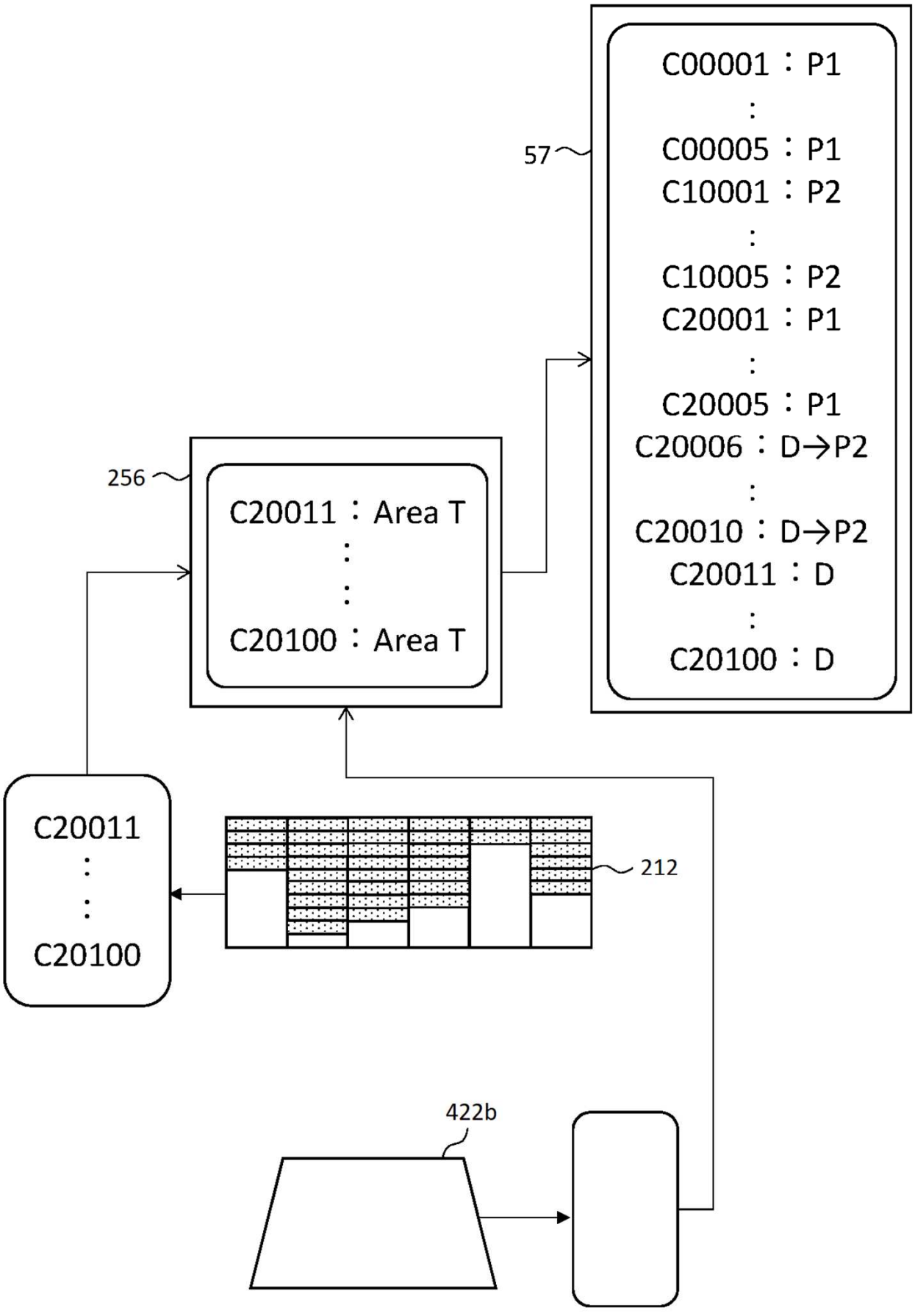

C00001 : P1
:
C00005 : P1
C10001 : P2
:
C10005 : P2
C20001 : P1
:
C20005 : P1
C20006 : D→P2
:
C20010 : D→P2
C20011 : D
:
C20100 : D
57
256
C20011 : Area T
:
:
C20100 : Area T
C20011
:
:
C20100
212
422b

FIG.47

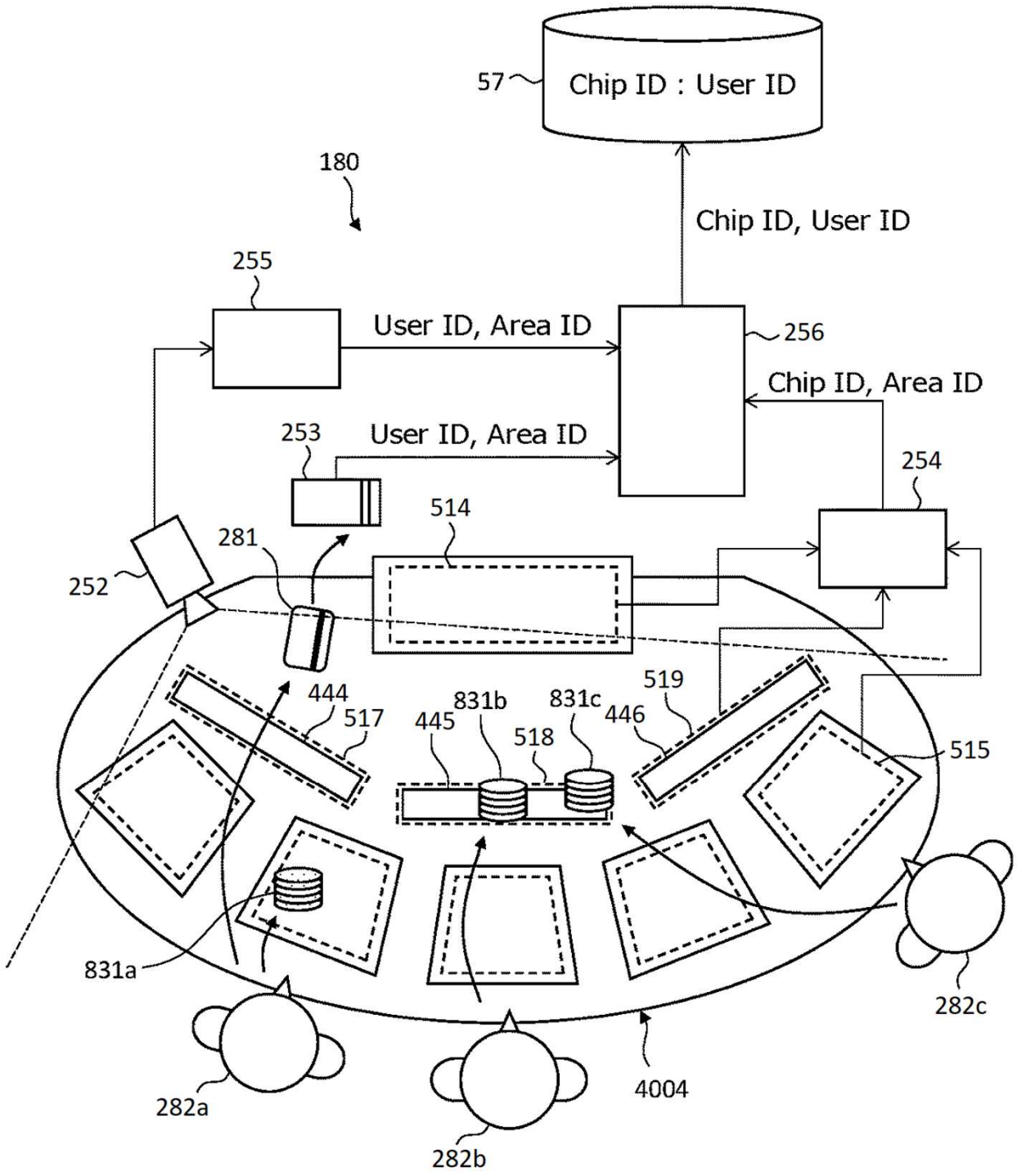

57
Chip ID : User ID
180
Chip ID, User ID
255
User ID, Area ID
256
Chip ID, Area ID
253
User ID, Area ID
254
514
281
252
444
517
831b
831c
445
518
446
519
515
831a
282c
282a
4004
282b

FIG.48

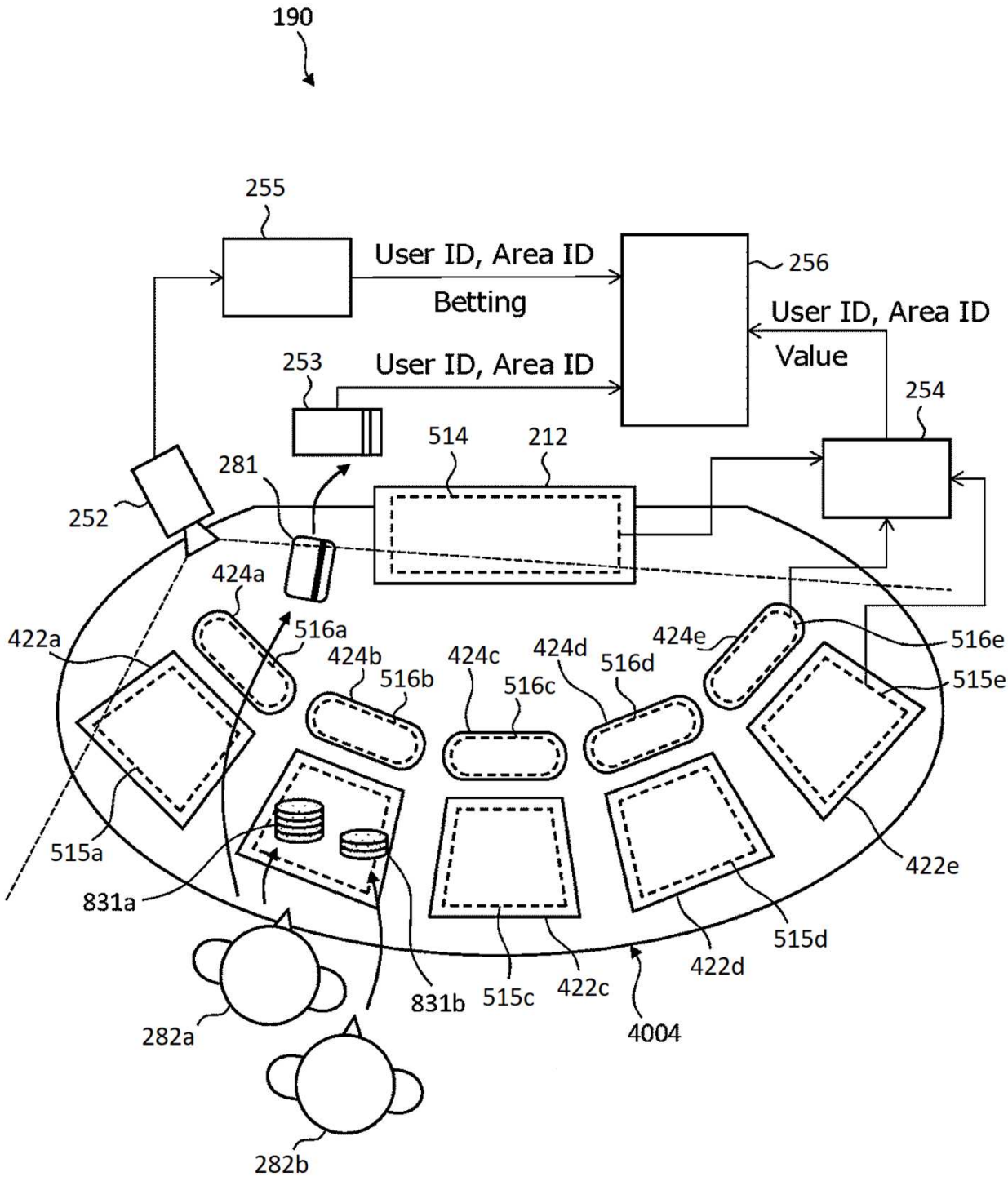
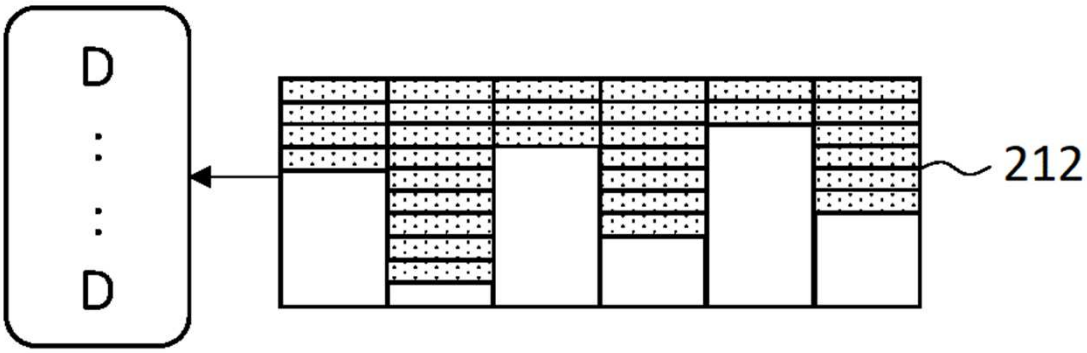

190
255
User ID, Area ID
Betting
256
User ID, Area ID
Value
253
User ID, Area ID
254
514
212
281
252
424a
516a
422a
424b
516b
424c
516c
424d
516d
424e
516e
515e
515a
831a
422e
515d
422d
831b
515c
422c
4004
282a
282b

FIG.49



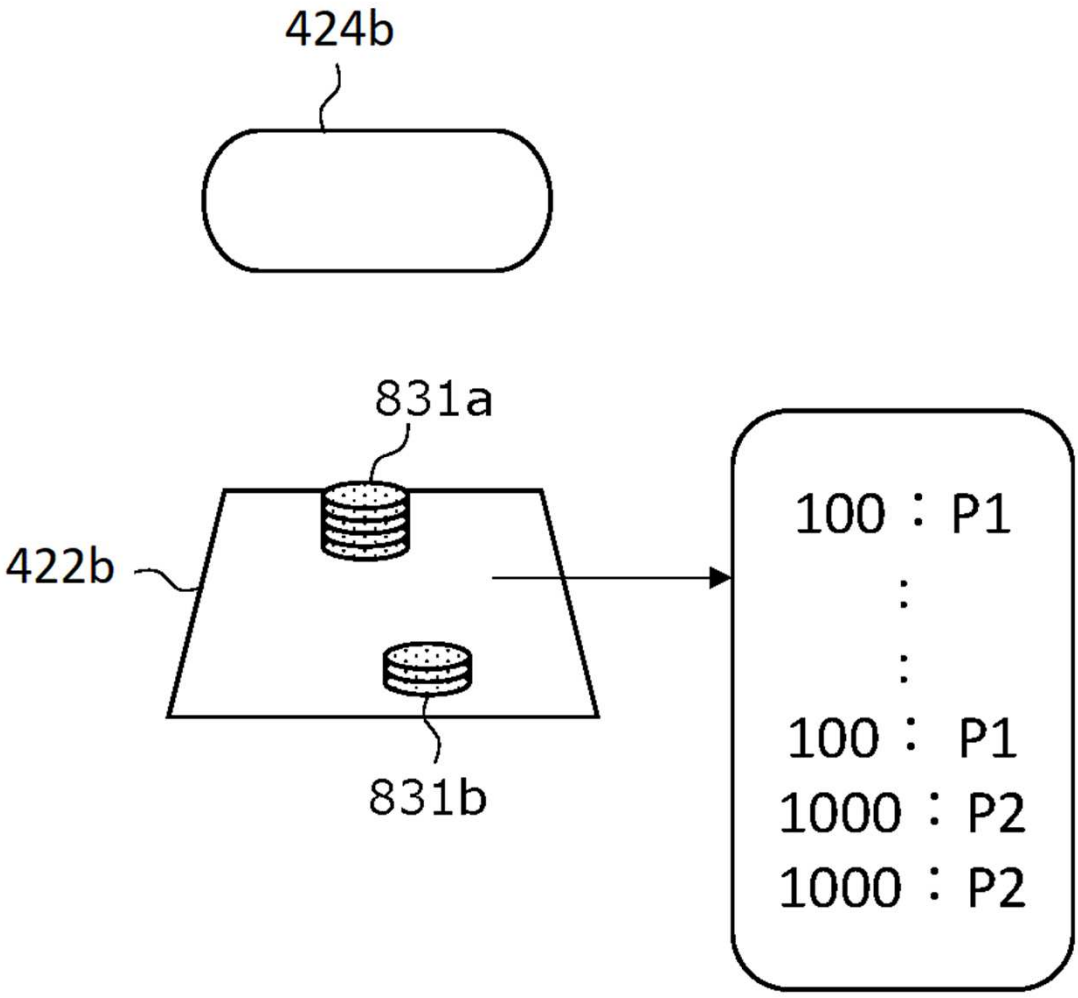


FIG.50

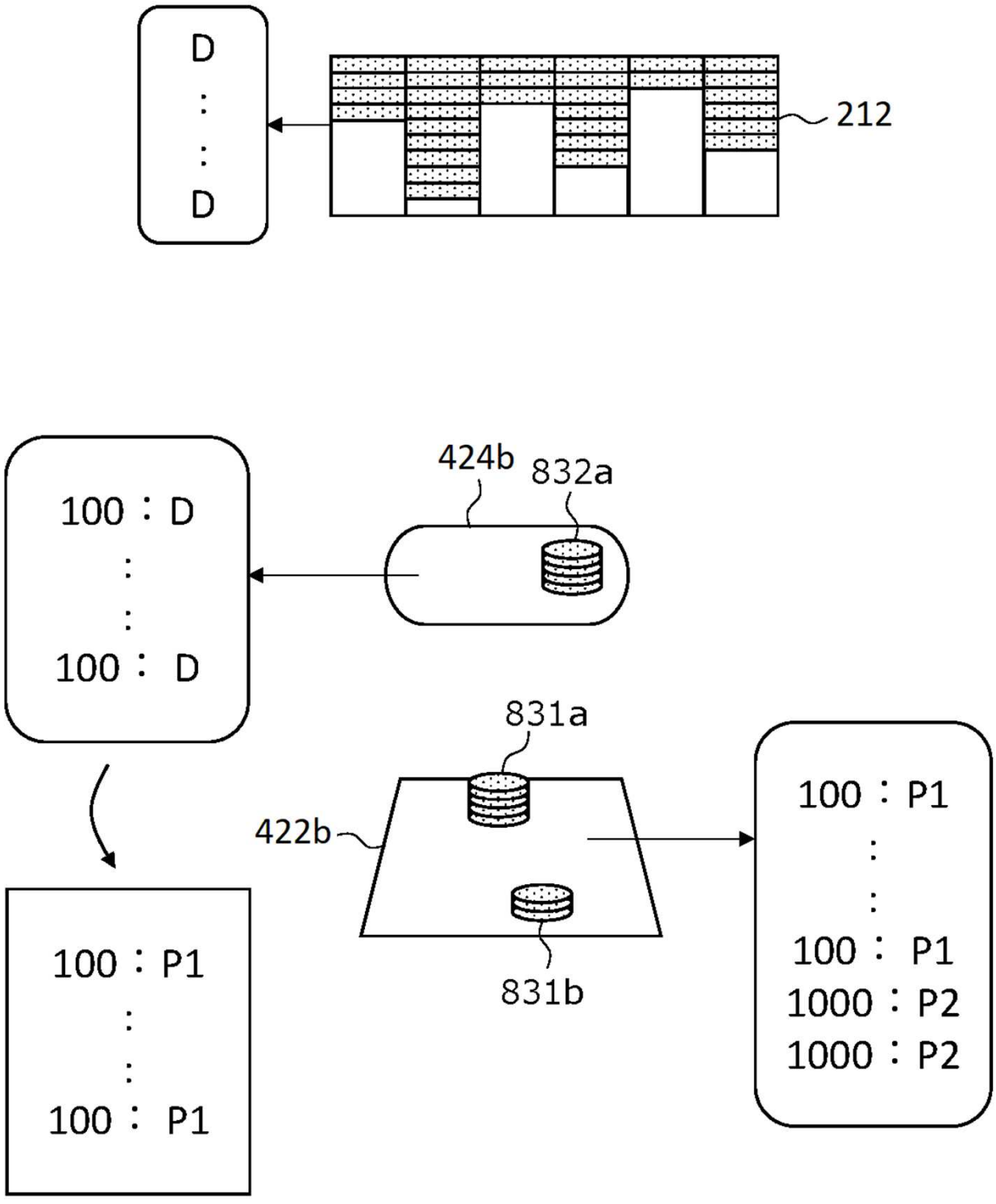

D
:
:
D
212
100 : D
:
:
100 : D
424b
832a
831a
422b
831b
100 : P1
:
:
100 : P1
100 : P1
:
:
100 : P1
1000 : P2
1000 : P2

FIG.51

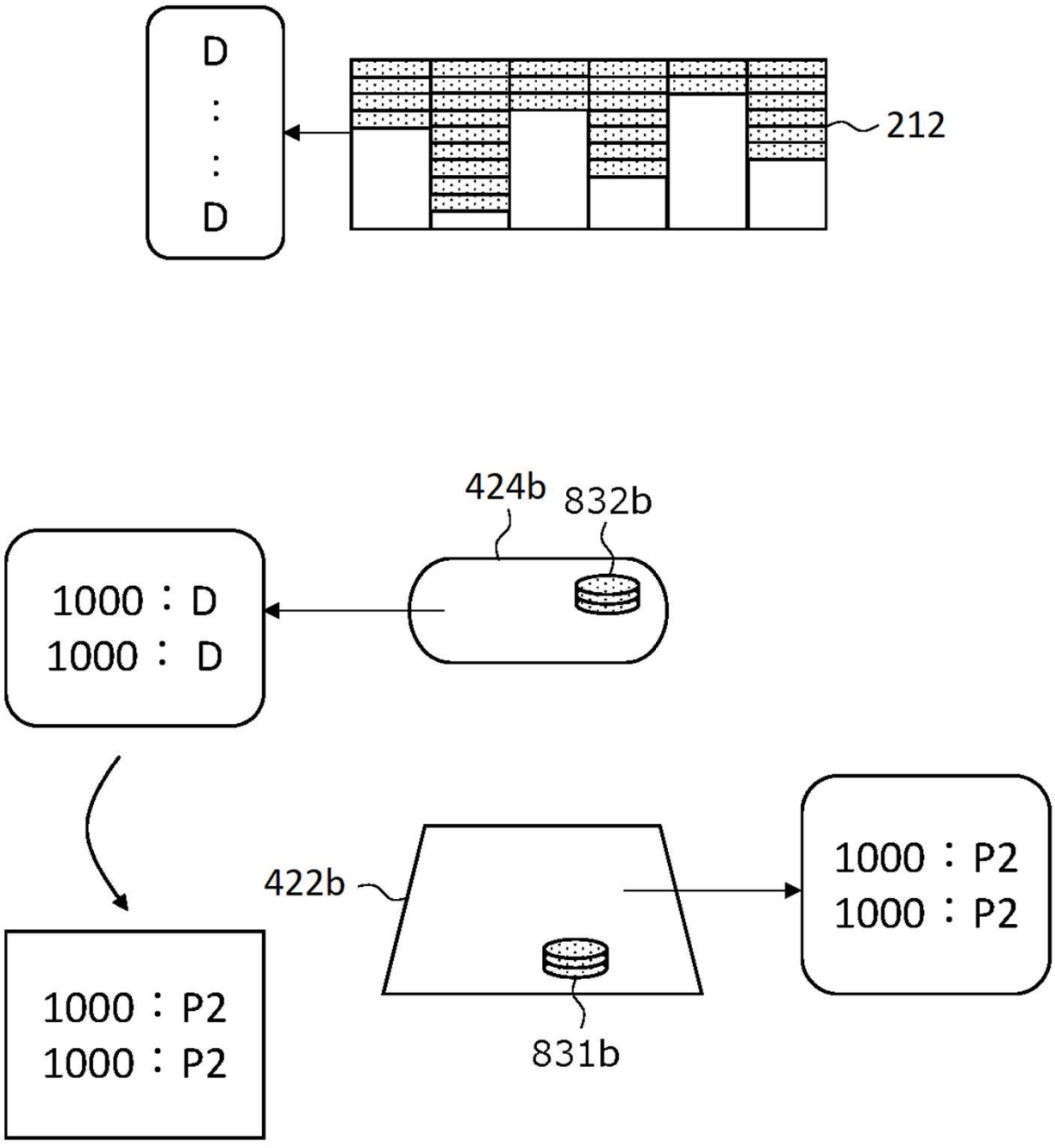

D
:
:
D
212
424b
832b
1000 : D
1000 : D
422b
1000 : P2
1000 : P2
1000 : P2
1000 : P2
831b

FIG.52

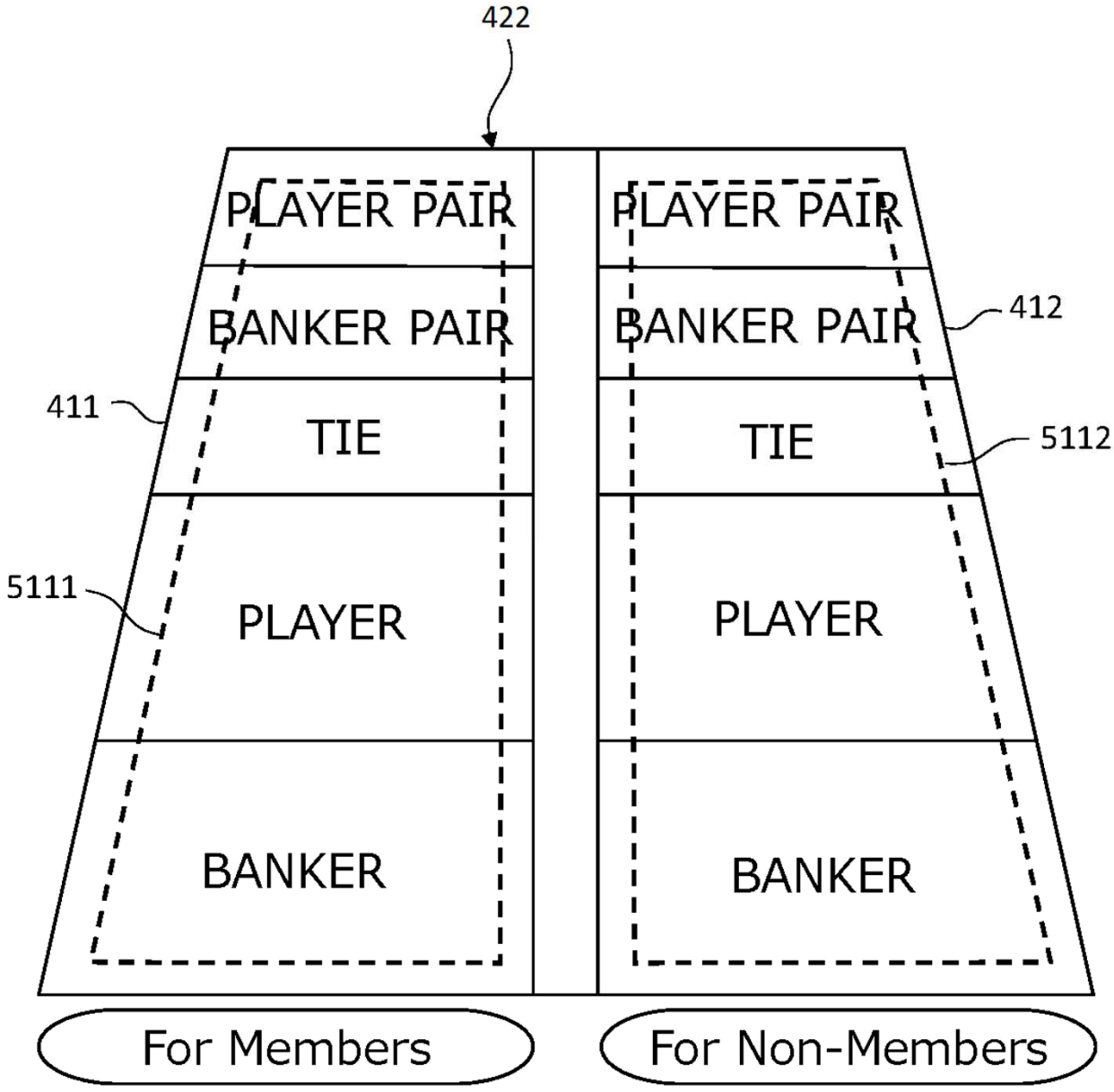

422
PLAYER PAIR
PLAYER PAIR
BANKER PAIR
BANKER PAIR
412
411
TIE
TIE
5112
5111
PLAYER
PLAYER
BANKER
BANKER
For Members
For Non-Members

FIG.53

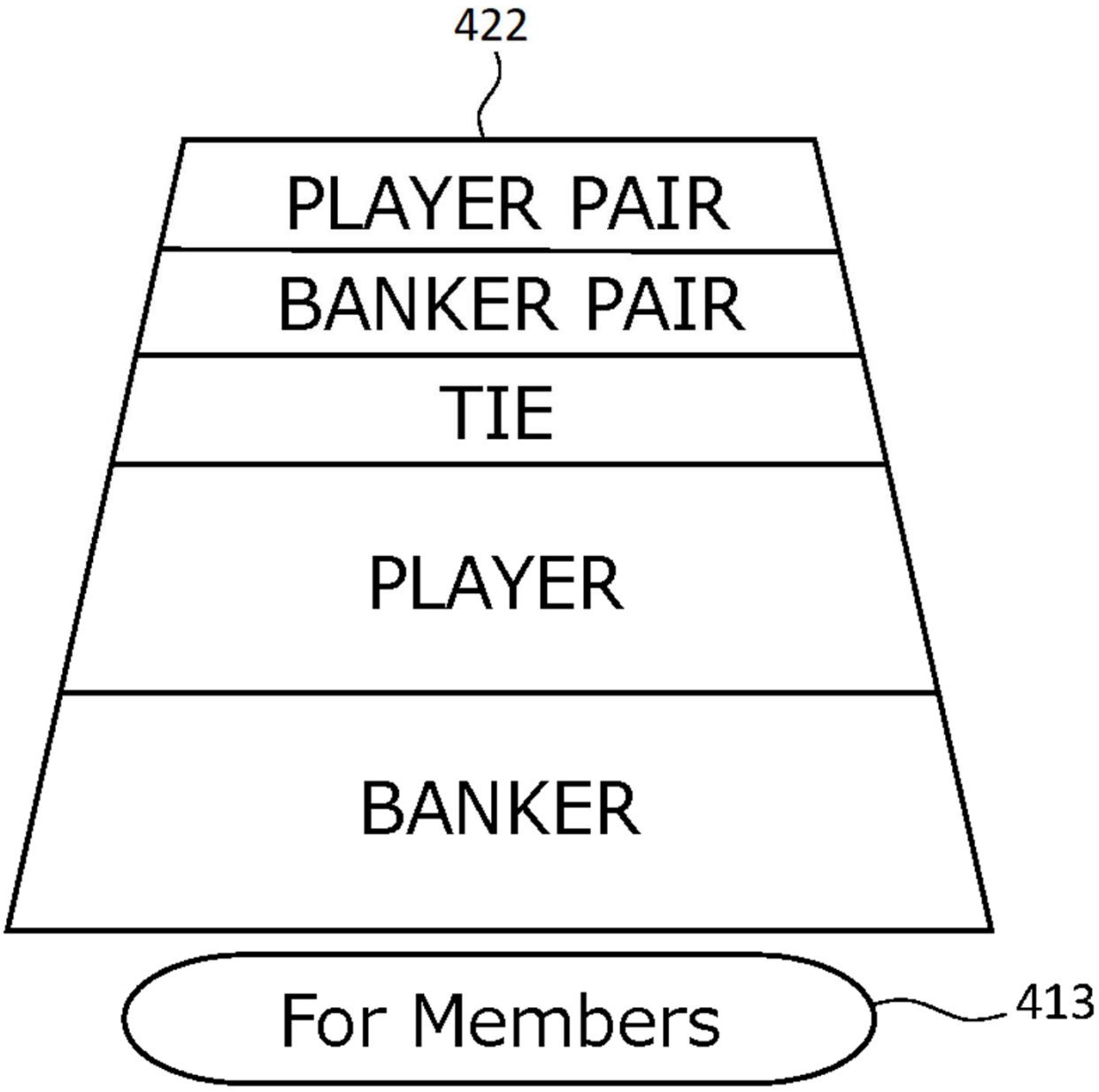

422
PLAYER PAIR
BANKER PAIR
TIE
PLAYER
BANKER
For Members
413

FIG.54A

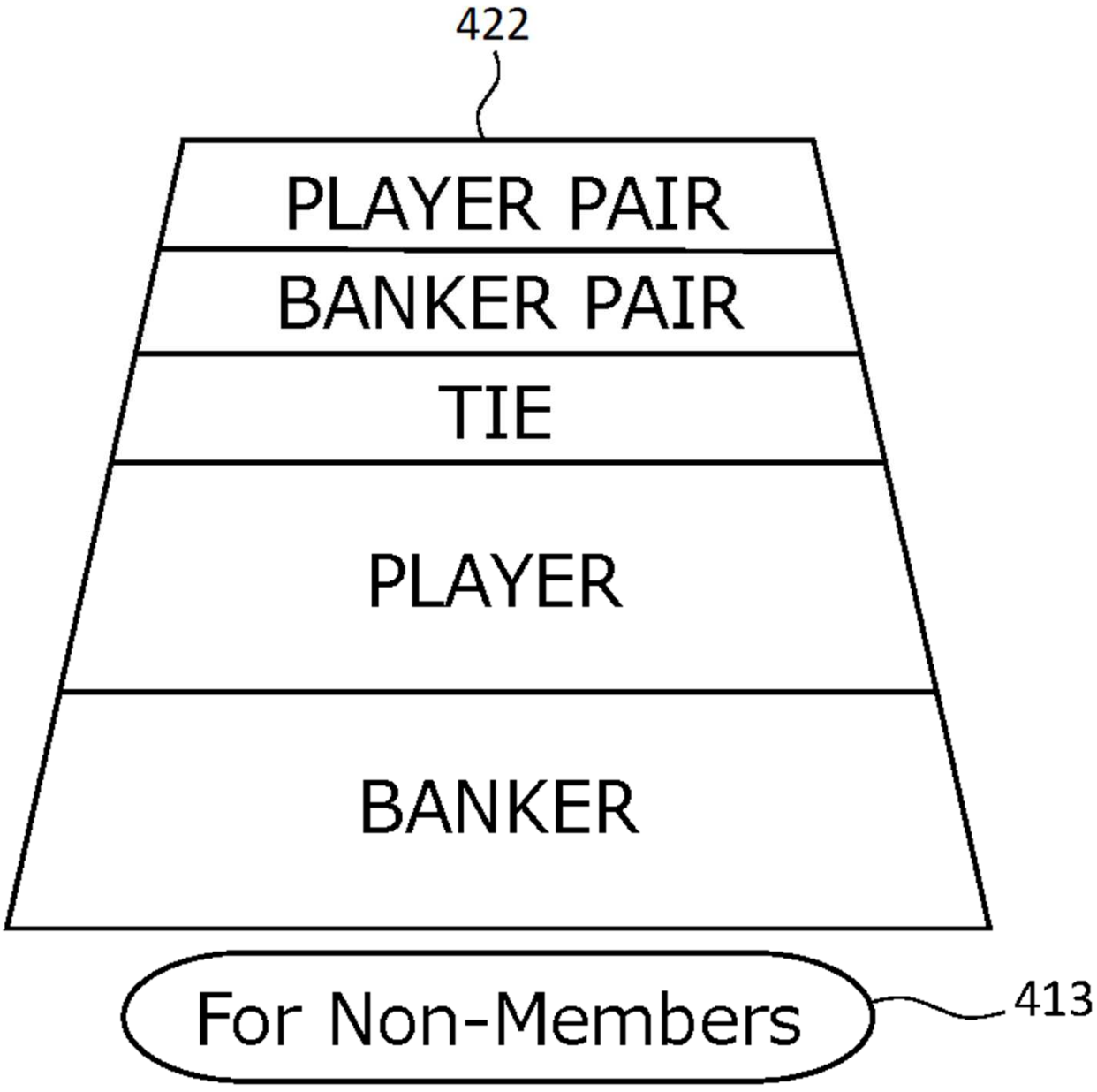

422
PLAYER PAIR
BANKER PAIR
TIE
PLAYER
BANKER
For Non-Members
413

FIG.54B