

SYSTEMS AND METHODS FOR CORRECTION OF IMPACT OF WAFER TILT ON MISREGISTRATION MEASUREMENTS

FIELD OF THE INVENTION

[0001] The present invention relates generally to metrology and more particularly
5 to misregistration measurements on semiconductor wafers.

BACKGROUND OF THE INVENTION

[0002] Various systems and methods for the measurement of misregistration in
the manufacture of semiconductor wafers are known in the art.

10 SUMMARY OF THE INVENTION

[0003] The present invention seeks to provide novel systems and methods for the
correction of misregistration measurements performed on semiconductor wafers for the
impact thereon of localized wafer tilt.

[0004] There is thus provided in accordance with a preferred embodiment of the
15 present invention a method for correcting misregistration measurements of a
semiconductor wafer for errors therein arising from tilt of the wafer including measuring,
for at least one location on a wafer, a difference between a Tool Induced Shift (TIS) of a
metrology device in a first illumination arrangement with respect to the wafer wherein a
surface of the wafer is generally orthogonally illuminated by an illumination source of
20 the metrology device and a TIS of the metrology device in a second illumination
arrangement with respect to the wafer, wherein the surface is obliquely illuminated by the
illumination source, and correcting a misregistration measurement measured by the
metrology device at the at least one location for errors therein arising from tilt of the wafer
at the location by subtracting from the misregistration measurement a weighted value of
25 the difference between the TIS in the first and second illumination arrangements.

[0005] Preferably, the difference between the TIS in the first and second
illumination arrangements includes a signature profile varying as a function of a
parameter of the metrology device.

[0006] Preferably, the metrology device illuminates the wafer by a multiplicity of wavelengths and the difference in TIS of the metrology device is measured as a function of the multiplicity of wavelengths.

[0007] Preferably, the measuring the difference in TIS is performed at a plurality of locations N_s on the wafer and the difference in TIS of the metrology device is measured as a function of the multiplicity of wavelengths for each of the plurality of locations.

[0008] Preferably, the weighted value of the difference between the TIS in the first and second illumination arrangements is calculated by multiplying the difference between the TIS in the first and second illumination arrangements by a weighting coefficient.

[0009] Preferably, the weighting coefficient is calculated based on only variable parts of the misregistration measurement and the difference between the TIS in the first and second illumination arrangements.

[0010] Preferably, the weighting coefficient is calculated in accordance with

$$\alpha(N_s) = \frac{\sum_{\lambda} \widetilde{MIS}_{\text{measured}}(N_s, \lambda) \cdot \widetilde{\Delta TIS}(\lambda)}{\sum_{\lambda} \widetilde{\Delta TIS}^2(\lambda)}$$

wherein $\alpha(N_s)$ is the weighting coefficient, $\widetilde{MIS}_{\text{measured}}(N_s, \lambda)$ is the variable part of the misregistration measurement and $\widetilde{\Delta TIS}(\lambda)$ is the variable part of the difference between the TIS in the first and second illumination arrangements.

[0011] Preferably, the variable part of the misregistration measurement is calculated in accordance with:

$$\widetilde{MIS}_{\text{measured}}(N_s, \lambda) = MIS_{\text{measured}}(N_s, \lambda) - \overline{MIS_{\text{measured}}(N_s)}$$

wherein $MIS_{\text{measured}}(N_s, \lambda)$ is the misregistration measurement and $\overline{MIS_{\text{measured}}(N_s)}$ is the misregistration measurement averaged over the multiplicity of illumination wavelengths.

[0012] Preferably, the subtracting from the misregistration measurement a weighted value of the difference between the TIS in the first and second illumination arrangements is performed in accordance with:

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$$\text{MIS}_{\text{corrected}}(N_s) = \text{MIS}_{\text{measured}}(N_s, \lambda) - \alpha(N_s) \cdot \Delta TIS(\lambda)$$

wherein $\text{MIS}_{\text{corrected}}(N_s)$ is a corrected value of the misregistration measurement and $\Delta TIS(\lambda)$ is the difference between the TIS in the first and second illumination arrangements.

10 [0013] In accordance with one preferred embodiment of the method of the present invention, the method also includes optimizing an operating parameter of the metrology device based on the difference in TIS of the metrology device.

[0014] There is also provided in accordance with another preferred embodiment of the present invention a system for correcting misregistration measurements on a
15 semiconductor wafer for errors therein arising from tilt of the wafer including an illumination source forming part of a metrology device and operative to illuminate a wafer in at least a first illumination arrangement wherein a surface of the wafer is generally orthogonally illuminated by the illumination source and a second illumination arrangement wherein the surface is obliquely illuminated by the illumination source, a
20 TIS calculator operative to find a difference between a TIS of the metrology device in the first and second illumination arrangements and a wafer tilt corrector operative to correct a misregistration measurement by the metrology device at a location on the wafer for errors therein arising due to tilt of the wafer at the location, based on subtracting from the misregistration measurement a weighted value of the difference between the TIS of
25 the metrology device in the first and second illumination arrangements.

[0015] Preferably, the difference between the TIS in the first and second illumination arrangements includes a signature profile varying as a function of a parameter of the metrology device.

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[0016] Preferably, the illumination source is operative to illuminate the wafer by a multiplicity of wavelengths and the TIS calculator is operative to find the difference in TIS of the metrology device as a function of the multiplicity of wavelengths.

- 5 [0017] Preferably, the TIS calculator is operative to find the difference in TIS at a plurality of locations N_s on the wafer, as a function of the multiplicity of wavelengths for each of the plurality of locations.

[0018] Preferably, the wafer tilt corrector is operative to calculate the weighted
10 value of the difference between the TIS in the first and second illumination arrangements by multiplying the difference between the TIS in the first and second illumination arrangements by a weighting coefficient.

[0019] Preferably, the wafer tilt corrector is operative to calculate the weighting
15 coefficient based on only variable parts of the misregistration measurement and the difference between the TIS in the first and second illumination arrangements.

[0020] Preferably, the wafer tilt corrector is operative to calculate the weighting coefficient in accordance with:

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$$\alpha(N_s) = \frac{\sum_{\lambda} \widetilde{MIS}_{\text{measured}}(N_s, \lambda) \cdot \widetilde{\Delta TIS}(\lambda)}{\sum_{\lambda} \widetilde{\Delta TIS}^2(\lambda)}$$

wherein $\alpha(N_s)$ is the weighting coefficient, $\widetilde{MIS}_{\text{measured}}(N_s, \lambda)$ is the variable part of the misregistration measurement and $\widetilde{\Delta TIS}(\lambda)$ is the variable part of the difference between TIS in the first and second illumination arrangements.

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[0021] Preferably, the wafer tilt corrector is operative to calculate the variable part of the misregistration measurement in accordance with:

$$\widetilde{MIS}_{\text{measured}}(N_s, \lambda) = MIS_{\text{measured}}(N_s, \lambda) - \overline{MIS_{\text{measured}}(N_s)}$$

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wherein $MIS_{measured}(N_s, \lambda)$ is the misregistration measurement and $\overline{MIS_{measured}(N_s)}$ is the misregistration measurement averaged over the multiplicity of illumination wavelengths.

5 [0022] Preferably, the wafer tilt corrector is operative to subtract from the misregistration measurement a weighted value of the difference between the TIS in the first and second illumination arrangements in accordance with:

$$MIS_{corrected}(N_s) = MIS_{measured}(N_s, \lambda) - \alpha(N_s) \cdot \Delta TIS(\lambda)$$

wherein $MIS_{corrected}(N_s)$ is a corrected value of the misregistration measurement and
 10 $\Delta TIS(\lambda)$ is the difference between the TIS in the first and second illumination arrangements.

[0023] In accordance with a preferred embodiment of the system of the present invention, an operating parameter of the metrology device is optimized based on the
 15 difference in TIS of the metrology device.

BRIEF DESCRIPTION OF THE DRAWINGS

[0024] The present invention will be understood and appreciated more fully from the following detailed description, taken in conjunction with the drawings in which:

20 [0025] Fig. 1 is a simplified schematic partially pictorial, partially block diagram illustration of a system for the correction of misregistration measurements for the impact thereon of wafer tilt, constructed and operative in accordance with a preferred embodiment of the present invention;

[0026] Figs. 2A and 2B are simplified schematic illustrations of respective first
 25 and second illumination conditions of a metrology device with respect to a semiconductor wafer, useful for providing data for the correction of misregistration measurements in a system of the type shown in Fig. 1;

[0027] Fig. 2C is a simplified graphical representation of data obtained under the illumination conditions of Figs. 2A and 2B; and

[0028] Fig. 3 is a simplified flow chart illustrating steps in the correction of misregistration measurement for the impact thereon of wafer tilt, in accordance with a preferred embodiment of the present invention.

5 DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

[0029] Reference is now made to Fig. 1, which is a simplified schematic partially pictorial, partially block diagram illustration of a system for the correction of misregistration measurements for the impact thereon of wafer tilt, constructed and operative in accordance with a preferred embodiment of the present invention.

10 [0030] As seen in Fig. 1, there is provided a metrology system 100 including a metrology tool 102 for measuring misregistration in the manufacture of a semiconductor device, such as a semiconductor wafer 104. Metrology tool 102 preferably includes an illumination source, here schematically represented as an illumination source 110, operative to illuminate wafer 104 in order to allow imaging of target structures formed on
15 layers of wafer 104 so as to measure misregistration between layers of wafer 104. It is appreciated that, in addition to illumination source 110, metrology tool 102 typically includes various additional components as are well known in the art, omitted here for clarity, in order to facilitate imaging of wafer 104 thereby and the measurement of misregistration between layers of wafer 104.

20 [0031] In accordance with a particularly preferred embodiment of the present invention, metrology tool 102 may be embodied as a multi-wavelength tool, such as an Archer 700 tool, commercially available from KLA, of California, USA, operative to perform multi-wavelength measurements on wafer 104. Metrology tool 102 is preferably operative to output at least one misregistration measurement representing misregistration
25 between layers of wafer 104 at at least one measurement location thereon. In the case that metrology tool 102 is a multi-wavelength instrument, the misregistration measurement $MIS_{measured}$ output thereby may be expressed as $MIS_{measured}(N_s, \lambda)$, wherein N_s represents the particular location on wafer 104 with respect to which the measurement is performed and λ represents the multiplicity of illumination wavelengths provided by illumination
30 source 110.

[0032] It is understood that local variations in the tilt of a surface 120 of wafer 104 may have an impact on the accuracy of the misregistration measurement $MIS_{measured}(N_s, \lambda)$ output by metrology tool 102, due to the influence of wafer tilt on the misregistration measurement, and may therefore introduce errors therein. Preliminary measurements performed by the present inventors have shown that the local wafer tilt at target locations may vary by about ± 0.5 mrad. Such local wafer tilt may have a considerable impact on misregistration measurements, which impact may become increasingly significant with increasing wafer stack height.

[0033] In accordance with preferred embodiments of the present invention, the value of the local wafer tilt at a particular location on wafer 104 may be found and the contribution thereof to errors in the misregistration measurement $MIS_{measured}(N_s, \lambda)$ may be quantified, so as to allow correction of the misregistration measurement for errors arising from local wafer tilt. This advantageously leads to the calculation of an improved misregistration measurement more accurately representing actual misregistration values for wafer 104. In accordance with preferred embodiments of the present invention, both the measurement of the wafer tilt and the quantification of the error in the misregistration measurement arising therefrom are found based on the understanding by the present inventors that the effect of wafer tilt on the misregistration measurement $MIS_{measured}(N_s, \lambda)$ is equivalent to the effect of the tilt of illumination source 110 with respect to surface 120 of wafer 104 illuminated thereby, due to the same telecentricity effect of both tilts.

[0034] The illumination source tilt contributes to the inherent limited measurement accuracy of metrology tool 102, which inherent measurement accuracy may be quantified as the Tool Induced Shift (TIS) of metrology tool 102, and does not change upon rotation of the wafer 104 by 180° . However, the wafer tilt contributes directly to the TIS calibrated misregistration measurement, and changes upon rotation of wafer 102 by 180° . Based on this understanding, measurement of changes in the TIS of metrology tool 102 under deliberately tilted illumination conditions, in comparison to non-tilted illumination conditions, may be used as a basis for finding corresponding changes in the misregistration measurement as a result of equivalent wafer tilt and hence both to quantify wafer tilt and correct the misregistration measurements for the impact of the wafer tilt thereon.

[0035] As seen in Fig. 1, the effect of tilted illumination conditions on the TIS of metrology tool 102 may be found by a TIS differential calculator 130. The preferred operation of TIS differential calculator 130 is best understood with additional reference to Figs. 2A – 2C.

5 [0036] As seen in Fig. 2A, the TIS of metrology tool 102 is preferably initially measured in a first non-tilted illumination arrangement, wherein illumination source 110 is preferably centered with respect to an illuminated location on surface 120 of wafer 104, such that surface 120 is generally orthogonally illuminated along an illumination axis 200 by illumination source 110. The TIS of metrology tool 102 may be found by any suitable
10 method, various types of which are well known in the art. Preferably the TIS may be quantified by measuring the same feature on surface 120 at 0° and 180° rotation of wafer 104, the TIS being equal to half of the sum of the measurements in each wafer orientation.

[0037] As seen in Fig. 2B, the TIS of metrology tool 102 is preferably additionally measured in a second, tilted illumination arrangement, wherein illumination source 110
15 is preferably shifted so as to be off-center with respect to the illuminated location on surface 120 of wafer 104, such that surface 120 is generally obliquely illuminated with respect to illumination axis 200 by illumination source 110. Here, by way of example, illumination source 110 is shown to be shifted by 10μm, such that illumination provided thereby is incident upon surface 120 at an oblique angle of about 5 mrad.

20 [0038] It is understood that such measurements under first and second illumination conditions with respect to surface 120 of wafer 104 are preferably carried out prior to the performance of metrology measurements by metrology tool 102 on wafer 104, optionally as part of a preliminary training procedure performed on metrology tool 102 prior to the operation thereof. Such measurements may be performed for multiple
25 locations on wafer 104 and, in the case of a multi-wavelength imaging metrology tool, at multiple wavelengths of illumination.

[0039] The difference in measurement accuracy, preferably expressed as the difference in TIS, of metrology tool 102 under first centered illumination conditions, such as shown in Fig. 2A, and under second off-center illumination conditions with respect to
30 surface 120 of wafer 104, such as shown in Fig. 2B, for multiple measurement locations

may be plotted as a function of illumination wavelength. An example of such a plot is shown in Fig. 2C, in which the difference in TIS (Δ TIS) of metrology tool 102 between the first and second illumination conditions of Figs. 2A and 2B is plotted as a function of illumination wavelength for 10 measurement locations, Δ TIS for each measurement location being represented by an individual line in the graph of Fig. 2C.

[0040] As appreciated from consideration of Fig. 2C, Δ TIS is seen to vary significantly with illumination wavelength but not to vary significantly with illuminated location. The variation of Δ TIS as a function of wavelength, as shown in Fig. 2C, thus may be considered to correspond to a differential signature profile or landscape representing the effect of deliberate, controlled, illumination tilt and hence the equivalent effect of wafer tilt on the measurement accuracy of metrology tool 102. Based on this profile, the effect of local wafer tilt on misregistration measurements by metrology tool 102 may be corrected, as if further detailed hereinbelow.

[0041] It is understood that a profile of the type shown in Fig. 2C may be output by TIS differential calculator 130 based on measurements under conditions shown in Figs. 2A and 2B. It is appreciated that such a profile is both specific to wafer 104 and to a given layer of wafer 104, and that such a profile thus is preferably obtained by TIS differential calculator 130 for each layer of wafer 104 upon which misregistration measurements by metrology tool 102 are to be performed. The output of such a profile by TIS differential calculator 130 is represented in Fig. 1 simply as Δ TIS. In one preferred embodiment of the present invention, TIS differential calculator 130 may be embodied as a computational module including computer code operative for finding a Δ TIS profile. It is appreciated that although TIS differential calculator 130 is shown in Fig. 1 to be embodied as a separate module, this is for the sake of clarity only, and the functionality of TIS differential calculator 130 may alternatively be included within metrology tool 102.

[0042] The Δ TIS profile obtained by TIS differential calculator 130 is preferably provided to a wafer tilt corrector, here shown to be embodied as a wafer tilt correction calculator module 140 included in system 100. Wafer tilt corrector 140 is preferably operative to correct at least one misregistration measurement measured by metrology device 102 at a given location on wafer 104 for errors therein arising due to tilt of wafer

104 at the given location. Preferably, the at least one misregistration measurement is corrected by wafer tilt corrector 140 based on subtracting from the misregistration measurement a weighted value of the difference between the TIS of metrology device 102 in first and second illumination arrangements, as quantified and output by TIS differential calculator 130.

[0043] The misregistration measurement as corrected for wafer tilt effects at a particular location N_s , $MIS_{corrected}(N_s)$, may be expressed as:

$$MIS_{corrected}(N_s) = MIS_{measured}(N_s, \lambda) - \alpha(N_s) \cdot \Delta TIS(\lambda) \quad (1)$$

wherein $MIS_{measured}(N_s, \lambda)$ represents the at least one misregistration measurement, which is preferably provided as a misregistration landscape, being a function of the measurement location N_s and the illumination measurement wavelength λ , $\Delta TIS(\lambda)$ represents the TIS differential signature profile, measured as described hereinabove with respect to Figs. 2A – 2C, and $\alpha(N_s)$ represents a location-specific weighting coefficient, representing the specific localized wafer tilt at a given location. It is appreciated that the product of the weighting coefficient $\alpha(N_s)$ and the TIS differential signature profile $\Delta TIS(\lambda)$ represents the contribution of the local value of the wafer tilt of wafer 104 to the corresponding local measured misregistration value and thus must be subtracted from the measured misregistration landscape in order to provide a ‘clean’ misregistration landscape corrected for errors caused by local wafer tilt. The correction of the misregistration measurement based on the TIS differential signature profile $\Delta TIS(\lambda)$ is based on the equivalence between changes in TIS as a result of illumination tilt and as a result of wafer tilt, as detailed hereinabove.

[0044] It is appreciated that in order to find $MIS_{corrected}(N_s)$, which parameter is the desired output of wafer tilt correction calculator module 140, the weighting coefficient $\alpha(N_s)$ must be ascertained. As a first step in ascertaining the weighting coefficient $\alpha(N_s)$, only the variable part of the measured misregistration at each location is considered, in accordance with:

$$\widetilde{MIS}_{measured}(N_s, \lambda) = MIS_{measured}(N_s, \lambda) - \overline{MIS_{measured}(N_s)} \quad (2)$$

wherein $\overline{\text{MIS}}_{\text{measured}}(N_s)$ is the measured misregistration averaged over all measurement wavelengths.

[0045] The weighting coefficient $\alpha(N_s)$ may then be found by calculating the projection of the variable part of the measured misregistration at each location by the variable part of the ΔTIS in accordance with:

$$\alpha(N_s) = \frac{\sum_{\lambda} \overline{\text{MIS}}_{\text{measured}}(N_s, \lambda) \cdot \overline{\Delta TIS}(\lambda)}{\sum_{\lambda} \Delta TIS^2(\lambda)} \quad (3)$$

where the variable part of the ΔTIS signature is defined for the same set of multiplicity wavelengths λ over which the misregistration is measured, by subtracting from the ΔTIS signature the value of the ΔTIS signature averaged over the multiplicity of wavelengths.

10 It is appreciated that calculating the weighting coefficient at each location using only the variable part of the measured misregistration landscape and the variable part of the difference in TIS, rather than the full values of these parameters, allows separation of the component of these parameters arising from wafer tilt from the actual misregistration values at each location.

15 [0046] The weighting coefficient $\alpha(N_s)$ found in accordance with equations (2) and (3) may then be substituted back into equation (1) so as to yield the misregistration measurement corrected for wafer tilt in accordance with

$$\text{MIS}_{\text{corrected}}(N_s) = \text{MIS}_{\text{measured}}(N_s, \lambda) - \frac{\sum_{\lambda} \overline{\text{MIS}}_{\text{measured}}(N_s, \lambda) \cdot \overline{\Delta TIS}(\lambda)}{\sum_{\lambda} \Delta TIS^2(\lambda)} \cdot \Delta TIS(\lambda) \quad (4)$$

[0047] It is appreciated that the calculations described hereinabove with reference to equations (1) – (4) are preferably performed by wafer tilt correction calculator module 140 and that the corrected misregistration value, cleaned for the inaccuracies introduced therein due to wafer tilt, is preferably output thereby. In one preferred embodiment of the present invention wafer tilt correction calculator module 140 may be embodied as a computational module including computer code operative to carry out calculations in accordance with equations (1) – (4). It is appreciated that although wafer tilt correction calculator module 140 is shown in Fig. 1 to be embodied as a separate module, this is for

the sake of clarity only, and the functionality of wafer tilt correction calculator module 140 may alternatively be included within metrology tool 102.

[0048] It is appreciated that the correction procedure carried out by system 100 may be implemented for correcting misregistration measurements performed on any type of symmetrical targets formed on wafer 104 and used as reference structures for measurement of misregistration in the manufacture of wafer 104. In the case that the reference targets are not symmetrical, the asymmetry of the targets must be additionally accounted for in correcting the misregistration measurements, in order to distinguish between inaccuracies in the misregistration measurements arising from local wafer tilt and components of misregistration measurements attributable to target asymmetry.

[0049] It is further appreciated that in some preferred embodiments of the present invention, measurement of ΔTIS as a function of wavelength may be used to optimize settings of metrology tool 102, for example by way of selecting a measurement wavelength having minimal sensitivity to illumination source tilt and hence to local wafer tilt. In such cases, the output of TIS differential calculator 130 may be fed back to metrology tool 102 and the settings thereof adjusted accordingly, as indicated by the double headed arrow showing optional bi-directional communication between metrology tool 102 and TIS differential calculator 130 in Fig. 1.

[0050] It is further appreciated that although the operation of TIS differential calculator 130 is described hereinabove with reference to the measurement of the dependency of the changes in TIS as a result of illumination source tilt on metrology illumination wavelength, this is by way of example only. In alternative embodiments of the present invention, changes in TIS as a result of illumination source tilt may be characterized with respect to other parameters of the metrology tool, such as, by way of example only, the focus position of the metrology tool.

[0051] Reference is now made to Fig. 3, which is a simplified flow chart illustrating steps in the correction of misregistration measurement for the impact thereon of wafer tilt, in accordance with a preferred embodiment of the present invention.

[0052] As seen in Fig. 3, a method 300 for the correction of misregistration measurements for the impact of wafer tilt thereon may begin at a first step 302, whereat a signature profile of the difference in TIS of a metrology tool under tilted and non-tilted illumination conditions is obtained. The signature profile may be obtained as a function of a variable characteristic of the metrology tool, for example as a function of wavelength. The signature profile may be calculated by a computerized TIS differential calculator module, such as module 130 of Fig. 1.

[0053] As seen at a second step 304, misregistration of a semiconductor device is preferably measured by the metrology tool characterized at first step 302, for at least one location on the semiconductor device. It is appreciated that although first step 302 may typically be performed prior to second step 304, this is not necessarily the case and the order of steps 302 and 304 may be reversed.

[0054] As seen at a third step 306, a weighted value of the signature profile of the difference in TIS is preferably found, which weighted value preferably corresponds to the value of the local wafer tilt at the measurement location on the semiconductor device under test. This correspondence is based on the understanding that the effect of wafer tilt on misregistration measurements is equivalent to the effect of illumination source tilt on misregistration measurements. Preferably, the weighted value is found in accordance with equations (2) and (3) detailed hereinabove.

[0055] As seen at a fourth step 308, the weighted value found at third step 306 is preferably subtracted from the misregistration measurement, so as to correct the misregistration measurement for inaccuracies therein due to local wafer tilt. It is appreciated that the corrected misregistration measurement thus obtained is more accurately representative of actual misregistration between layers of the semiconductor device under test. Preferably, the subtraction is performed in accordance with equation (4) detailed hereinabove.

[0056] It will be appreciated by persons skilled in the art that the present invention is not limited to what has been particularly shown and described hereinabove. The scope of the present invention includes both combinations and subcombinations of various

features described hereinabove as well as modifications thereof, all of which are not in the prior art.