the response, the CPU 21 transmits the response to the IC card processing device 1 through the communication section 25.

The CPU 11 of the IC card processing device 1 receives the response including the objective data through the card reader/writer 15. Upon receipt of the response, the CPU 11 checks the EDC of the response. That is, the CPU 11 determines whether or not the response has appropriately been received by using a redundancy check code stored in the epilogue of the response. Here, it is assumed that the CPU 11 determines that the response has not appropriately been received by the checking of the EDC.

Upon determination that the response has not appropriately been received, the CPU 11 transmits a retransmission request command (R-block) (second command) which is a retransmission request of the objective data to the IC card 2 through the card reader/writer 15.

For example, the retransmission request command is an R-block indicating an NAK (Negative AcKnowledgement).

When the retransmission request command has been transmitted, the CPU 11~~21~~ creates a detailed redundancy check code of the received data.

The CPU 21 of the IC card 2 receives the retransmission request command which is a retransmission request of the objective data through the communication section 25. Upon receipt of the retransmission request command, the CPU 21 increments the retransmission request

redundancy check code (EDC) of the received response is inconsistent (step S33). That is the CPU 11 checks the response by using the redundancy check code stored in the epilogue field.

Upon determination that the EDC is inconsistent (YES in step S33), the CPU 11 transmits a retransmission request to the IC card 2 through the card reader/writer 15 (step S34).

When the retransmission request has been transmitted, the CPU 11~~21~~ creates a detailed redundancy check code of the data (broken objective data) stored in the data section of the response received in step S32 (step S35).

When the detailed redundancy check code has been created, the CPU 11 determines whether or not a response to the retransmission request has been received through the card reader/writer 15 (step S36). Upon determination that a response has not been received (NO in step S36), the CPU 11 returns to step S36.

Upon determination that a response has been received, the CPU 11 determines whether or not the EDC of the received response is inconsistent (step S37). Upon determination that the EDC is inconsistent (YES in step 37), the CPU 11 returns to step S34.

Upon determination that the EDC is not inconsistent (NO in step S37), the CPU 11 determines whether or not the received response stores therein a detailed redundancy check code (step S38).

a third transmission processing section configured to transmit a third response including a divided block corresponding to the erroneous divided block through the communication section.

2. The portable electronic device according to claim 1, wherein

the second response does not include at least part of the data.

3. The portable electronic device according to claim 1 or 2, further comprising:

a storage section storing therein a counter configured to count the number of times the second reception processing section receives the second command,

wherein the creation section creates the detailed redundancy check code when the counter stored in the storage section has reached a predetermined threshold.

4. The portable electronic device according to claim 1 or 2, wherein

the creation section ~~divides the data into a plurality of divided blocks each possessing predetermined lengths, creates a redundancy check code of each of the plurality of divided blocks, and~~ connects the plurality of created redundancy check codes to each other in the order of the divided blocks to thereby create the detailed redundancy check code.

5. The portable electronic device according to claim 1 or 2, wherein

the third command includes a block number identifying the erroneous divided block.

6. The portable electronic device according to claim 1 or 2, wherein

the second command is an R-block indicating an NAK.

7. The portable electronic device according to claim 1, further comprising:

a module including the communication section, the first reception processing section, the first transmission processing section, the second reception processing section, the creation section, the second transmission processing section, the third reception processing section, and the third transmission processing section; and

a main body in which the module is encased.

8. The portable electronic device according to claim 1 or 2, wherein

the portable electronic device is an IC card.

9. An IC card processing device configured to supply a command to an IC card comprising:

a communication section configured to communicate with the IC card;

a first transmission processing section configured to transmit a first command to request data through the communication section;

a first reception processing section configured to receive a first response including the data through the communication section;

a checking section configured to check whether or not a redundancy check code of the data included in the first response received by the first reception processing section is inconsistent;

a creation section configured to create a first detailed redundancy check code of the data which has been checked by the checking section when the checking section has determined that the redundancy check code is inconsistent;

a second transmission processing section configured to transmit a second command which is a retransmission request of the data through the communication section when the checking section has determined that the redundancy check code is inconsistent;

a second reception processing section configured to receive a second response including a second detailed redundancy check code of the data through the communication section;

an identification section configured to identify an erroneous divided block which is a broken divided block on the basis of the first detailed redundancy check code created by the creation section, and the second detailed redundancy check code included in the second response received by the second reception processing section;

a third transmission processing section configured to transmit a third command to request the erroneous divided block identified by the identification section through the communication section;

a third reception processing section configured to receive a third response including a divided block corresponding to the erroneous divided block through the communication section; and

a replacement section configured to replace the erroneous divided block with the divided block included in the third response received by the third reception processing section.

10. The IC card processing device according to claim 9, wherein

the identification section compares a redundancy check code of each of the divided blocks, the redundancy check code being included in the first detailed redundancy check code created by the creation section, and a redundancy check code of each of the divided blocks, the redundancy check code being included in the second detailed redundancy check code included in the second response received by the second reception processing section with each other, and identifies a divided block in which both the redundancy check codes differ from each other as an erroneous divided block.

11. The IC card processing device according to claim 9 or 10, wherein

the second response does not include at least part of the data.

12. The IC card processing device according to claim 9 or 10, wherein

the creation section divides the data into a plurality of divided blocks each possessing predetermined lengths, creates a redundancy check code of each of the plurality of divided blocks, and connects the plurality of created redundancy check codes to each other in the order of the divided blocks to thereby create the first detailed redundancy check code.

13. The IC card processing device according to claim 9 or 10, wherein

the third command includes a block number identifying the erroneous divided block.

14. The IC card processing device according to claim 9 or 10, wherein

the second command is an R-block indicating an NAK.