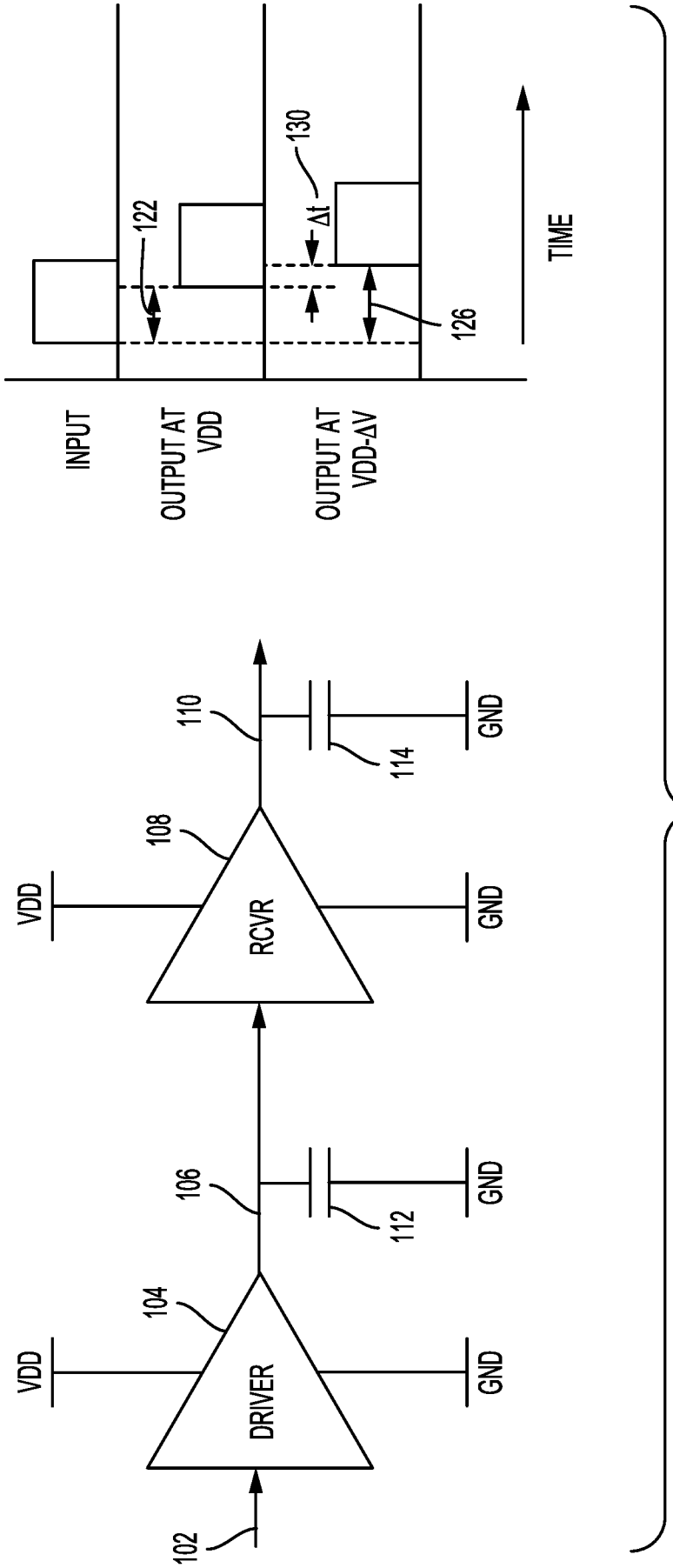

INPUT
OUTPUT AT VDD
OUTPUT AT VDD-ΔV
TIME
122
130
Δt
126
102
DRIVER
104
VDD
GND
106
112
RCVR
108
110
114

FIG. 1

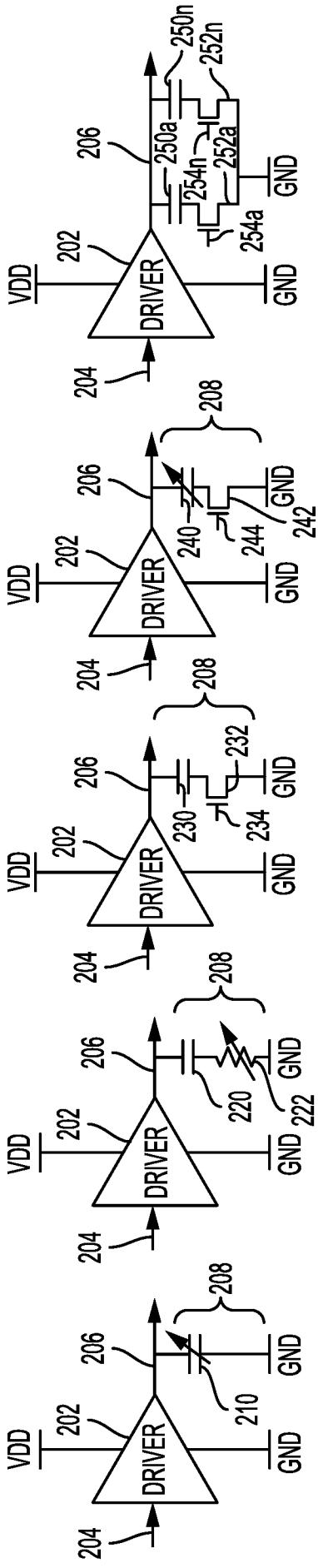


FIG. 2A FIG. 2B FIG. 2C FIG. 2D FIG. 2E

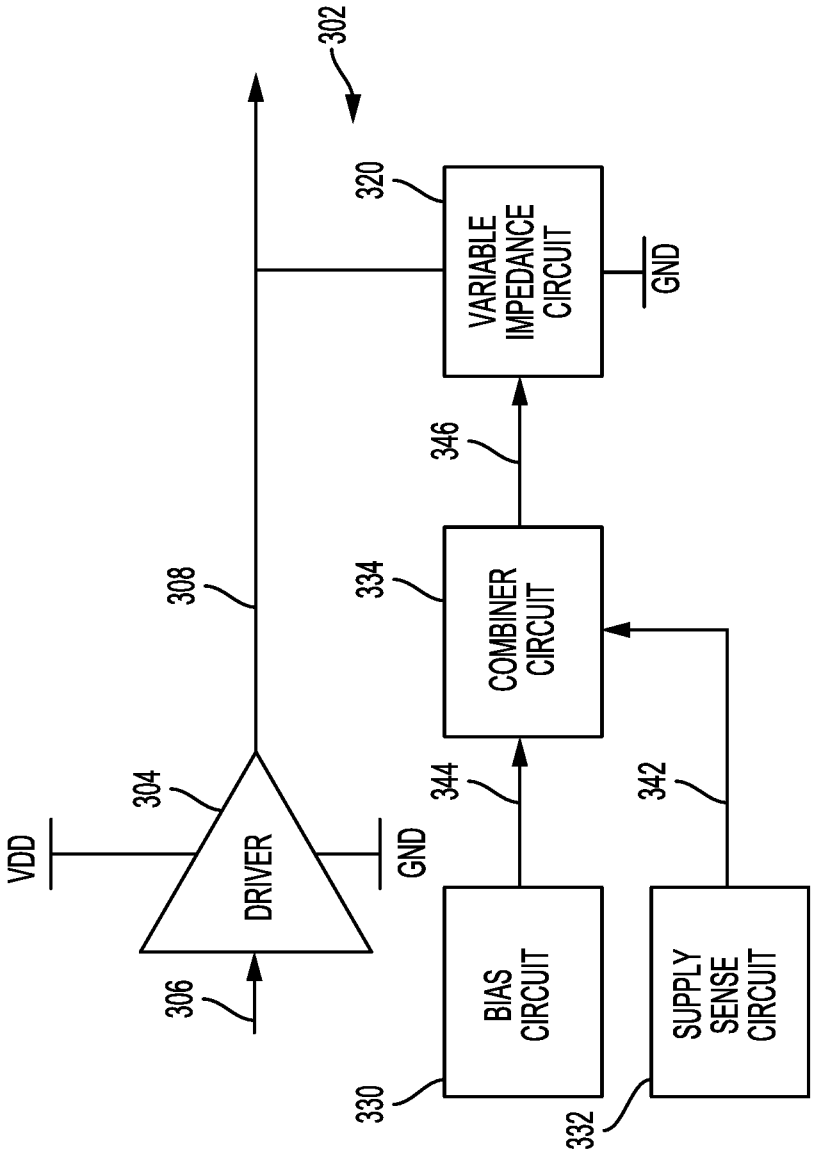

302
320
VARIABLE IMPEDANCE CIRCUIT
GND
346
334
COMBINER CIRCUIT
308
304
344
342
VDD
DRIVER
GND
306
BIAS CIRCUIT
SUPPLY SENSE CIRCUIT
330
332
322

FIG. 3

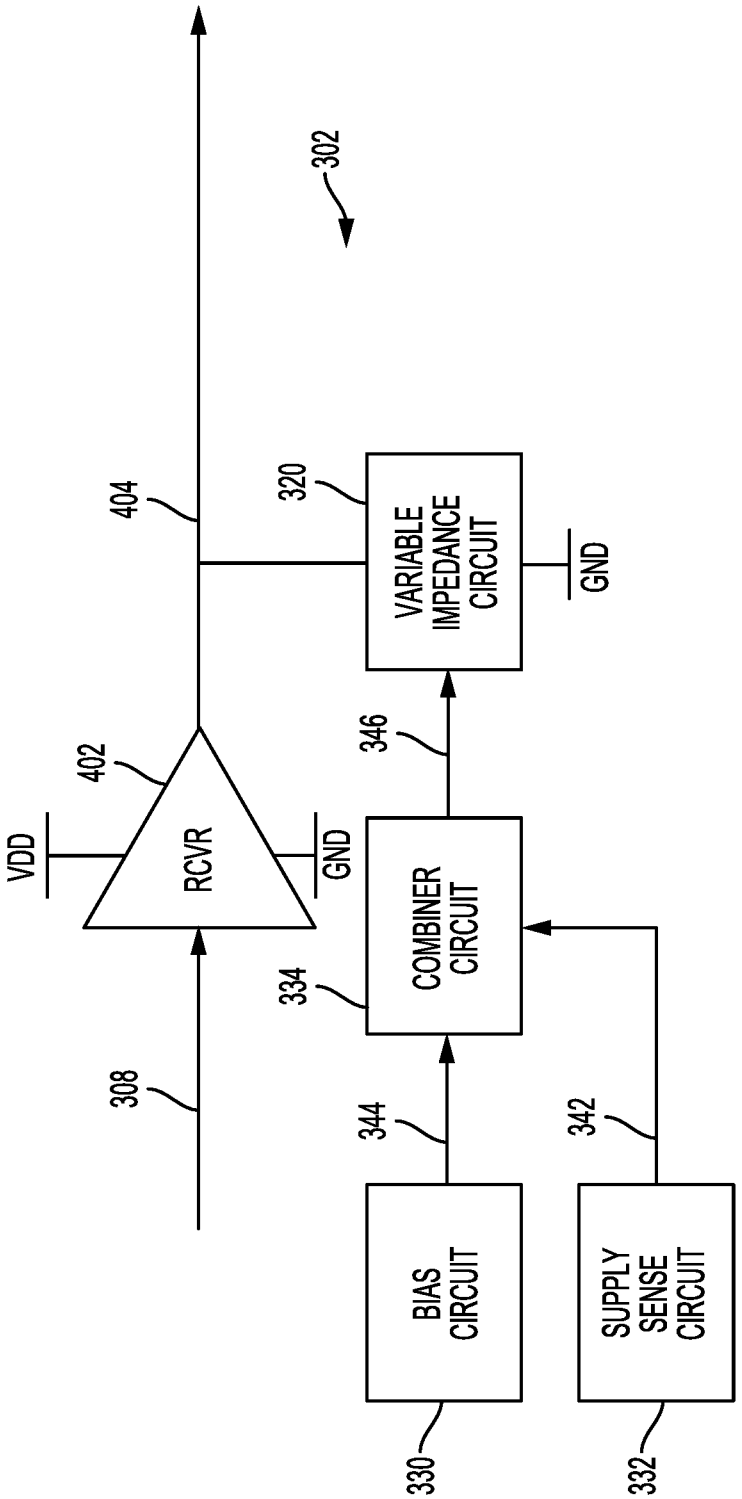

302
404
320
VARIABLE IMPEDANCE CIRCUIT
GND
402
346
VDD
RCVR
GND
COMBINER CIRCUIT
334
308
344
342
322
BIAS CIRCUIT
SUPPLY SENSE CIRCUIT
330
332

FIG. 4

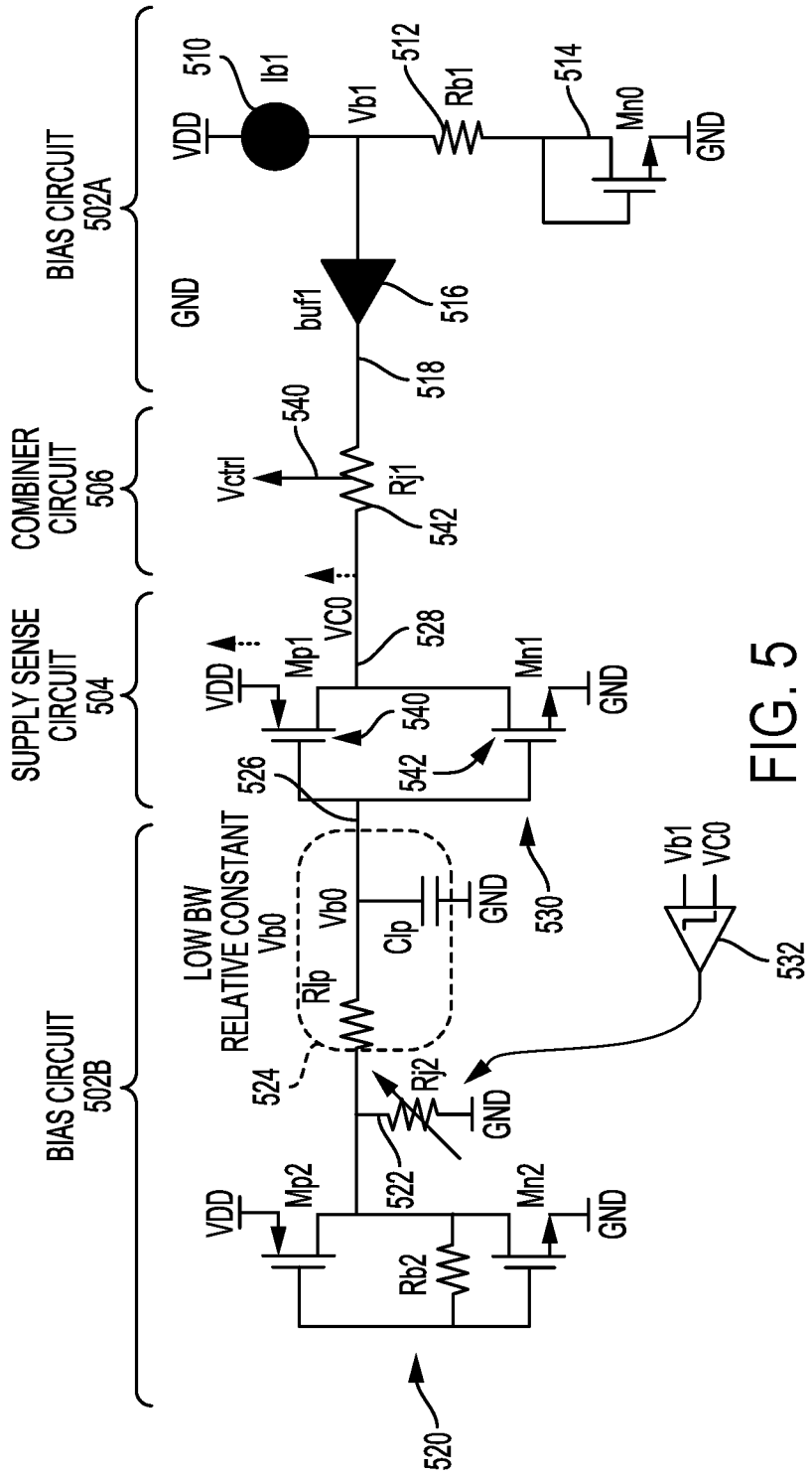

BIAS CIRCUIT 502A
COMBINER CIRCUIT 506
SUPPLY SENSE CIRCUIT 504
BIAS CIRCUIT 502B
LOW BW RELATIVE CONSTANT Vb0
VDD
GND
Ib1
510
Vb1
512
Rb1
514
Mn0
buf1
516
518
Vctrl
540
Rj1
542
VC0
528
Mp1
Mn1
526
530
Vb0
Clp
Rlp
524
Rj2
522
Mp2
Mn2
Rb2
520
532

FIG. 5

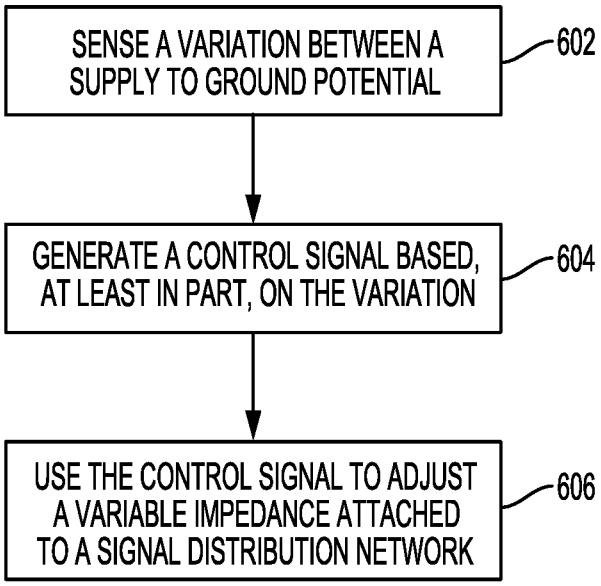

SENSE A VARIATION BETWEEN A SUPPLY TO GROUND POTENTIAL
602
GENERATE A CONTROL SIGNAL BASED, AT LEAST IN PART, ON THE VARIATION
604
USE THE CONTROL SIGNAL TO ADJUST A VARIABLE IMPEDANCE ATTACHED TO A SIGNAL DISTRIBUTION NETWORK
606

FIG. 6