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MULTIPLE DATA SEPARATING

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The present invention relates to a data multiplexing apparatus for timeshare-multiplexing a plurality of variable rate data and a multiplexed data demultiplexing apparatus for demultiplexing multiplexed data into a plurality of original data.

So far, to timeshare-multiplex a plurality of variable rate time sequencing data (for example, compressed digital moving picture (or sound) data) and to store them on a data storing medium, a plurality of data generated in a particular time of period were grouped together (this grouped data is hereinafter referred to as a pack), a header was added at the beginning of the pack, a code representing a time was written in the header, and thereby, the multiplexed data was generated. To demultiplex the multiplexed data into the original data, the data transfer rate of the data storing medium was adjusted in accordance with the time written at the pack header.

In the above mentioned conventional data multiplexing method, since a plurality of data generated in a particular period of time was grouped together as a multiplexing unit, the data length of the multiplexing unit varied, so that when this multiplexed data was stored on the data storing medium, the multiplexing unit

could not be matched with the storing unit of the data
storing medium.

According to an aspect of the invention, there
is provided a data multiplexing apparatus for timeshare-
multiplexing a plurality of variable rate data,
comprising:

controlling means for timeshare-multiplexing the plurality of variable rate data in a multiplexing unit of fixed data length.

In the data multiplexing apparatus mentioned above, since a control means is arranged to timeshare-multiplex a plurality of variable rate data in a multiplexing unit of fixed data length, the multiplexing unit can be matched with the physical storing unit of a data storing medium. As a result, the overhead involved in the storing control of the data storing medium can be reduced.

According to another aspect of the invention, there is provided a multiplexed data demultiplexing apparatus for demultiplexing multiplexed data where a plurality of variable rate data were timeshare-multiplexed in a multiplexing unit of fixed data length into a plurality of original data, comprising:

a plurality of buffers provided in correspondence to the plurality of original data, wherein the plurality of buffers have at least a storage capacity of the multiplexing unit of fixed data length.

In the multiplexed data demultiplexing apparatus mentioned above, since buffers are arranged to store data in a multiplexing unit of fixed data length in the case that a data multiplexing apparatus prereads variable rate

data for use in generating a multiplexing unit of fixed data length to generate a pack and a device downstream of the multiplexed data demultiplexing apparatus does not read this data when the pack is separated, even if multiplexed data is stored on a data storing medium and the multiplexed data demultiplexing apparatus is arranged to read the multiplexed data from the data storing medium, the demultiplexing apparatus can read the multiplexed data for example at the maximum transfer rate thereof and thereby no abnormality will take place.

According to still another aspect of the invention, there is provided a multiplexed data demultiplexing apparatus for demultiplexing multiplexed data where a plurality of variable rate data were timeshare-multiplexed in a particular multiplexing interval into a plurality of original data, comprising:

a plurality of buffers for storing the plurality of original data, each of the plurality of buffers having at least the storage capacity equivalent to the maximum data length generated in the multiplexing interval; and

delaying means for causing the plurality of buffers to delay read of data from each of the plurality of buffers for the time corresponding to the multiplexing interval.

In the multiplexed data demultiplexing apparatus mentioned above, original data demultiplexed is stored in a buffer having at least the storage capacity equivalent

to the maximum data length of data which can be generated in a multiplexing interval and the data demultiplexed is delayed in accordance with the multiplexing interval. Thus, since the multiplexed data demultiplexing apparatus can have a resistance to the fluctuation of delays of data generation in the multiplexing interval, the multiplexing apparatus does not need to consider such fluctuation of delays.

According to further aspect of the invention, there is provided a multiplexed data demultiplexing apparatus for reading a plurality of data which were timeshare- and multiplex-stored on a storing medium and for demultiplexing the read data into a plurality of original data, comprising:

- a plurality of buffers provided in correspondence to the plurality of original data;

- reading means for reading data stored on the storing medium at a predetermined speed; and

- controlling means for temporarily stopping the reading operation of the reading means when there is no storage space area in at least one of the plurality of buffers.

According to the multiplexed data demultiplexing apparatus mentioned above, since the controlling means temporarily stops reading data from the storing medium in the case that there is no storage space in any of a

plurality of buffers, the reading operation of the storing medium can be controlled in a simple construction. In addition, as long as the buffers have a storage space, the reading operation is continued. As a result, as much data as possible can be prepared from the storing medium.

Objects, features and advantages of the present invention will become readily apparent from the following detailed description thereof which is to be read in connection with the accompanying drawings, in which:

Fig. 1 is a block diagram showing a construction of each embodiment of a data multiplexing apparatus and a multiplexed data demultiplexing apparatus according to the invention;

Fig. 2 is a schematic diagram showing an example of a pack generated by the data multiplexing apparatus of Fig. 1;

Fig. 3 is a schematic diagram describing an example of operation of the embodiment of Fig. 1;

Fig. 4 is a block diagram showing a construction of another embodiment of a multiplexed data demultiplexing apparatus according to the invention as well as that of a multiplexing apparatus;

Fig. 5 is a schematic diagram showing a fluctuation of delay of data generation in a multiplexing

interval or a pack generation interval;

Fig. 6 is a schematic diagram showing the fluctuation of read timing of time sequencing data from the demultiplexing apparatus due to fluctuation of delay involved in data generation in a multiplexing interval or a pack generation interval;

Fig. 7 is a flow chart showing an example of operation of data multiplexing apparatus of the embodiment of the multiplexed data-demultiplexing apparatus of Fig. 4;

Fig. 8 is a flow chart showing an example of a process of a conventional data multiplexing apparatus for dealing with the fluctuation of delay involved in data generation in the multiplexing interval or pack generation interval;

Fig. 9 is a block diagram showing a construction of still another embodiment of a multiplexed data demultiplexing apparatus according to the invention as well as that of a multiplexing apparatus; and

Fig. 10 is a schematic diagram showing an operation of the apparatus of the embodiment shown in Fig. 9 and that of the apparatus shown in Fig. 1.

Fig. 1 is a block diagram showing a construction of each embodiment of a data multiplexing apparatus and a multiplexed data demultiplexing apparatus. For example,

variable rate time sequencing data D1, D2, ..., and Dn composed of compressed digital moving picture (or sound) data are stored in code buffers M1, M2, ..., and Mn of the data multiplexing apparatus. The variable rate time sequencing data contain in itself a synchronization signal, i.e., information with respect to data read intervals for use with a device downstream of a demultiplexing apparatus which will be described later. Output terminals of the code buffers M1, M2, ..., and Mn are connected to input terminals E1, E2, ..., and En of a switching circuit 2. An output terminal F of the switching circuit 2 is connected to an input terminal of a header adding circuit 3. An output of the header adding circuit 3 is sent to a variable rate data storing medium DSM such as an optical disc. The variable rate data storing medium DSM is a storing medium which can always read data at any data transfer rate up to the maximum transfer rate thereof in accordance with a request from the reading side. In the embodiment shown in Fig. 1, the multiplexed data demultiplexing apparatus always reads data from the data storing medium DSM at the maximum transfer rate thereof.

A control device 4 receives an output of a multiplexing apparatus system clock 5 and connects the input terminals E1, E2, ..., and En of the switching circuit 2 to the output terminal F at a predetermined interval one after the other. Thus, the control device 4

successively and respectively retrieves the variable rate time sequencing data D_1 , D_2 , ..., and D_n from the code buffers M_1 , M_2 , ..., and M_n and groups the data for each time sequence together. (This grouped data is hereinafter referred to as a packet). In addition, the control device 4 causes the header adding circuit 3 to add a packet header at the beginning of each packet (see Fig. 2). As described above, by slicing in a multiplexing unit of fixed data length a variable length packet train periodically generated, the control device 4 generates a fixed length pack. Moreover, the control device 4 causes the header adding circuit 3 to add a pack header containing time information at the beginning of each pack (see Fig. 2).

When the total data amount of a packet train to be packed is less than the multiplexing unit of fixed data length, the control device 4 waits until the next pack generation time rather than packing the packet train. When the total data amount exceeds the multiplexing unit of fixed data length, which is the data amount of one pack, the control device 4 generates a pack. At this point, one pack contains data of a plurality of times. However, the control device 4 controls the header adding circuit 3 so that the earliest one of the plurality of times is recorded at the pack header. Thus, part of data are sent earlier than the times they have. Although a

packet header may be present at the boundary of the pack, the control device 4 may change the order of packets which are sent earlier than the times they have, or, group data having the same time in the pack together so as to prevent the header from being divided. The timeshare-multiplexed data which was packed is sent to the variable rate data storing medium DSM.

A header separating circuit 6 of the multiplexed data demultiplexing apparatus separates a pack header from data being read from the data storing medium DSM and sends the pack header to a pack time information storing circuit 7. In addition, the header separating circuit 6 sends the variable rate time sequencing data D1, D2, ..., and Dn to the input terminal G of the switching circuit 8. The header separating circuit 6 also switches the connection of the input terminal G of the switching circuit 8 and the output terminals H1, H2, ..., and Hn in response to the separation of the packet header from data read from the data storing medium DSM so that the variable rate time sequencing data D1, D2, ..., and Dn are supplied to the code buffers S1, S2, ..., and Sn, respectively, and stored therein.

The pack time information storing circuit 7 stores time information that a pack header contains and sends the time information to one input of a comparator 10. The comparator 10 receives from the other input terminal thereof an output of the demultiplexing apparatus

system clock 9. When the time which is represented with the pack time information storing circuit 7 matches the time which is represented with the system clock 9, the comparator 10 sends a read request to the data storing medium DSM.

As described above, when one pack contains a plurality of data having different times, the control device 4 of the data multiplexing apparatus records the earliest one of the plurality of times at the pack header. Thus, when the multiplexed data demultiplexing apparatus reads a pack from the data storing medium DSM at a time recorded at the pack header to retrieve the multiplexed data within the pack into the original data, since the pack also earlier contains data which should be read at later times, a device downstream of the multiplexed data demultiplexing apparatus does not read this data. As a result, the multiplexed data demultiplexing apparatus cannot continuously read data from the data storing medium DSM at the maximum transfer rate thereof. To prevent this situation, the storage capacity of the code buffers S1, S2, ..., and Sn is equivalent to the fixed data length of multiplexed data, namely the data amount equivalent to one pack. This is because the amount of data which is earlier sent than the time at which it should be sent does not exceed the data amount of one pack at most.

In the multiplexed data demultiplexing apparatus

as shown in Fig. 1, the initial time of the system clock 9 is set to time represented with the header of the first pack. Thus, the comparator 10 outputs a read request to the data storing medium DSM. Accordingly, the variable rate time sequencing data D1, D2, ..., and Dn contained in the first pack are sent from the data storing medium DSM to the switching circuit 8 through the header separating circuit 6. The switching circuit 8 switches the variable rate time sequencing data D1, D2, ..., and Dn to the code buffers S1, S2, ..., and Sn, respectively. On the other hand, the header separating circuit 6 outputs the pack header of a pack preceded by the first pack to the pack time information storing circuit 7. The storing circuit 7 stores the time information contained in the pack header. When the time represented with the system clock 9 matches the time stored in the storing circuit 7, the comparator 10 outputs a read request to the data storing medium DSM. Thus, the variable rate time sequencing data D1, D2, ..., and Dn contained in the pack preceded by the first pack are sent from the data storing medium DSM to the switching circuit 8 through the header separating circuit 6. In such a manner, the variable rate time sequencing data D1, D2, ..., and Dn divided into a plurality of packs are stored in the code buffers, S1, S2, ..., and Sn, respectively and retrieved into the original variable rate time sequencing data D1, D2, ..., and Dn.

Next, with reference to Fig. 3, the operation of

the embodiment shown in Fig. 1 will be described. In Fig. 3, it is assumed that there are two channels to be timeshare-multiplexed for simplicity. In other words, in the figure, there are two variable rate time sequencing data D1 and D2 as opposed to Fig. 1. In this case, the multiplexing apparatus side has two code buffers M1 and M2, while the demultiplexing apparatus side has two code buffers S1 and S2. The control device 4 of the data multiplexing apparatus generates the first pack by the use of a part B1 of packet train B which was conventionally contained in the second pack as well as packet train A. Thereafter, the control device 4 sends the first pack to the data storing medium DSM. Next, the control device 4 generates a second pack of fixed data length by the use of part C1 of packet train C which was conventionally contained in the third pack as well as remaining part B2 of packet train B. Then, the control device 4 sends the second pack to the data storing medium DSM. Thereafter, the control device 4 generates a third pack of fixed data length by the use of all the pack train D which was conventionally contained in the fourth pack as well as part E1 of packet train E which was conventionally contained in the fifth pack. Next, the control device 4 sends the third pack to the data storing medium DSM. In the subsequent pack generation period, the control device 4 does not generate a pack. Thereafter, the control

device 4 generates a fourth pack of fixed data length by the use of remaining part E2 of packet train E, all the packet train F which was conventionally contained in the sixth pack, and part G1 of the packet train G which was conventionally contained in the seventh pack. Then, the control device 4 sends the fourth pack to the data storing medium DSM. In such a manner, the packs of fixed data length are generated in succession and then sent to the data storing medium DSM.

As described above, since the data length of packs generated by the multiplexing apparatus of Fig. 1 is fixed, this data length can be matched with the physical storing unit of the data storing medium DSM. Thus, the overhead involved in the storing control can be decreased. In addition, when data is reproduced from the data storing medium at random, the waiting time can be reduced. Moreover, data search and intermittent reproduction can be performed at high speeds.

The lower half of Fig. 3 represents variation of storage amount of the code buffer S1 of the demultiplexing apparatus with hatched lines. As described above, data which has a later time than it is actually read is written to the code buffer S1. However, since the storage capacity of the code buffer S1 is equal to the multiplexing unit of fixed data length, which is the data amount of one pack, no problem will take place.

In the above mentioned embodiment, the storage

capacity of the code buffers S_1 , S_2 , ..., and S_n of the demultiplexing apparatus was the multiplexing unit of fixed data length, which is the data amount of one pack, the storage capacity can be larger than the data amount of one pack. However, from size and cost points of view, the storage capacity is preferably the data amount of one pack.

In the above mentioned embodiment, an optical disc was used as a data storing medium. However, it should be understood that magneto-optical disc, a magnetic hard disc, a digital audio tape, or the like can be used.

In the conventional data multiplexing method mentioned previously, when the plurality of variable rate time sequencing data are denoted D_1 , D_2 , and D_3 , the generation delay time which takes place in a pack of D_3 significantly varies depending on the data lengths of D_1 and D_2 as shown in Fig. 5. Thus, the timing at which D_3 is read from the multiplexed data demultiplexing apparatus, which demultiplexes multiplexed data, into plurality of original data D_1 , D_2 , and D_3 , remarkably fluctuates as shown in Fig. 6.

Fig. 8 is a flow chart showing a process that the multiplexing apparatus performs to prevent underflow from taking place in the multiplexed data demultiplexing apparatus due to a delay of data generation in the pack. In this example, the number of channels on which data is

timeshare-multiplexed is n , that is, the number of variable rate time sequencing data is n . First, the amount of data to be transferred on channel 1, N_1 , is determined (in step S11). Then, the amount of data to be transferred on channel 2, N_2 , is temporarily determined (in step S12). Thereafter, the delay time of the data of channel 2 is calculated by the use of the total amount of data transferred on the channel preceded by channel 2 (in step S13). Thereafter, depending on the calculated delay time, the amount of data to be transferred is added in such a way that the buffer of channel 2 on the demultiplexing apparatus side does not overflow. Thus, the amount of data to be transferred on channel, N_2 , is determined (in step S14). Thereafter, the process from steps S12 to S14 is repeated and thereby the amounts of data to be transferred on channels 3 to n , N_3 to N_n , are determined. Thereafter, it is determined whether or not the total amounts of data to be transferred, N_1 to N_n , are within the maximum data length (in step S15). When the total amount of data does not exceed the maximum data length, recalculation is performed so that depending on how much the total amount of data to be transferred, N_1 to N_n , exceeds the maximum data length of the pack, the amount of data on each channel; N_k , is decreased in such a way that each buffer of the demultiplexing apparatus does not overflow.

However, in the process that the multiplexing

apparatus shown in Fig. 8 performs, since the time sequencing data of each channel to be processed is of a variable rate, the delay time of data generation in the multiplexing interval varies from interval to interval. Thus, the process becomes complicated.

The above mentioned problem can be solved by another embodiment of the invention.

Fig. 4 is a block diagram showing a construction of another embodiment of a multiplexed data demultiplexing apparatus as well as that of a multiplexing apparatus. Variable rate time sequencing data D1, D2, ..., and Dn composed of for example compressed digital moving picture (or sound) data are stored in code buffers M1, M2, ..., and Mn of a multiplexing apparatus. Each variable rate time sequencing data contain in itself a synchronization signal i.e., information with respect to a data read interval for use with a device downstream of a demultiplexing apparatus which will be described later. Output terminals of the code buffers M1, M2, ..., and Mn are connected to input terminals E1, E2, ..., and En of a switching circuit 2, respectively. An output terminal F of the switching circuit 2 is connected to an input terminal of a header adding circuit 3. An output of the header adding circuit 3 is sent to a variable rate data storing medium DSM such as an optical disc. The variable rate data storing medium DSM is a storing medium which can

always read data at any rate of up to the maximum transfer rate thereof in accordance with a request of the reading side. In the embodiment shown in Fig. 4, the multiplexed data demultiplexing apparatus side always reads data from the data storing medium DSM at the maximum transfer rate thereof.

A control unit 4 receives an output of a multiplexing apparatus system clock 5 and connects the input terminals E1, E2, ..., and En of the switching circuit 2 to the output terminal F at a particular interval one after the other. Thus, the control unit 4 retrieves the variable rate time sequencing data D1, D2, ..., and Dn from the code buffers M1, M2, ..., and Mn, respectively and groups the variable rate data of each time sequence together. In addition, the control unit 4 causes the header adding circuit 3 to add a packet header at the beginning of each packet (see Fig. 2). Moreover, the control unit 4 groups the packets of all the time sequences together. Thereafter, the control unit 4 causes the header adding circuit 3 to add at the beginning of each pack a pack header containing time information for use in reading the pack (see Fig. 2). The timeshare-multiplexed data with a pack header is sent to the variable rate data storing medium DSM.

A header separating circuit 6 of the multiplexed data demultiplexing apparatus separates a pack header from data being read from the data storing medium DSM and sends

the pack header to a pack time information storing circuit 7. In addition, the header separating circuit 6 sends the variable rate time sequencing data D1, D2, ..., and Dn to an input terminal G of a switching circuit 8. The header separating circuit 6 also switches the connection of the input terminal G of the switching circuit 8 and the output terminals H1, H2, ..., and Hn in response to the separation of the packet header from data read from the data storing medium DSM so that the variable rate time sequencing data D1, D2, ..., and Dn are supplied to the code buffers S1, S2, ..., and Sn respectively, and stored therein.

The pack time information storing circuit 7 stores time information contained in a pack header and sends this information to one input of a comparator 10. In addition, the comparator 10 receives from the other input thereof an output of the demultiplexing apparatus system clock 9. When the time represented with the output of the pack time information storing circuit 7 matches the time represented with the system clock 9, the comparator 10 sends a read request to the data storing medium DSM.

As described with reference to Fig. 5, a multiplexing interval of a plurality of variable rate time sequencing data D1, D2, ..., and Dn, which is a generation delay time which takes place in a pack remarkably varies depending on the length of data which precedes thereof.

Thus, as described with reference to Fig. 6, the timing for reading the plurality of variable rate time sequencing data D_1 , D_2 , ..., and D_n retrieved from the code buffers S_1 , S_2 , ..., and S_n of the multiplexed data demultiplexing apparatus remarkably varies. Consequently, when the multiplexing apparatus directly sends multiplexed data to the demultiplexing apparatus, the data varies for the multiplexing interval, which is the pack generation interval, in the worst case. On the other hand, in the case of the embodiment shown in Fig. 4, when multiplexed data is transferred from the data storing medium DSM to the demultiplexing apparatus, the multiplexed data varies for the time where the data length of a pack is divided by the data transfer rate of the data storing medium DSM (or by the maximum transfer rate when data is transferred at the maximum transfer rate). Thus, in the embodiment shown in Fig. 1, so as to read data from the code buffers S_1 , S_2 , ..., and S_n even in the worst case, a read delay device 11 for delaying data which is read from the code buffers S_1 , S_2 , ..., and S_n for the time where the data length of the pack is divided by the data transfer rate of the data storing medium DSM (or by the maximum transfer rate when the data is transferred at the maximum transfer rate). In addition, the code buffers S_1 , S_2 , ..., and S_n have the capacity of the maximum data length which can be generated in the multiplexing interval.

In the multiplexed data demultiplexing apparatus

shown in Fig. 4, the initial time of the system clock 9 is set to the time represented with the header of a first pack. Thus, the comparator 10 sends a read request to the data storing medium DSM. Accordingly, the data storing medium DSM transfers the variable rate time sequencing data D1, D2, ..., and Dn contained in the first pack to the multiplexed data demultiplexing apparatus at the maximum transfer rate thereof. The multiplexed data demultiplexing apparatus sends the data to the switching circuit 8 through the header separating circuit 6. The switching circuit 8 switches the variable rate time sequencing data D1, D2, ..., and Dn to the code buffers S1, S2, ..., and Sn, respectively. On the other hand, the header separating circuit 6 outputs the pack header of the pack preceded by the first pack to the pack time information storing circuit 7. The storing circuit 7 stores time information contained in the pack header. When the time represented with the system clock 9 matches the time stored in the storing circuit 7, the comparator 10 outputs a read request to the data storing medium DSM. Accordingly, the variable rate time sequencing data D1, D2, ..., and Dn contained in the pack preceded by the first pack is sent from the data storing medium DSM to the switching circuit 8 through the header separating circuit 6. The switching circuit 8 switches these data to the code buffers S1, S2, ..., and Sn, respectively.

Consequently, the variable rate time sequencing data D_1 , D_2 , ..., and D_n divided into a plurality of packs are stored in the code buffers S_1 , S_2 , ..., and S_n , respectively and then the original variable rate time sequencing data D_1 , D_2 , ..., and D_n are obtained.

The original data D_1 , D_2 , ..., and D_n obtained are stored in the code buffers S_1 , S_2 , ..., and S_n having the capacity of the maximum data length of data which can be generated in the multiplexing interval. The read delay device 11 delays the data for the period of time where the data length of the pack is divided by the maximum transfer rate of the data storing medium DSM. Thus, the demultiplexing apparatus can have a resistance to the fluctuation of delay of data generation in the multiplexing interval. As a result, without necessity of considering the fluctuation of delay on the multiplexing apparatus side, as shown in step S1 of Fig. 7, the control unit 4 of the multiplexing apparatus may only determine each transfer data amount N_k of the variable rate time sequencing data D_1 , D_2 , ..., and D_n corresponding to the channels 1 to n .

In the embodiment as just described, the storage capacity of the code buffers S_1 , S_2 , ..., and S_n of the demodulating apparatus was the maximum data length which can be generated in a multiplexing interval. However, it should be understood that the storage capacity of these code buffers can be larger than the maximum data length.

However, from the view points of the size and cost of the apparatus, the storage capacity of the code buffers is preferably the maximum data length which can be generated in a multiplexing interval.

In the multiplexed data demultiplexing apparatus of Fig. 1, time information should be written to each pack header. In addition, since this apparatus requires the time information storing circuit 7 and the comparator 10, the construction thereof is a little complicated.

The above mentioned problem can be solved by still another embodiment of the invention.

Fig. 9 shows a construction of still another embodiment of a multiplexed data demultiplexing apparatus in accordance with the present invention. In Fig. 9, the same parts as those in Fig. 1 are represented with the same reference numerals. In other words, since each construction and operation of the code buffers M1, M2, ..., and Mn, the switching circuit 2, the header adding circuit 3, the variable rate data storing medium DSM, the control unit 4, the multiplexing unit system clock 5, the header separating circuit 6, the switching circuit 8, and the code buffers S1, S2, ..., and Sn of Fig. 9 are the same as those of Fig. 1, their description will be omitted.

In the embodiment of Fig. 9, an NOR gate 12 having n input terminals connected to code buffers S1, S2, ..., and Sn constructs a reading control means of the

data storing medium DSM. In other words, when the NOR gate 12 receives a space-absence signal "1" representing there is no storage space from any one of code buffers S1, S2, ..., and Sn, the NOR gate 12 outputs a read cancel signal "0" to the data storing medium DSM. On the other hand, when the NOR gate 12 receives a space-presence signal representing there is a storage space from any one of the code buffers S1, S2, ..., and Sn, the NOR gate 12 outputs a read request signal "1" to the data storing medium DSM.

Next, with reference to Fig. 10, the operation of the embodiment shown in Fig. 9 will be described. When data is prepared from the data storing medium DSM at the maximum rate thereof and there is no storage space in for example the code buffer S1, the NOR gate 12 receives the space-absence signal "1" from the code buffer S1. At this point, the NOR gate 12 outputs the read stop signal "0" to the data storing medium DSM. Thus, the data reading operation of the data storing medium DSM is stopped. Thereafter, after data is read from the code buffer S1, when the NOR gate 12 receives the space-presence signal "0" from the code buffer S1, the NOR gate 12 outputs the read request signal "1" to the data storing medium DSM. As a result, the reading operation of the data storing medium DSM is resumed. In this manner, the data reading operation of the data storing medium DSM is controlled.

In this construction, it is not necessary to

store time information of a header in demultiplexing data which were multiplexed. However, the time information can be stored for use in random access.

In the embodiment just described, the reading control means was constructed of a NOR gate. However, it should be appreciated that a logical circuit having the similar function can be used.

Having described specific preferred embodiments of the present invention with reference to the accompanying drawings, it is to be understood that the invention is not limited to those precise embodiments, and that various changes and modifications may be effected therein by one skilled in the art without departing from the scope of the invention as defined in the appended claims.

of
In an embodiment/ the data multiplexing apparatus of the invention, since a plurality of variable rate data are timeshare-multiplexed in a multiplexed data apparatus of fixed data length, the multiplexing unit can be matched with the physical storing unit of the data storing medium. Thus, the overhead involved in storing control of the data storing medium can be reduced. In addition, when data is reproduced from the data storing medium at random, the waiting time can be reduced. Moreover, data search and intermittent reproduction can be performed at high speeds.

of
In an embodiment/ the multiplexed data demultiplexing

apparatus of the invention, since the data multiplexing apparatus generates the multiplexing unit of fixed data length, even if a device downstream of the multiplexed data demultiplexing apparatus does not read variable rate data which is prepared by the demultiplexing apparatus, since the buffers can store multiplexing unit of fixed data length, no problem will take place.

In an embodiment^{of}/the multiplexed data demultiplexing apparatus of the invention, the original data demultiplexed is stored in buffers having at least the storage capacity of the maximum data length of data which can be generated in a multiplexing interval. In addition, the data is read with a delay corresponding to the multiplexing interval. As a result, the demultiplexing apparatus can have a resistance to the fluctuation of delay involved in data generation in the multiplexing interval. Thus, the multiplexing apparatus side does not need to consider such fluctuation of delay.

In an embodiment^{of}/ the multiplexed data demultiplexing apparatus of the invention, since the reading operation of the data storing medium is stopped in the case that there is no storage space in a plurality of buffers, it is not necessary to write time information to each pack header as seen in the prior art. In addition, since the apparatus of the present invention does not require a time information storing circuit and a comparator, the apparatus in simple construction can control the reading

operation of the data storing medium and demultiplex the original data which were multiplexed. Moreover, as long as there is a storage space in the buffers, the reading operation is continued. Thus, as much data as possible can be prepared from the data storing medium.

Therefore, an ^{embodiment} / of the present invention can provide a data multiplexing apparatus wherein the multiplexing unit can be matched with the physical storing unit of the data storing medium.

An embodiment of the present invention can also provide a multiplexed data demultiplexing apparatus wherein data can be prepared in timeshare-multiplexing a plurality of variable rate data in a multiplexing unit of fixed data length without hindrance.

An embodiment of the present invention can also provide a multiplexed data demultiplexing apparatus where a multiplexing apparatus does not need to take care of the fluctuation of delay of data generation in a multiplexing interval.

Moreover,
/ an embodiment of the present invention can provide multiplexed data demultiplexing apparatus in a simple construction for demultiplexing data timeshare- and multiplex-stored on a storing medium into a plurality of original data.

CLAIMS

1. A data multiplexing apparatus for timeshare-multiplexing a plurality of variable rate data, comprising:

controlling means for timeshare-multiplexing said plurality of variable rate data in a multiplexing unit of fixed data length.
2. A data multiplexing apparatus comprising:

a plurality of code buffers provided in correspondence to a plurality of variable rate data;

a switching circuit for being supplied with outputs of said plurality of code buffers and for selecting one of the outputs;

a header adding circuit for being supplied with said output elected by said switching circuit; and

a controlling circuit for controlling said switching circuit so as to successively retrieve said variable rate data from said plurality of code buffers,

wherein an output of said header adding circuit is supplied to a variable rate data storing medium and

wherein said controlling circuit timeshare-multiplexes a plurality of variable rate data in a multiplexing unit of fixed data length.
3. A data multiplexing apparatus according to claim 2, wherein said controlling circuit groups said variable

rate data retrieved from said code buffers for each time sequence together to form a packet and controls said header adding circuit so as to add a packet header at the beginning of the packet.

4. A data multiplexing apparatus according to claim 3, wherein said controlling circuit further generates a pack of fixed length by slicing in a multiplexing unit of fixed data length a train of said packet of variable length periodically generated and controls said header adding circuit so as to add a pack header containing time information at the beginning of said pack of fixed length.

5. A data multiplexing method for timeshare-multiplexing a plurality of variable rate data, comprising:

a controlling step for timeshare-multiplexing said plurality of variable rate data in a multiplexing unit of fixed data length.

6. A multiplexed data storing medium for storing a multiplexed data where a plurality of variable rate data were timeshare-multiplexed,

wherein said plurality of variable rate data are timeshare-multiplexed in a multiplexing unit of fixed data length and the multiplexing unit can be matched with the physical storing unit of said storing medium.

7. A multiplexed data storing medium according to

claim 6, wherein said storing medium can always read data at any data transfer rate up to the maximum transfer rate thereof in accordance with a request from the reading side.

8. A multiplexed data demultiplexing apparatus for demultiplexing multiplexed at where a plurality of variable rate data were timeshare-multiplexed in a multiplexing unit of fixed data length into a plurality of original data, comprising:

a plurality of buffers provided in correspondence to said plurality of original data, wherein said plurality of buffers have at least a storage capacity of said multiplexing unit of fixed data length.

9. A multiplexed data demultiplexing apparatus according to claim 8, wherein said multiplexed data is retrieved from a storing medium and wherein said apparatus further comprises a header separating circuit for separating a header information from the retrieved data.

10. A multiplexed data demultiplexing apparatus according to claim 9, wherein said header information comprises a pack header and a packet header.

11. A multiplexed data demultiplexing apparatus according to claim 10, further comprising:

a pack time information storing circuit supplied with the separated pack header; and

a switching circuit for separating said multiplexed data into said plurality of original data, wherein outputs of said switching circuit is successively stored in said plurality of buffers.

12. A multiplexed data demultiplexing apparatus according to claim 11, wherein said pack time information storing circuit stores a time information in said pack header.

13. A multiplexed data demultiplexing apparatus according to claim 12, further comprising a comparator for comparing said time information and information represented with system clock,

wherein when said comparator detects matching of said time information and said information represented with system clock, said comparator outputs a read request of said storing medium.

14. A multiplexed data demultiplexing apparatus for demultiplexing multiplexed data where a plurality of variable rate data were timeshare-multiplexed in a particular multiplexing interval into a plurality of original data, comprising:

a plurality of buffers for storing said plurality of original data, each of said plurality of

buffers having at least the storage capacity equivalent to the maximum data length generated in said multiplexing interval; and

delaying means for causing said plurality of buffers to delay read of data from each of said plurality of buffers for the time said corresponding to multiplexing interval.

15. A multiplexed data demultiplexing apparatus according to claim 14, wherein when said multiplexed data stored on a storing medium is read and said plurality of original data is demultiplexed, the delay time by said delaying means is the time which is obtained by dividing data length of said multiplexing interval by transfer rate of said storing medium.

16. A multiplexed data demultiplexing apparatus according to claim 15, wherein said transfer rate is the maximum transfer rate.

17. A multiplexed data demultiplexing apparatus according to claim 14, wherein when said plurality of original data are demultiplexed from said multiplexed data which is directly transmitted, the delay time by said delaying means is the time of said multiplexing interval itself.

18. A multiplexed data demultiplexing apparatus according to any one of claims 14 to 17, wherein said

multiplexed data is formed by a pack of fixed length where a train of packets of variable length is formed for every multiplexing unit of fixed data length and

wherein said multiplexing interval is a pack interval.

19. A multiplexed data demultiplexing apparatus for reading a plurality of data which were timeshare- and multiplex-stored on a storing medium and for demultiplexing the read data into a plurality of original data, comprising:

a plurality of buffers provided in correspondence to said plurality of original data;

reading means for reading data stored on said storing medium at a predetermined speed; and

controlling means for temporarily stopping the reading operation of said reading means when there is no storage space area in at least one of said plurality of buffers.

20. A multiplexed data demultiplexing apparatus according to claim 19, wherein said controlling means comprises a logic circuit which received a space absence signal from each of said plurality of buffers and outputs a read request signal.