

ABSTRACT OF THE DISCLOSURE (FIG. 1)

"ELECTRONIC DEVICE WITH SERIAL ATA INTERFACE
AND SIGNAL AMPLITUDE ADJUSTING METHOD"

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The CPU of an electronic device generates a parameter for determining the amplitude of a serial data signal when it is output from an output device to a serial ATA bus. The parameter indicates a value that is needed to make the amplitude of the received serial data signal fall within a range, stipulated in serial ATA interface standards, when another electronic device receives the serial data signal. The parameter is generated in accordance with the cable length of the serial ATA bus designated by a cable length designation unit. The other electronic device is connected to the serial ATA bus.

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