

FIG. 1

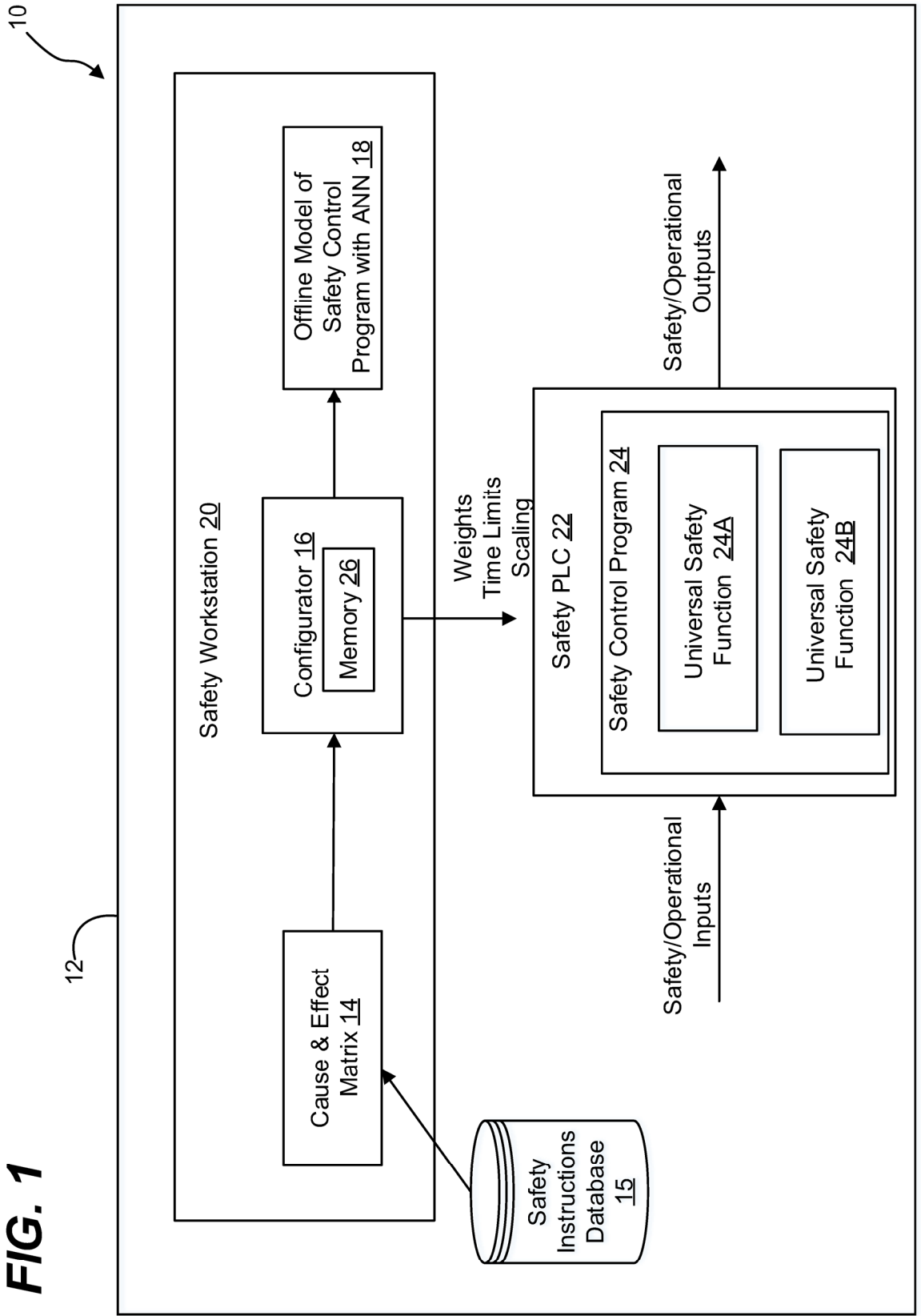


FIG. 2

200

	Output 1	Output 2
Input 1	X	
Input 2	AND 1,2	X
Input 3	X	Not X

202

204

FIG. 3

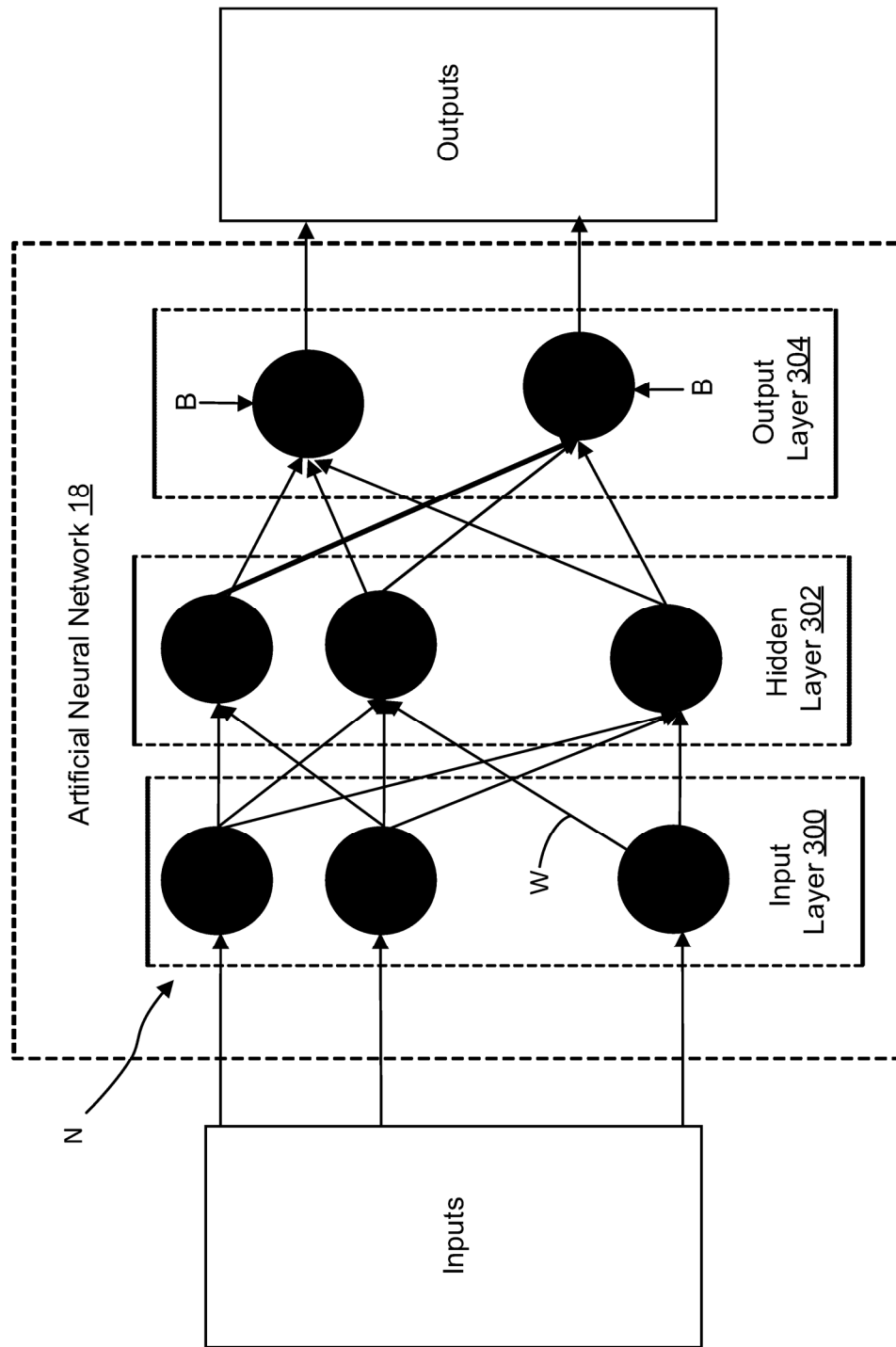


FIG. 4

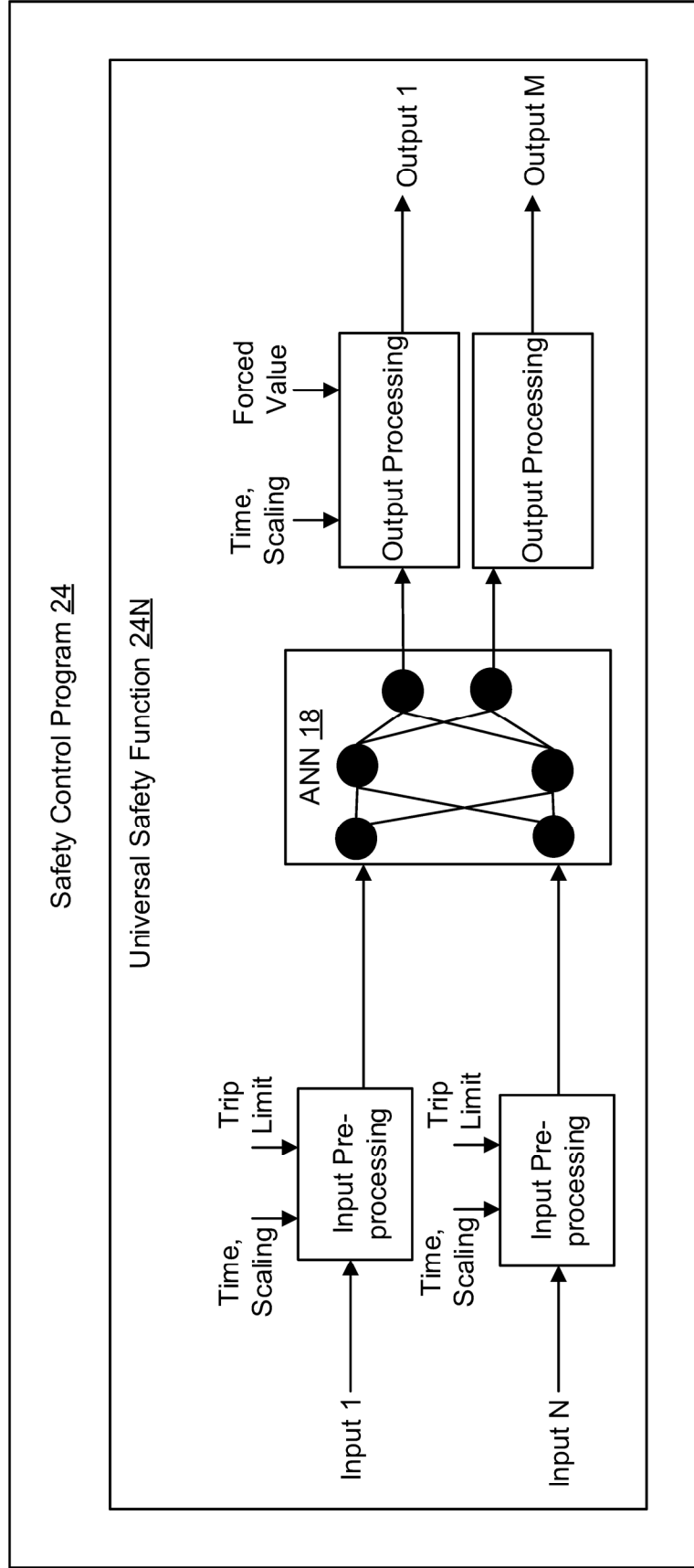


FIG. 5

