CLAIMS

1. An elevator comprising:

a board provided above a door device of a car, and comprising an outer wall facing a hall side, an inner wall facing inside of the car, and a bottom wall connecting the outer wall and the inner wall and comprising a window portion; and

a camera device provided in an upper portion of the window portion, and capturing the hall from the window portion in a state where a door of the door device is open, wherein

the camera device comprises a camera substrate on which a camera is mounted, an image processing unit and an interface unit in a housing,

the housing comprises a base to which the camera substrate and the units are secured,

a lens hole in which a lens of the camera is provided is formed in the base,

a center of the lens hole is located in a center of the base in a width direction of the base,

a distance from the center of the lens hole to one end is less than a distance from the center of the lens hole to the other end in a longitudinal direction of the base, and

the distance from the center of the lens hole to the one end is a specified distance in which, in a case where the door device opens one of right and left doors, the lens hole is provided at an appropriate position with respect to a door end frame of the door when the one end of the base is provided at a door opening side end of the board, and in a case where the door device opens the other one of right and left doors, the lens hole is provided at the appropriate position with respect to the door end frame of the door when the camera device is horizontally inverted and the one end of the base is provided at

the door opening side end of the board.

2. The elevator of claim 1, wherein a circuit substrate and a connector for connecting an external device are provided in the image processing unit, and the connector is provided in an upper portion of the housing.

3. The elevator of claim 2, wherein a surface of the circuit substrate of the image processing unit is vertically provided, and the connector is attached to an upper end of the circuit substrate.