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(54) Title: HIGH-PERFORMANCE CODE PARALLELIZATION COMPILER WITH LOOP-LEVEL PARALLELIZATION

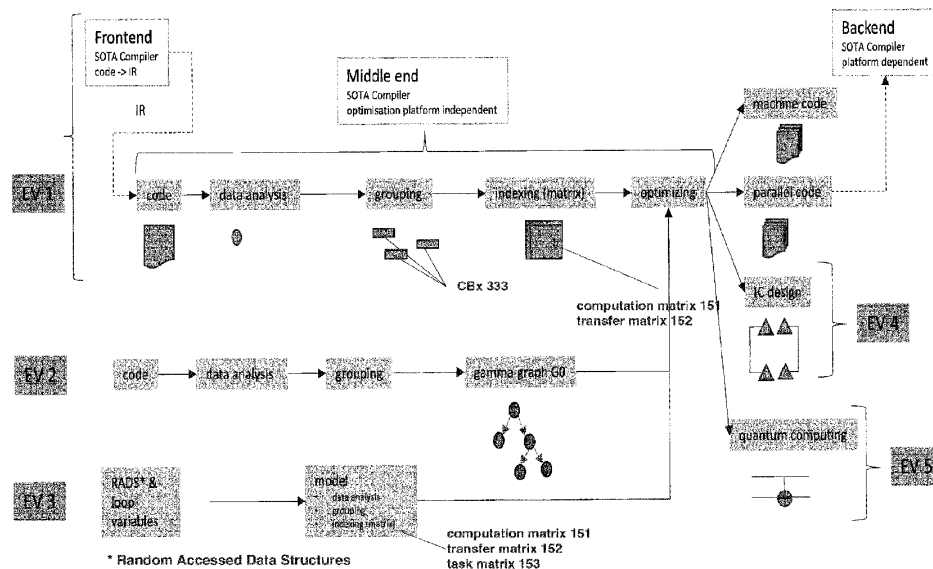


Fig. 106

(57) Abstract: Symmetric auto-compiler system (1) and method for high performance, hardware-optimized auto-parallelization of a program code (3) for execution by a multi-core or multi-processor parallel processing system (2) having a plurality of processing units (21) simultaneously processing instructions on data in the parallel processing system (2) by executing the program code (3). The auto-compiler system (1) transforms a serial source code (31) of the program code (3) into a parallel processing machine code (32) comprising a number of instructions executable by the plurality of processing units (21) of the parallel processing system (2) or controlling the operation of the plurality of processing units (21).

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