METHODS FOR PLANARIZATION OF GROUP VIII METAL-CONTAINING SURFACES USING OXIDIZING GASES

Field of the Invention

The present invention relates to methods for planarization of Group VIII metal-containing (preferably, platinum-containing) surfaces, particularly in the fabrication of semiconductor devices.

Background of the Invention

Films of metals and metal oxides, particularly the heavier elements of Group VIII, are becoming important for a variety of electronic and electrochemical applications. This is at least because many of the Group VIII metal films are generally unreactive, resistant to oxidation or retard the diffusion of oxygen, and are good conductors . Oxides of certain of these metals also possess these properties, although perhaps to a different extent.

Thus, films of Group VIII metals, their alloys, and metal oxides, particularly the second and third row metals (e.g., Ru, Os, Rh, Ir, Pd, and Pt) have suitable properties for a variety of uses in integrated circuits. For example, they can be used in integrated circuits for barrier materials, for example. They are particularly suitable for use as barrier layers between the dielectric material and the silicon substrate in memory devices. Furthermore, they are suitable as the plate (i.e., electrode) itself in capacitors.

Platinum is one of the candidates for use as an electrode for high dielectric capacitors. Capacitors are the basic charge storage devices in random access memory devices, such as dynamic random access memory (DRAM) devices, static random access memory (SRAM) devices, and now ferroelectric memory (FE RAM) devices. They consist of two conductors, such as parallel metal or polysilicon plates, which act as the electrodes (i.e., the storage node electrode and the cell plate capacitor electrode), insulated from each other by a

dielectric material (a ferroelectric dielectric material for FE RAMs). Thus, there is a continuing need for methods and materials for the processing of Group VIII metal-containing films, preferably, platinum-containing films.

Many surfaces that result during the formation of Group VIII metal-containing films, particularly in the wafer fabrication of semiconductor devices, do not have uniform height, and therefore, the wafer thickness is also non-uniform. Further, surfaces may have defects such as crystal lattice damage, scratches, roughness, or embedded particles of dirt or dust. For various fabrication processes to be performed, such as lithography and etching, height non-uniformities and defects at the surface of the wafer must be reduced or eliminated. Also, excess material may need to be removed to form a structure with selectivity relative to the underlying substrate. Various planarization techniques are available to provide such reduction and/or elimination. One such planarization technique includes mechanical and/or chemical-mechanical polishing (abbreviated herein as "CMP").

The process of planarization is used to remove material, and preferably achieve a planar surface, over the entire chip and wafer, sometimes referred to as "global planarity." Conventionally, the process of planarization, and particularly CMP, involves the use of a wafer holder that holds a wafer, a polishing pad, and an abrasive slurry that includes a dispersion of a plurality of abrasive particles in a liquid. The abrasive slurry is applied so that it contacts the interface of the wafer and the polishing pad. A table or platen has a polishing pad thereon. The polishing pad is applied to the wafer at a certain pressure to perform the planarization. At least one of the wafer and a polishing pad are set in motion relative to the other. In some planarization processes, the wafer holder may or may not rotate, the table or platen may or may not rotate and/or the platen may be moved in a linear motion as opposed to rotating. There are numerous types of planarization units available which perform the process in different manners. Alternatively, the polishing pad and abrasive slurry may be replaced by a fixed abrasive article that includes a plurality of abrasive particles dispersed within a binder adhered to at least one surface of a backing material.

The planarization of a surface that includes platinum and other Group VIII metals typically involves more mechanical than chemical action during a polishing process because they are relatively chemically inert and/or have relatively few volatile produces. Such mechanical polishing uses alumina and silica particles. Unfortunately, mechanical polishing tends to cause the formation of defects (e.g., scratches and particles), both of which can be detected optically, rather than the clean removal of the platinum. Also, many commercially available abrasive slurries do not effectively planarize platinum or other Group VIII metal-containing surfaces either because no material is removed (which results in no change in resistance of the wafer) or the resultant surface has defects therein.

Thus, there is still a need for methods for planarizing an exposed surface of a substrate that includes platinum and/or other Group VIII metals, particularly in the fabrication of semiconductor devices.

Summary of the Invention

The present invention provides methods that overcome many of the problems associated with the planarization of a surface that includes platinum and/or another of the Group VIII second or third row metals (i.e., Groups 8, 9, and 10, preferably, Rh, Ru, Ir, Pd, and Pt). Such a surface is referred to herein as a platinum-containing surface, or more generally, a Group VIII metal-containing surface. A "Group VIII metal-containing surface" refers to an exposed region having a Group VIII metal (particularly, platinum) preferably present in an amount of at least about 10 atomic percent, more preferably at least about 20 atomic percent, and most preferably at least about 50 atomic percent, of the composition of the region, which may be provided as a layer, film, coating, etc., to be planarized (e.g., via chemical-mechanical or mechanical planarization or polishing) in accordance with the present invention. The surface preferably includes one or more Group VIII metals in elemental form or an alloy thereof (with each other and/or one or more other metals of the Periodic Table), as well as oxides, nitrides, and silicides thereof. More preferably, the surface includes (and most preferably, consists essentially of)

one or more Group VIII metals in elemental form or an alloy of Group VIII metals only.

The methods of the present invention involve planarizing a surface using a planarization composition that preferably includes a solid or liquid oxidant (i.e., oxidizer or oxidizing agent) therein (either dispersed or dissolved therein) that has a standard reduction potential of at least about 1.4 versus a standard hydrogen electrode at 25°C.

Preferably, the oxidizing gas is present in the composition in an amount such that the solution is saturated. More preferably, the oxidizing gas is present in the composition in an amount of no greater than about 10% by weight, and more preferably, in an amount of about 0.1% to about 1% by weight. A particularly preferred group of oxidants includes oxygen, nitrous oxide, air, or combinations thereof.

Herein, as is conventionally understood, "planarizing" or "planarization" refers to the removal of material from a surface, whether it be a large or small amount of material, either mechanically, chemically, or both. This also includes removing material by polishing. As used herein, "chemical-mechanical polishing" and "CMP" refer to a dual mechanism having both a chemical component and a mechanical component, wherein corrosion chemistry and fracture mechanics both play a roll in the removal of material, as in wafer polishing.

The planarization composition can optionally include abrasive particles, thereby resulting in an abrasive slurry, and be used in planarization techniques with conventional polishing pads that do not have abrasive particles embedded therein. Alternatively, the planarization composition without abrasive particles therein can be used with fixed abrasive articles (also referred to as abrasive polishing pads) in place of conventional polishing pads. Such fixed abrasive articles include a plurality of abrasive particles dispersed within a binder adhered to at least one surface of a backing material. Whether in a fixed abrasive article or in the planarization composition, preferred abrasive particles have a hardness of no greater than about 9 Mohs. If the oxidizing gas is not stable in a composition with abrasive particles (i.e., an abrasive slurry),

they may be provided by separate delivery systems and/or in separate compositions and mixed at the point of use.

In one aspect of the present invention, a planarization method is provided that includes: positioning a Group VIII metal-containing surface of a substrate (preferably, a semiconductor substrate or substrate assembly such as a wafer) to interface with a polishing pad; supplying a planarization composition in proximity to the interface; and planarizing the Group VIII metal-containing surface. The Group VIII metal is selected from the group consisting of rhodium, iridium, ruthenium, osmium, palladium, platinum, and combinations thereof. The planarization composition includes an oxidizing gas having a standard reduction potential of at least about 1.4 versus a standard hydrogen electrode at 25°C.

In another aspect of the invention, a planarization method includes: positioning a Group VIII metal-containing surface of a substrate to interface with a polishing surface, wherein the Group VIII metal is selected from the group consisting of rhodium, iridium, ruthenium, osmium, palladium, platinum, and combinations thereof; supplying a planarization composition in proximity to the interface; and planarizing the Group VIII metal-containing surface; wherein the planarization composition comprises an oxidizing gas selected from the group consisting of oxygen, air, chlorine, nitrous oxide, nitric oxide, sulfur trioxide, an interhalogen, and combinations thereof.

In yet another aspect of the invention, a planarization method incudes: positioning a Group VIII metal-containing surface of a substrate to interface with a polishing surface, wherein the Group VIII metal is selected from the group consisting of rhodium, iridium, ruthenium, osmium, palladium, platinum, and combinations thereof; supplying a planarization composition in proximity to the interface; and planarizing the Group VIII metal-containing surface; wherein the planarization composition comprises an oxidizing gas having a standard reduction potential of at least about 1.4 versus a standard hydrogen electrode at 25°C, wherein the oxidizing gas is present in the composition in an amount of no greater than about 10% by weight.

Yet another aspect includes a planarization method that includes:

providing a semiconductor substrate or substrate assembly including at least one region of a platinum-containing surface; providing a polishing surface; providing a planarization composition at an interface between the at least one region of platinum-containing surface and the polishing surface; and planarizing the at least one region of platinum-containing surface; wherein the planarization composition comprises an oxidizing gas having a standard reduction potential of at least about 1.4 versus a standard hydrogen electrode at 25°C.

In another aspect there is provided a planarization method for use in forming a capacitor or barrier layer that includes: providing a wafer having a patterned dielectric layer formed thereon and a Group VIII metal-containing layer formed over the patterned dielectric layer, wherein the Group VIII metal is selected from the group consisting of rhodium, iridium, ruthenium, osmium, palladium, platinum, and combinations thereof; positioning a first portion of a polishing surface for contact with the Group VIII metal-containing layer; providing a planarization composition in proximity to the contact between the polishing surface and the Group VIII metal-containing layer; and planarizing the Group VIII metal-containing layer; wherein the planarization composition comprises an oxidizing gas having a standard reduction potential of at least about 1.4 versus a standard hydrogen electrode at 25°C.

As used herein, "semiconductor substrate or substrate assembly" refers to a semiconductor substrate such as a base semiconductor layer or a semiconductor substrate having one or more layers, structures, or regions formed thereon. A base semiconductor layer is typically the lowest layer of silicon material on a wafer or a silicon layer deposited on another material, such as silicon on sapphire. When reference is made to a substrate assembly, various process steps may have beenpreviously used to form or define regions, junctions, various structures or features, and openings such as capacitor plates or barriers for capacitors.

Brief Description of the Figures

Figure 1A and 1B are cross-sectional illustrations of one portion

of a wafer before and after a planarization process has been performed in accordance with the present invention.

Figures 2A and 2B are cross-sectional illustrations of one portion of a wafer before and after a planarization process has been performed in accordance with the present invention.

Description of the Preferred Embodiments

The present invention provides methods of planarization of a surface that includes platinum and/or one or more of the other Group VIII metals. The Group VIII metals are also referred to as the Group VIIIB elements or transition metals of Groups 8, 9, and 10 of the Periodic Table. The second and third row Group VIIIB metals include Rh, Ru, Ir, Pd, Pt, and Os. Preferably, surfaces that include Rh, Ru, Ir, Pd, and/or Pt can be planarized according to methods of the present invention. Such a surface is referred to herein as a Group VIII metal-containing surface (this refers to those containing second and/or third row transition metals).

Preferably, a "Group VIII metal-containing surface" refers to an exposed region having a Group VIII metal (particularly, platinum) present in an amount of at least about 10 atomic percent, more preferably at least about 20 atomic percent, and more preferably at least about 50 atomic percent, of the composition of the region, which may be provided as a layer, film, coating, etc., to be planarized (e.g., via chemical-mechanical or mechanical planarization or polishing) in accordance with the present invention.

The planarization of such surfaces, particularly a surface that includes platinum, typically involves mechanical methods with relatively hard particles such as alumina (Al_2O_3) and/or silica (SiO_2) particles, which by themselves can cause severe smearing, slow removal rates, and defects, rather than the clean removal of the material. Use of a planarization composition that includes an oxidizing gas either in combination with a plurality of abrasive particles in the composition or with a fixed abrasive article reduces, and often eliminates, the problem of defect formation, smearing, and often results in increased selectivity and removal rates.

Significantly, the methods of the present invention are particularly advantageous in removing platinum-containing or other second and third row Group VIII metal-containing materials (e.g., metals, alloys, oxides) from a surface in preference to materials containing other metals. This is important in selectively removing material from platinum-containing or other second and third row Group VIII metal-containing layers without removing, for example, significant amounts of underlying layers, such as oxide layers and nitride layers (e.g., TEOS or BPSG layers). Preferably, the selectivity for removal of material from a second and third row Group VIII metal-containing surface relative to materials containing other metals (e.g., BPSG or TEOS) is at least about 10:1, and more preferably, within a range of about 20:1 to about 100:1, depending on the chemistry and process conditions.

The planarization composition can be used in slurry planarization (i.e., in a conventional planarization process in which the planarization composition includes the abrasive particles with a polishing pad that does not include abrasive particles) or in fixed abrasive planarization. Thus, as used herein, a "polishing surface" refers to a polishing pad or a fixed abrasive article. Preferably, slurry planarization is used in the methods of the present invention. Preferably, when present in the planarization composition, the composition includes the abrasive particles in an amount of about 1% by weight to about 30% by weight, and more preferably, about 1% by weight to about 15% by weight, based on the total weight of the composition.

Typically, lower amounts of abrasive particles provide better selectivity of the Group VIII metal-containing surface relative to an oxide layer such as BPSG. Generally, however, the optimum level of selectivity for a particular metal is obtained by balancing the type and amount of abrasive particles, the type and amount of oxidizing gas, and the pH of the composition.

A wide variety of abrasive particles can be used either in an abrasive slurry or in a fixed abrasive article. Typically, such abrasive particles range in particle size (i.e., the largest dimension of the particle) on average from about 10 nanometers (nm) to about 5000 nm, and more often about 30 nm to about 1000 nm. For preferred embodiments, suitable abrasive particles have an

average particle size of about 100 nm to about 300 nm.

Examples of suitable abrasive particles include, but are not limited to, alumina (Al_2O_3), silica (SiO_2), ceria (CeO_2), titania (TiO_2), zirconia (ZrO_2), manganese dioxide (MnO_2), and tantalum dioxide (TaO_2). Preferred abrasive particles include alumina (Al_2O_3), silica (SiO_2), ceria (CeO_2), titania (TiO_2), and zirconia (ZrO_2). Various combinations of abrasive particles can be used if desired.

Preferably, for certain embodiments, abrasive particles include those that have a hardness of no greater than about 9 Mohs, and more preferably no less than about 6 Mohs. These include, for example, ceria (CeO_2), which has a hardness of about 6 Mohs, alumina (Al_2O_3), which has a hardness of about 9 Mohs, and silica (Si_2O_3), which has a hardness of about 7.

In certain methods in accordance with the present invention, preferably, a majority of the plurality of abrasive particles (either in an abrasive slurry or in a fixed abrasive article) are CeO_2 particles. This typically results in reduced formation of defects. In certain other methods in accordance with the present invention, preferably, a majority of the plurality of abrasive particles (either in an abrasive slurry or in a fixed abrasive article) have a hardness of about 8 Mohs to about 9 Mohs. This typically results in increased removal rates.

The planarization composition includes a gaseous oxidizing agent (i.e., oxidizer or oxidant) having a standard reduction potential of at least about 1.4 versus standard hydrogen electrode at 25°C. The oxidizing agent is a gas at room temperature. It is dissolved in the planarization composition, which typically includes water as the liquid medium.

Examples of suitable oxidizing gases include oxygen, ozone, air, chlorine, nitrous oxide, nitric oxide, sulfur trioxide, and an interhalogen (e.g. ClF_3). Various combinations of such oxidizing gases can be used in the methods of the present invention. A preferred group of oxidizing agents include oxygen, air, chlorine, nitrous oxide, nitric oxide, sulfur trioxide, and an interhalogen. A more preferred group of oxidizing agents include oxygen, nitrous oxide, and air. A most preferred group of oxidizing gases include oxygen and air.

Preferably, the oxidizing gas is present in the composition in an amount such that the solution is saturated. More preferably, the oxidizing gas is present in the composition in an amount of no greater than about 10% by weight, and more preferably, in an amount of about 0.1% to about 1% by weight, based on the total weight of the composition. For certain embodiments, however, higher amounts of the oxidant can be used if desired.

For various desired effects, a planarization composition can optionally and preferably include a complexing agent as described in Applicants' Assignee's copending U.S. Patent Application Serial Number 10/028,040, filed on December 21, 2001entitled METHODS FOR PLANARIZATION OF GROUP VIII METAL-CONTAINING SURFACES USING COMPLEXING AGENTS, to aid in the planarization. Such complexing agents are chosen such that their effectivity is not destroyed by the oxidizing gas. Other additives can be included as well for desired effects. These include, but are not limited to, a surfactant (e.g., polyethylene glycol, polyoxy ethylene ether, or polypropylene glycol) to enhance wettability and reduce friction, a thickener (e.g., CARBOPOL) to achieve a desired viscosity, a buffering agent (e.g., H_2SO_4, NH_4OH, acetate, and acetic acid) to achieve a desired pH, etc. Preferably, the composition is an aqueous solution of these components. More preferably, the aqueous planarization composition has an acidic pH.

For certain embodiments, the planarization composition includes a plurality of abrasive particles. For other embodiments, the planarization composition is essentially free of abrasive particles when supplied to the interface of the fixed abrasive article and the workpiece surface. However, in these latter embodiments, it is contemplated that planarization is accomplished by one or both of the fixed abrasive article and/or abrasive particles that may be removed from the fixed abrasive article at the fixed abrasive/surface interface in combination with the planarization composition. In any event, abrasive particles are typically not present in the composition as initially applied, i.e., they are not supplied from a source external to the polishing interface.

Preferably, a method in accordance with the present invention is

conducted at atmospheric pressure and at a temperature of about 40°F (about 4°C) to about 115°F (about 46°C), and more preferably at a temperature of about 75°F (24°C) to about 115°F (46°C). In many instances, however, it would be desirable to maintain a temperature at or below ambient temperature during planarization of Group VIII metal with a fixed abrasive article. This is seldom practical in slurry planarization (i.e., in a conventional planarization process in which the planarization composition includes the abrasive particles) where a lower slurry temperature likely results in poor dispersion of abrasive particles in the slurry composition during planarization. Accordingly, elevated temperatures are typically utilized during slurry planarization, although too high a temperature could result in too low a concentration of oxidizing gas in the planarization composition.

Various planarization assemblies or units for performing methods of the invention are readily available and are clearly contemplated by the scope of the present invention as described in the accompanying claims. Such planarization assemblies can create an interface between a polishing pad or a fixed abrasive article and the substrate surface (e.g., wafer surface) in various manners, e.g., rotation, movement, pressure, etc., to achieve planarization. A planarization composition is typically introduced at or near the interface, by a variety of methods such as by dripping, spraying, or other dispensing means, or by presoaking a polishing pad, although other locations and methods of introduction can be used.

In a typical planarizing machine, a polishing pad is fixed on a platen or table, a carrier assembly that includes a substrate holder to support the substrate (e.g., wafer) typically using suction, and a drive assembly to rotate and/or reciprocate the platen and/or a drive assembly to rotate and/or translate the substrate holder during planarization. Thus, conventional planarizing machines rotate the carrier assembly, the polishing pad, or both the carrier assembly and the polishing pad. In general, the planarizing machines are used to produce a planarization reaction product at the surface of a substrate whose hardness is less than the hardness of the abrasive particles and whose adhesion to the substrate is less than the original surface material; and to remove the

reaction produce using the abrasive particles.

Typically, the polishing pads, with or without abrasive particles embedded therein, are disk-shaped and rotatable about a fixed plane and axis at constant or variable speeds. Typically, the speed of rotation ranges from about 2 revolutions per minute (rpm) to about 200 rpm.

Typically, a polishing pad is presoaked and continually rewet with the planarization composition. If the polishing pad does not include abrasive particles embedded therein, the planarization composition includes abrasive particles, which is then referred to as an abrasive slurry. The planarization composition may be applied to the interface between a polishing pad and a substrate surface using a variety of techniques. For example, the component parts of the composition may be applied separately and mixed at the interface or immediately before contacting the interface. The planarization composition can be applied by pumping it through the pad. Alternatively, it can be applied at the leading edge of the pad, although this may not provide uniform distribution of the planarization composition across the surface being planarized, which is desirable.

The polishing pad can be any of a wide variety of conventional polishing pads that are used with abrasive slurries. They can be made from a material such as polyurethane, polyester, acrylic, acrylic ester copolymers, polytetrafluoroethylene, polypropylene, polyethylene, cellulose, cellulose esters, polamides, polyimides, polysiloxane, polycarbonates, epoxides, phenolic resins, etc. They include, for example, a polyurethane-based foam material, wherein the foam cell walls of the pad aid in removal of reaction products at the wafer surface and the pores within the pad assist in supplying slurry to the pad/wafer interface. They can include convex or concave features, which can be formed by embossing a surface pattern. For example, a polishing pad can have continuous grooves in concentric ellipses in the surface of the pad for more uniform slurry delivery and more effective debris removal. Commercially available polishing pads can be obtained under the trade designations "URII," "Sycamore," and "Polytex" from Rodel, Phoenix, AZ. Examples of polishing pads are also disclosed in U.S. Pat. No. 6,039,633 (Chopra).

In general, a fixed abrasive article includes a plurality of abrasive particles dispersed within a binder that forms a three-dimensional fixed abrasive element that is adhered to one surface of a backing material. They are described, for example, in U.S. Patent No. 5,692,950 (Rutherford, et al.) and International Patent Publication WO 98/06541. Commercially available fixed abrasive articles can be obtained from Tokyo Sumitsu Kageki and Ebera Corporation, both of Japan, and Minnesota Mining and Manufacturing Company (3M Company) of St. Paul, MN. An example of a preferred fixed abrasive article is a ceria-based pad commercially available from 3M Company under the trade designation "SWR 159." Such fixed abrasive articles can be used with a planarization composition as described herein with or without abrasive particles in the planarization composition.

It is highly desirable to have a high polishing rate (i.e., the rate at which material is removed from the substrate) to reduce the duration of each planarizing cycle, the polishing rate is preferably uniform across the substrate to produce a uniformly planar surface. Preferably, the polishing rate is controlled to provide accurate, reproducible results. Also, preferably, the planarization process is carried out in one cycle (i.e., one step). That is, for the removal of any material from a particular surface, there is only one planarization cycle without any intervening rinse cycles. This planarization process is then typically followed by a post-planarization clean process in which abrasive particles are not used.

The figures provide further information about the methods of the invention. Figure 1A illustrates one portion of a wafer 10 prior to planarization in accordance with the present invention having features that are filled with the material to be removed through planarization. The wafer portion 10 includes a substrate assembly 12 having junctions 16 formed thereon. A capacitor and/or barrier layer material 19 is then formed over the substrate assembly 12 and the junctions 16. The capacitor and/or barrier layer material 19 may be any conductive material such as platinum or any other suitable conductive second or third row Group VIII metal-containing capacitor and/or barrier material. Generally, as shown in Figure 1A, the nonplanar upper surface 13 of capacitor

and/or barrier layer 19 is subjected to planarization or other processing in accordance with the present invention. The resulting wafer 10, which is shown in Figure 1B, includes an upper surface 17 planarized such that the thickness of the wafer 10 is substantially uniform across the entire wafer 10 so that the wafer now includes a capacitor and/or barrier structure layer 14.

Figure 2A illustrates one portion of a wafer 20 prior to planarization in accordance with the present invention having features that have a conformal layer of the material to be removed through planarization. The wafer portion 20 includes a substrate assembly 22 having a patterned dielectric layer 26 formed thereon. Such a patterned dielectric layer 26 can be used in a variety of structures, particularly a capacitor structure. The patterned dielectric layer 26 can be formed of any material that provides electrical isolation between metal regions (e.g., silicon dioxide, silicon nitride, or BPSG). An electrode layer 29 is then formed over the substrate assembly 22 and the patterned dielectric layer 26. The electrode layer 29 may be platinum or any other suitable conductive second or third row Group VIIIB or Group IB metal-containing material. Generally, as shown in Figure 2A, the nonplanar upper surface 23 of electrode layer 29 is subjected to planarization or other processing in accordance with the present invention. The resulting wafer 20, as shown in Figure 2B, includes an upper surface 27 planarized such that the thickness of the wafer 20 is substantially uniform across the entire wafer 20 so that the wafer now includes electrically conducting regions 24 isolated within the patterned dielectric material 26 forming a capacitor structure. If desired, prior to planarization, the conformal layer 29 and openings 24 can be covered with a photoresist or other material that is removed after the planarization so the abrasive does not fall into the openings 24.

These figures are shown only to illustrate a surface having nonuniformities, such as height differences, in the fabrication of semiconductor devices. The present invention is not limited to use with nonplanar surfaces, such as that shown in figures. The present invention is also beneficial for use with substantially planar surfaces. For example, the methods in accordance with the present invention is beneficial during the whole planarization process,

even at the end of the process when the surface being planarized is in a substantially planar state.

The following example is offered to further illustrate the various specific and preferred embodiments and techniques. It should be understood, however, that many variations and modifications may be made while remaining within the scope of the present invention.

Example

A blanket platinum wafer is used as a test sample. A slurry is prepared by diluting three liters of Rodel Granite Part A slurry with three liters of water. To that mixture 10 mL of concentrated acetic acid is added. The mixture is stirred for thirty minutes. For the polish, a conventional, rotary polisher such as AMAT MIRRA is used with a Rodel URII pad. The tool is modified to allow the addition of oxygen into the slurry shortly before entering the pad. The gas is added with a flow of 10 sccm. The polish is performed with a down force of 4.5 lbs, table speed of 55 rpm and head speed of 50 rpm. Slurry flow is set to 150 mL/min. After polishing the wafer is cleaned with conventional scrubbing.

The foregoing detailed description and example have been given for clarity of understanding only. No unnecessary limitations are to be understood therefrom. The invention is not limited to the exact details shown and described, for variations obvious to one skilled in the art will be included within the invention defined by the claims. For example, while the description above focused on planarization of semiconductor-based substrates, the compositions and methods of the invention are also applicable to, for example, polishing glasses and contact lenses, as one of many other possible applications. The complete disclosures of all patents, patent documents, and publications listed herein are incorporated by reference, as if each were individually incorporated by reference.

What Is Claimed Is:

1. A planarization method comprising:

positioning a Group VIII metal-containing surface of a substrate to interface with a polishing surface, wherein the Group VIII metal is selected from the group consisting of rhodium, iridium, ruthenium, osmium, palladium, platinum, and combinations thereof, and further wherein the substrate surface comprises one or more materials containing one or more other metals;

supplying an acidic planarization composition in proximity to the interface;

feeding an oxidizing gas into planarization composition; and

planarizing the Group VIII metal-containing surface to selectively remove the Group VIII metal-containing surface relative to materials containing other metals at a selectivity ratio of at least 10:1;

wherein the oxidizing gas is selected from the group consisting of oxygen, air, nitrous oxide, nitric oxide, sulphur trioxide, and combinations thereof;

wherein the polishing surface comprises a fixed abrasive article or a polishing pad; and

wherein when the polishing surface comprises the polishing pad the planarization composition comprises a plurality of abrasive particles having a hardness of no greater than 9 Mohs.

2. The method of claim 1 wherein the Group VIII metal-containing surface of the substrate comprises a Group VIII metal in elemental form or an alloy thereof.

3. The method of claim 2 wherein the Group VIII metal-containing surface comprises elemental platinum, rhodium, iridium, ruthenium, or a combination thereof.

4. The method of claim 3 wherein the Group VIII metal-containing surface comprises elemental platinum.

5. The method of any one of claims 1-4 wherein the Group VIII metal is present in an amount of about 10 atomic percent or more.

6. The method of any one of claims 1-4 wherein the substrate is a semiconductor substrate or substrate assembly.

7. The method of any one of claims 1-4 wherein the plurality of abrasive particles comprise CeO_2, Al_2O_3, SiO_2, and mixtures thereof.

8. The method of any one of claims 1-4 which is carried out in one step.

9. The method of any one of claims 1-4 wherein the oxidizing gas is selected from the group consisting of oxygen, air, and combinations thereof.

10. The method of claim 9 wherein the oxidizing gas is oxygen.

11. The method of any one of claims 1-4, wherein the

oxidizing gas is fed into the composition in an amount of no greater than about 10% by weight.

12. The method of any one of claims 1-4, wherein the substrate is a semiconductor substrate or substrate assembly including at least one region of a platinum- containing surface;] providing a polishing surface; and the method comprises planarizing the at least one region of platinum-containing surface.

13. The method of claim 12 wherein the platinum-containing surface comprises elemental platinum in an amount of about 10 atomic percent or more.

14. The method of claim 12 wherein the platinum-containing surface comprises a platinum alloy.

15. The method of claim 12 wherein the semiconductor substrate or substrate assembly is a silicon wafer.

16. The method of claim 12 wherein the oxidizing gas is selected from the group consisting of oxygen, nitrous oxide, air, or combinations thereof.

17. The method of claim 16 wherein the oxidizing gas is selected from the group consisting of oxygen, air, or combinations thereof.

18. The method of claim 17 wherein the oxidizing gas is selected from the group consisting of oxygen.

19. The method of any one of claims 1-4 wherein the substrate is a wafer having a patterned dielectric layer formed thereon and a Group VIII metal-containing layer formed over the patterned dielectric layer, and wherein the planarization method is used in forming a capacitor or barrier layer.