

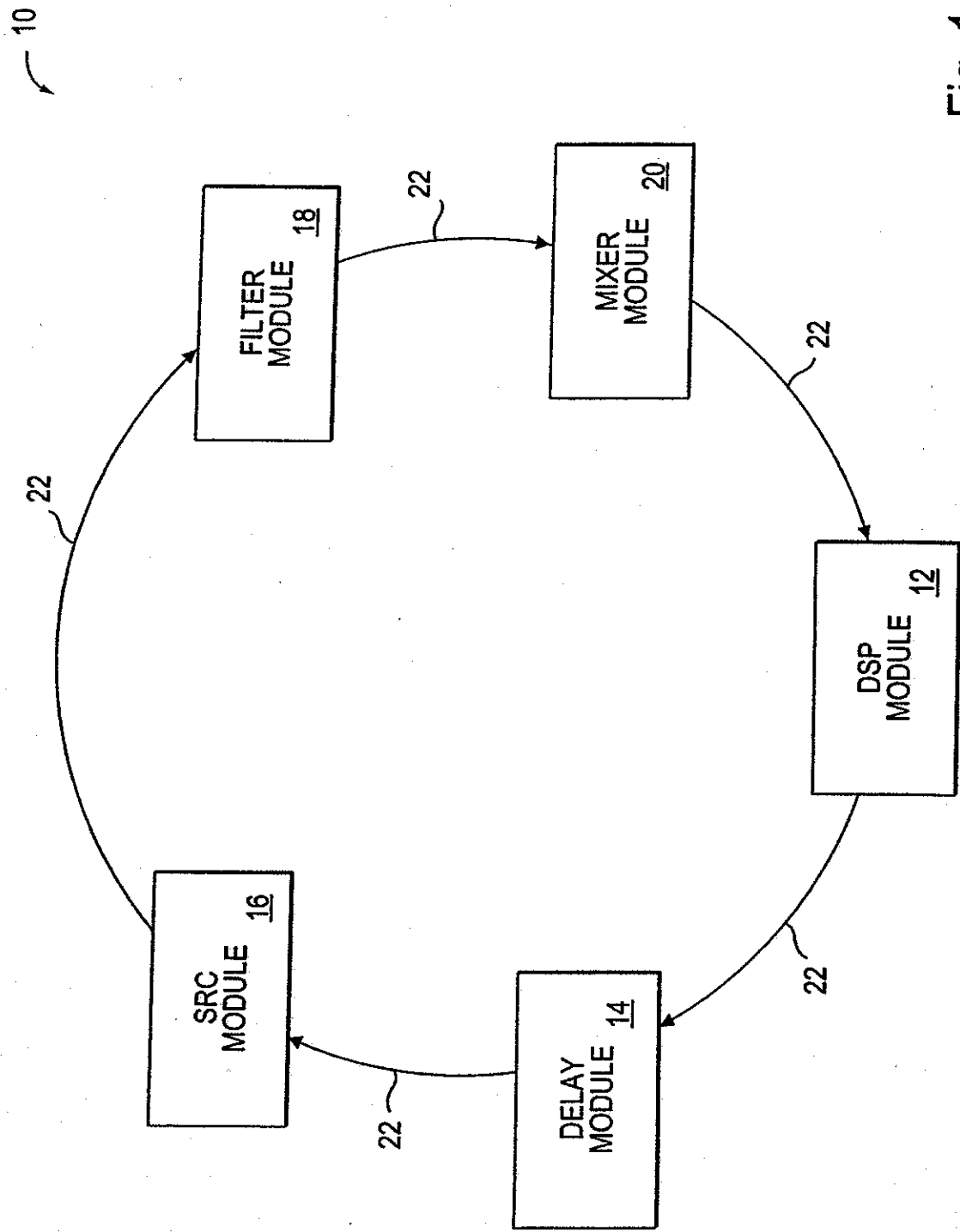


Fig. 1

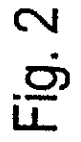


Fig. 2

54

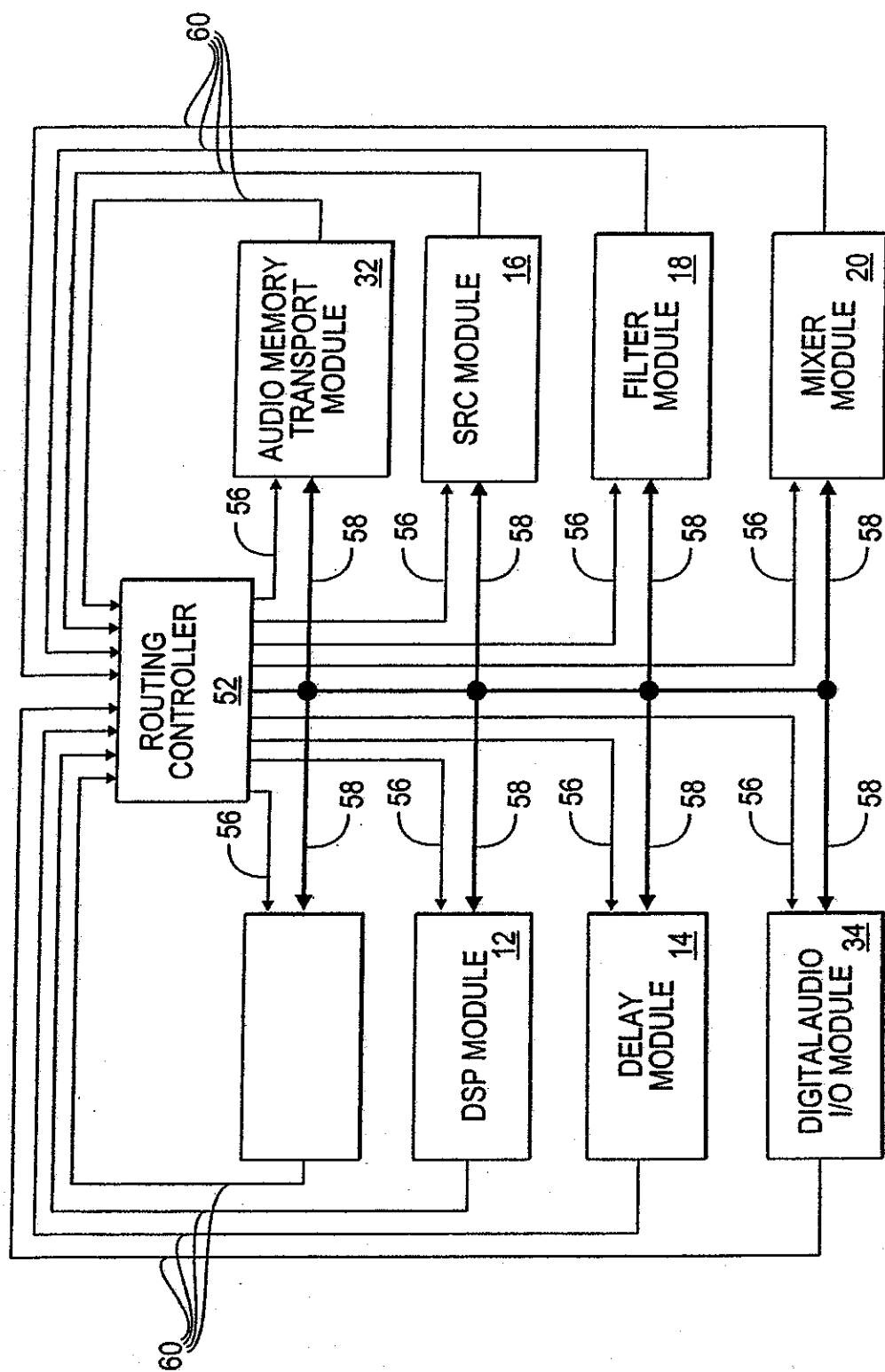


Fig. 3

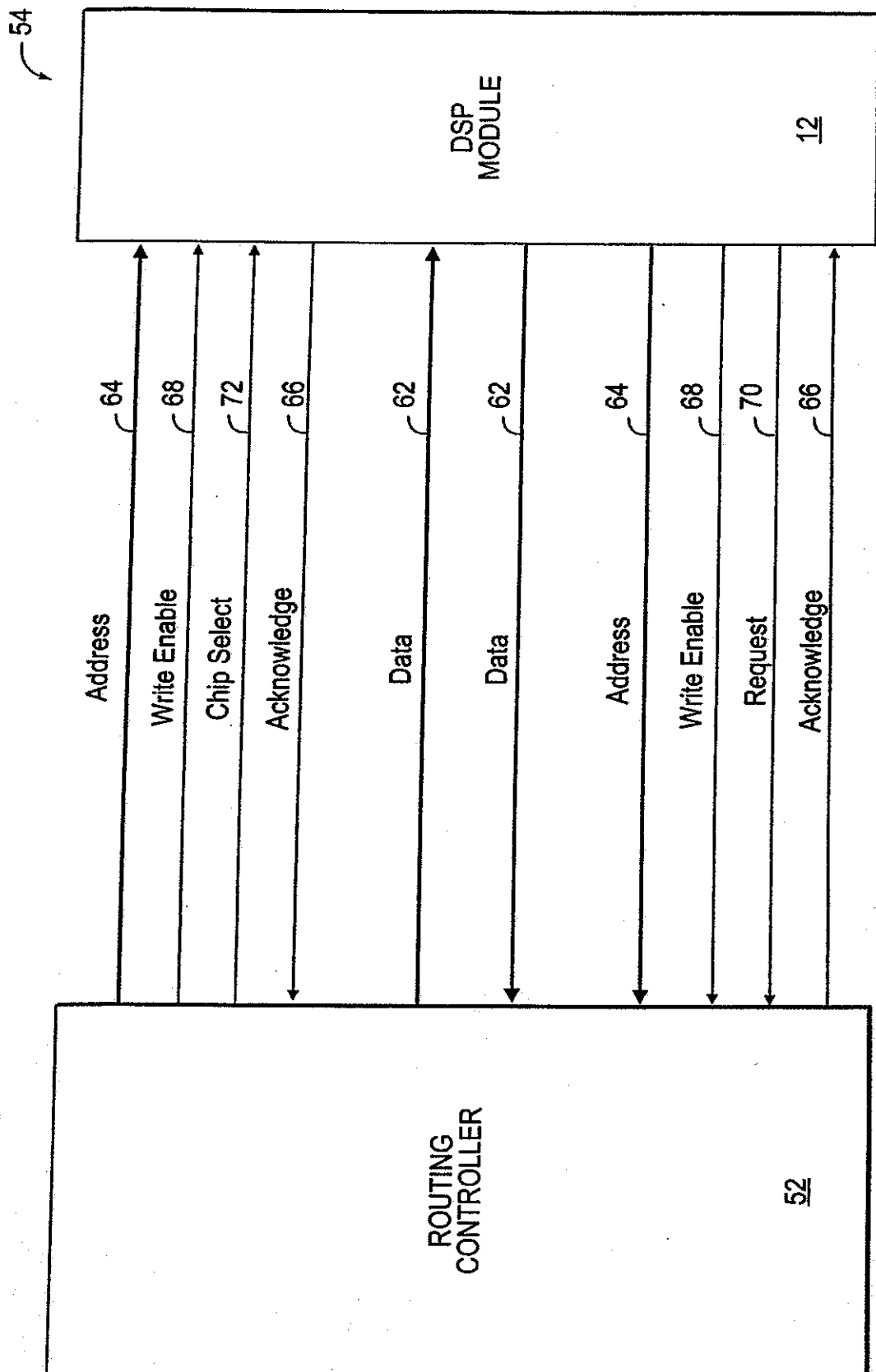


Fig. 4

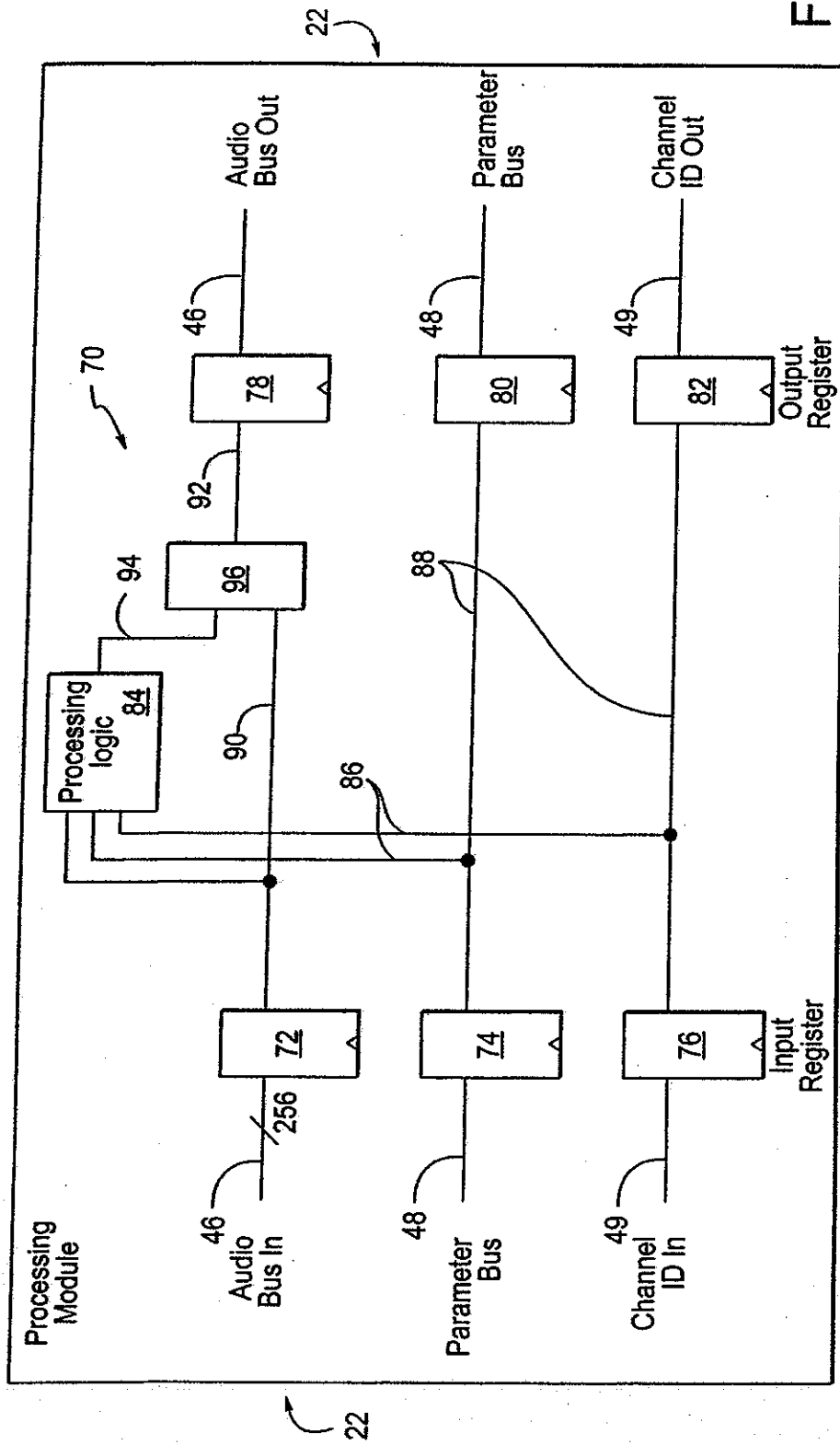


Fig. 5

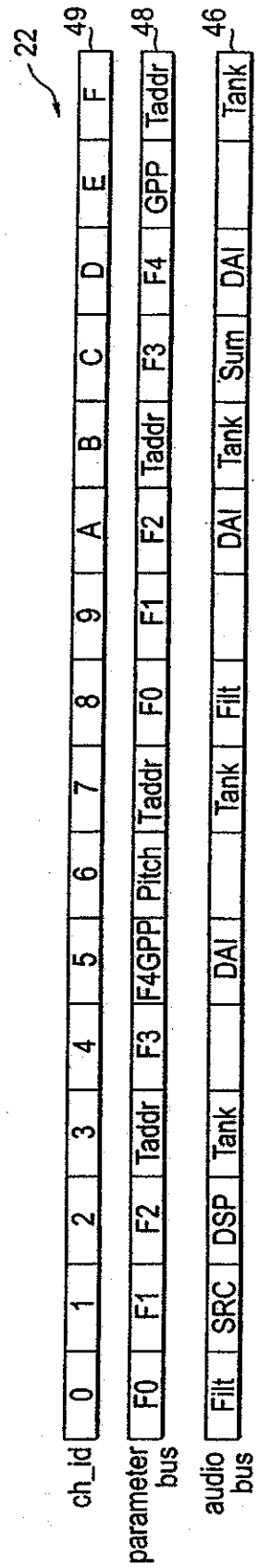


Fig. 6

100

next linked List address	input channel ID	input audio buffer address
• • •	• • •	• • •
21	2	21
22	71	22
• • •	• • •	• • •
22	3	379
• • •	• • •	• • •

101

102

104

106

next

Fig. 7

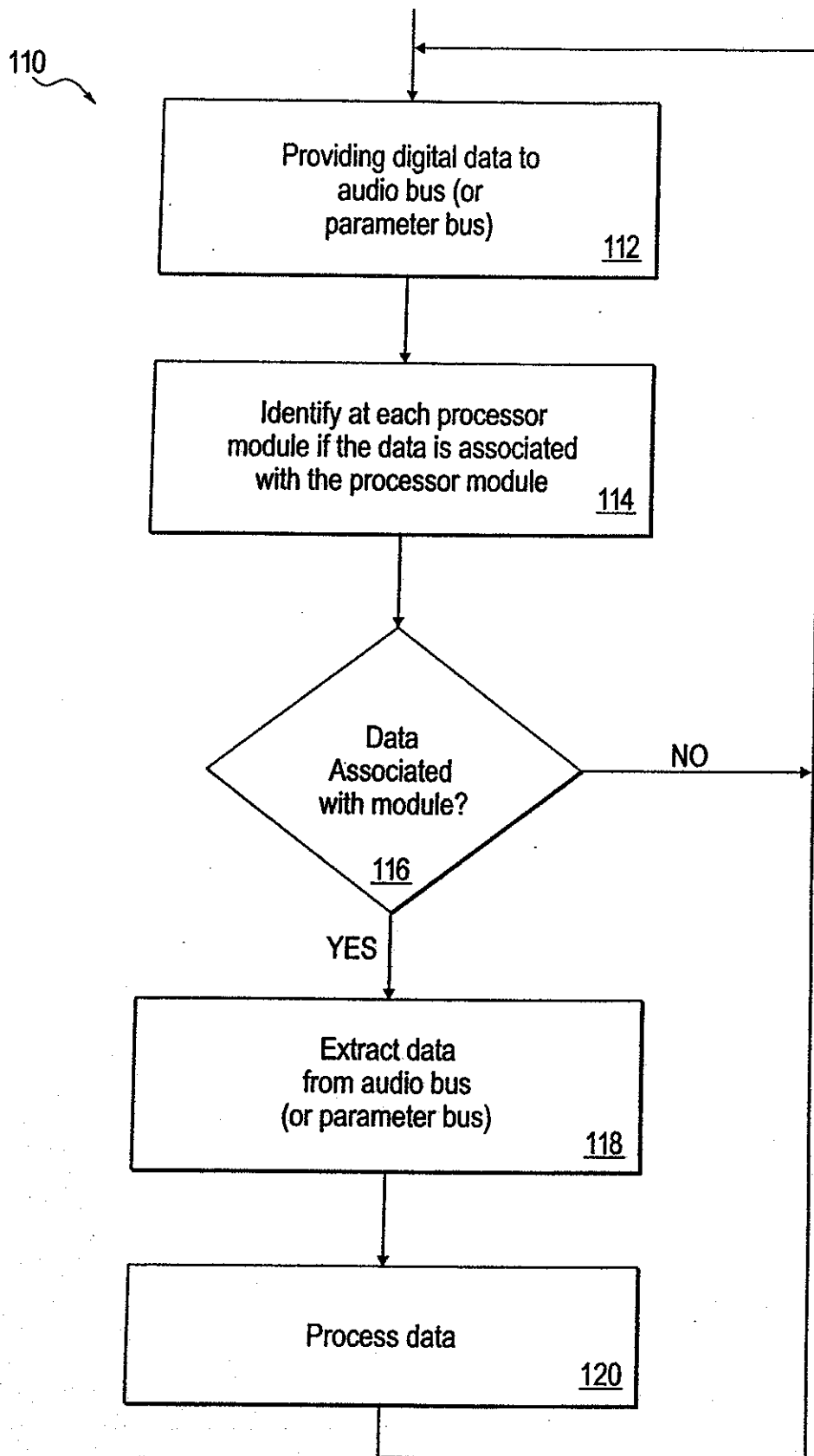


Fig. 8