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(71) Applicant: **LANDIS+GYR INNOVATIONS, INC.**
[US/US]; 30000 Mill Creek Avenue, Suite 100, Alpharetta,
Georgia 30022 (US).

(72) Inventors: **HETT, Christopher**; 30000 Mill Creek Av-
enue, Suite 100, Alpharetta, 30022 (US). **HANLEY, James**
Patrick; 30000 Mill Creek Avenue, Suite 100, Alpharetta,
Georgia 30022 (US).

(74) Agent: **HOLMES, Brenda**; Suite 2800, 1100 Peachtree
Street NE, Atlanta, Georgia 30309 (US).

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(54) Title: OVERLOADING BROADCAST DWELL INTERVALS IN UNSYNCHRONIZED CHANNEL HOPPING MESH NET-
WORKS

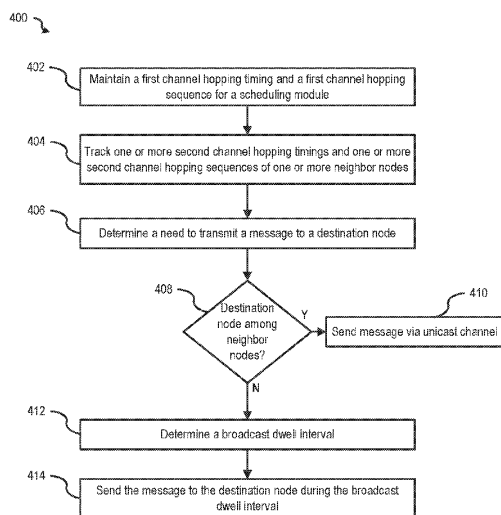


FIG. 4

(57) Abstract: A node includes a processor coupled to a wire-
less transceiver and a memory. The memory includes instructions
that can cause the processor to store a broadcast channel hop-
ping timing interval and a broadcast channel hopping sequence,
as well as, channel hopping timings and channel hopping se-
quences for tracked neighbor nodes. The instructions can also
cause the processor to determine whether a destination node
for a unicast message corresponds to tracked neighbor nodes.
Based on a determination that the destination node does not
correspond to any tracked neighbor nodes, the processor can
determine (i) a broadcast dwell interval based on the broadcast
channel hopping timing interval and (ii) a next broadcast chan-
nel based on the broadcast channel hopping sequence. Addi-
tionally, instructions can cause the processor to transmit the
unicast message to the destination node during the broadcast
dwell interval.