

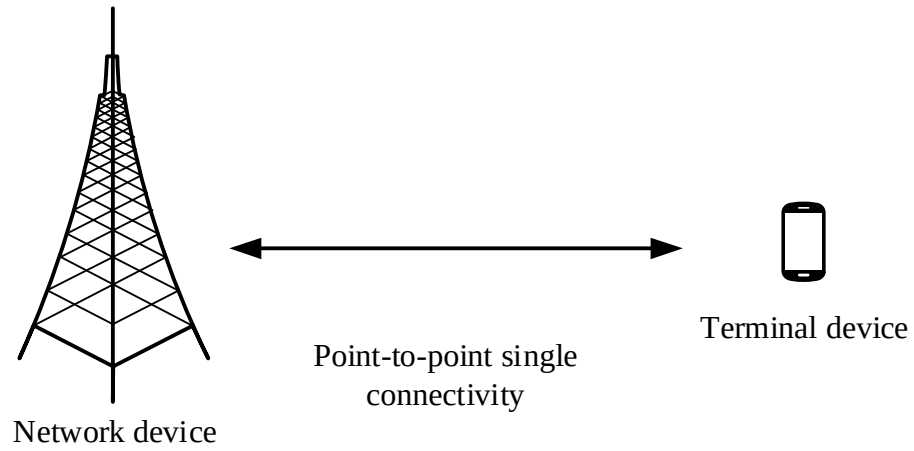


FIG. 1a

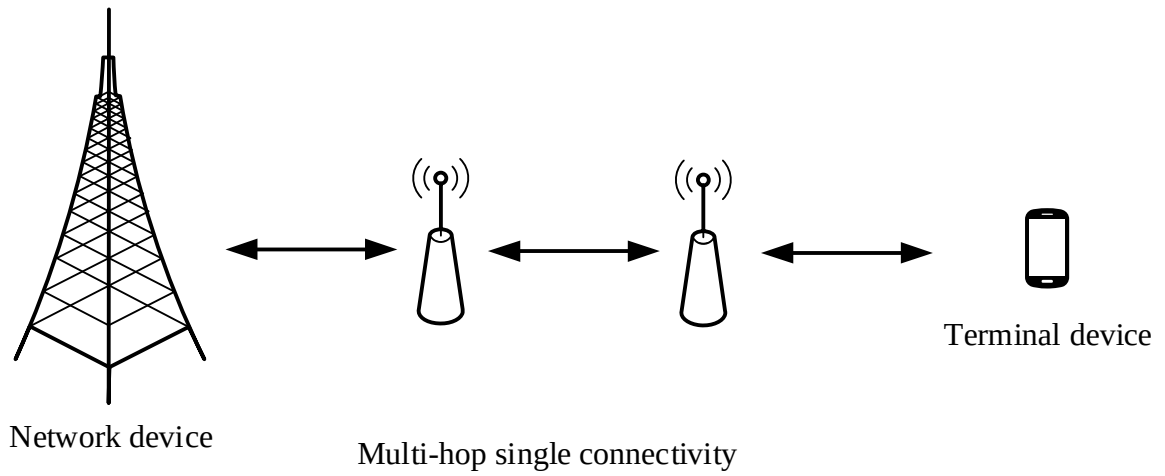
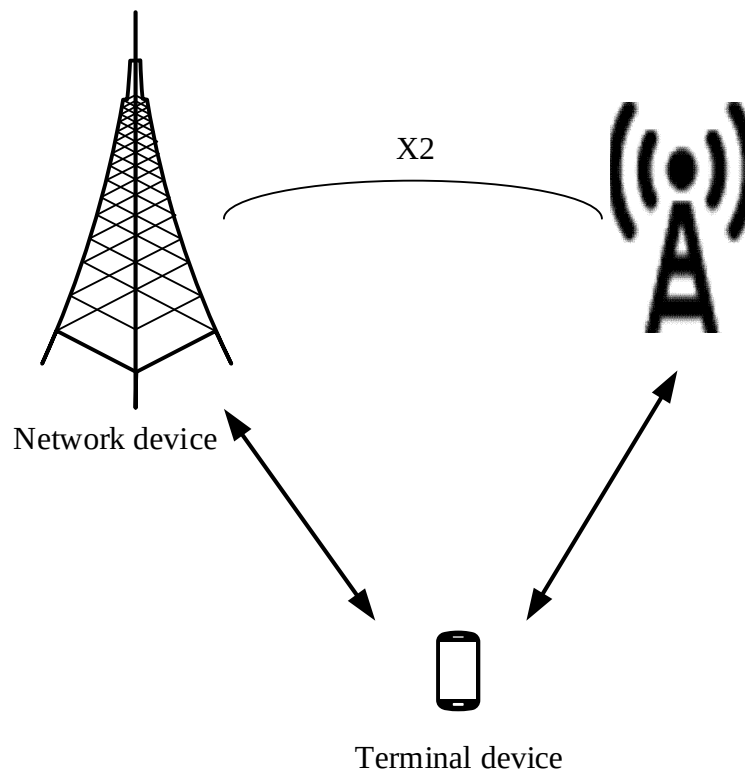


FIG. 1b



Dual connectivity DC

FIG. 1c

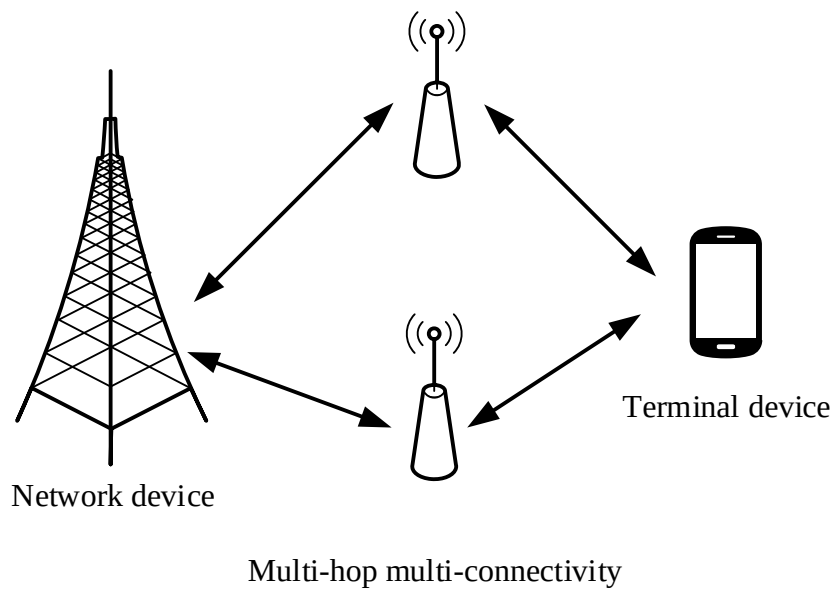


FIG. 1d

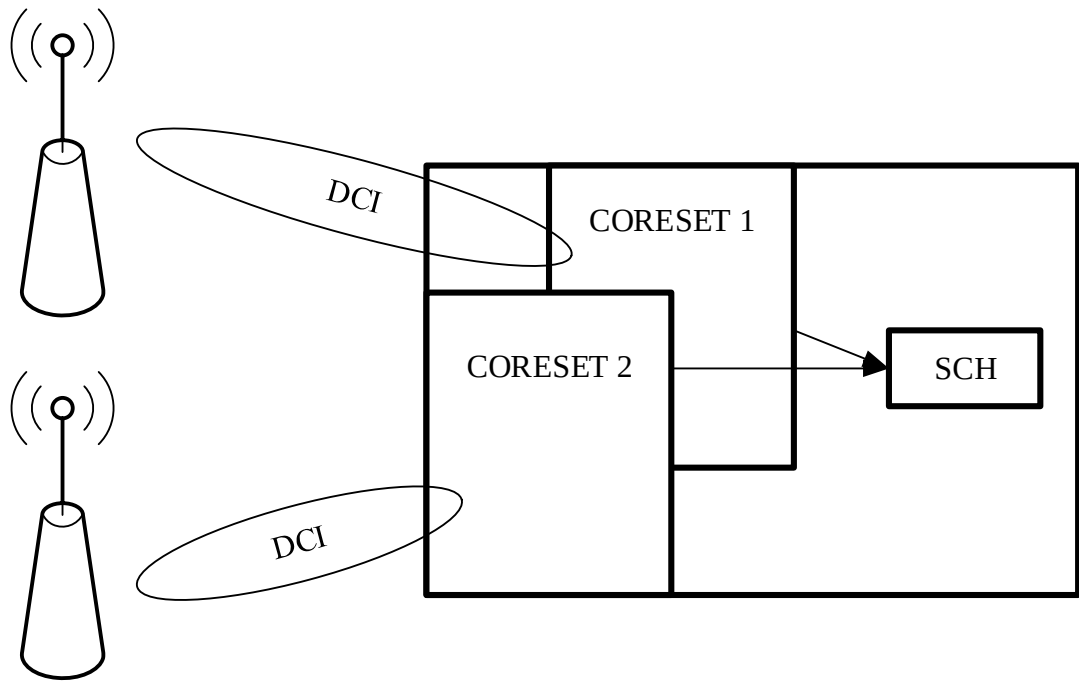


FIG. 2

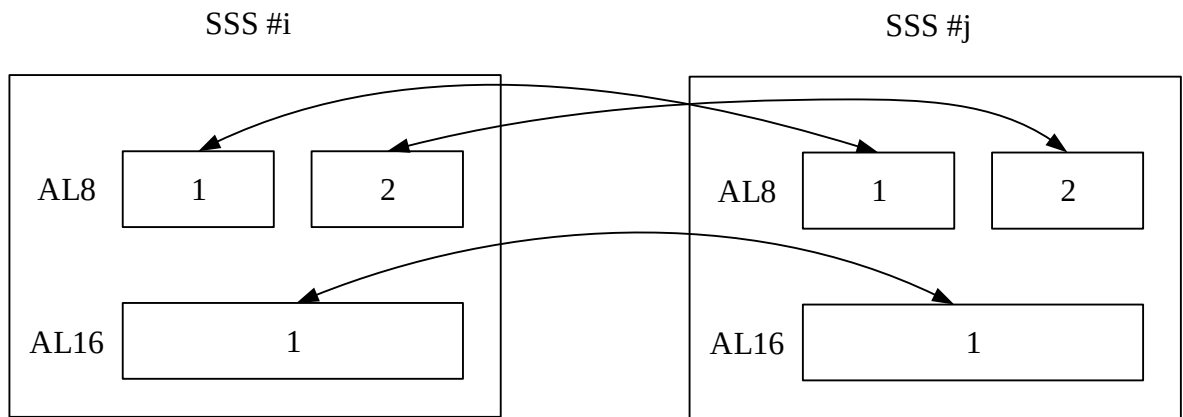


FIG. 3

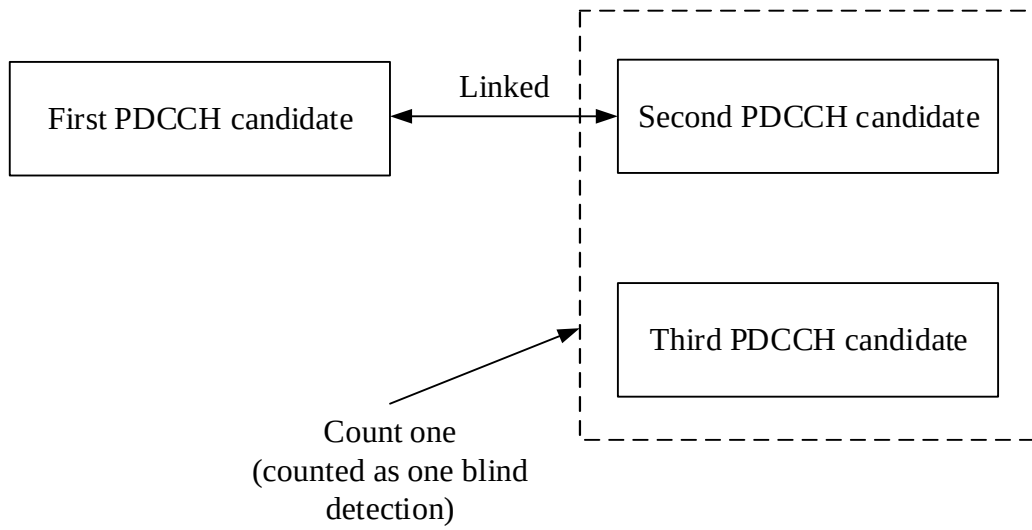


FIG. 4

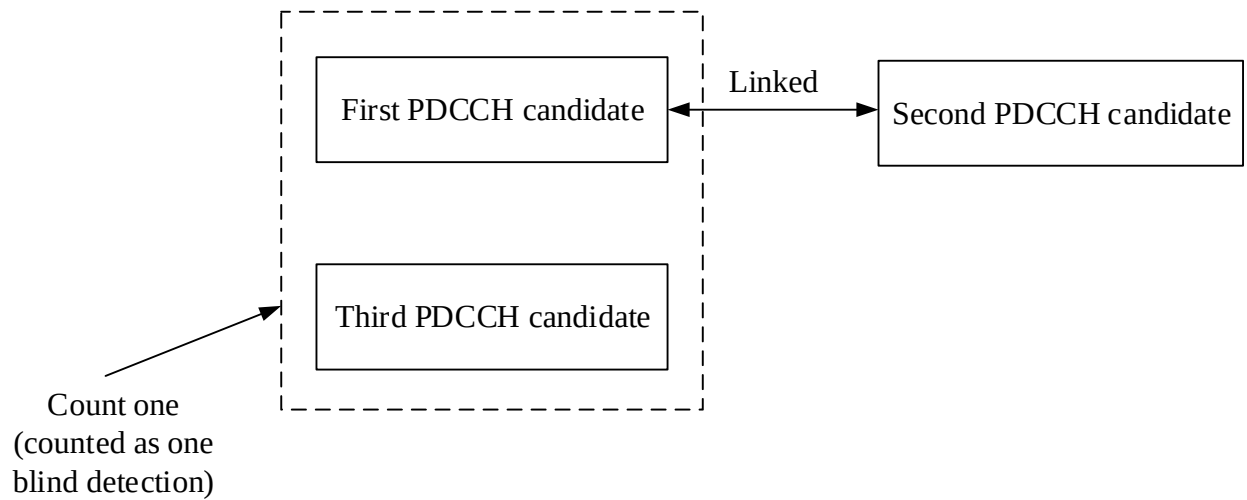


FIG. 5

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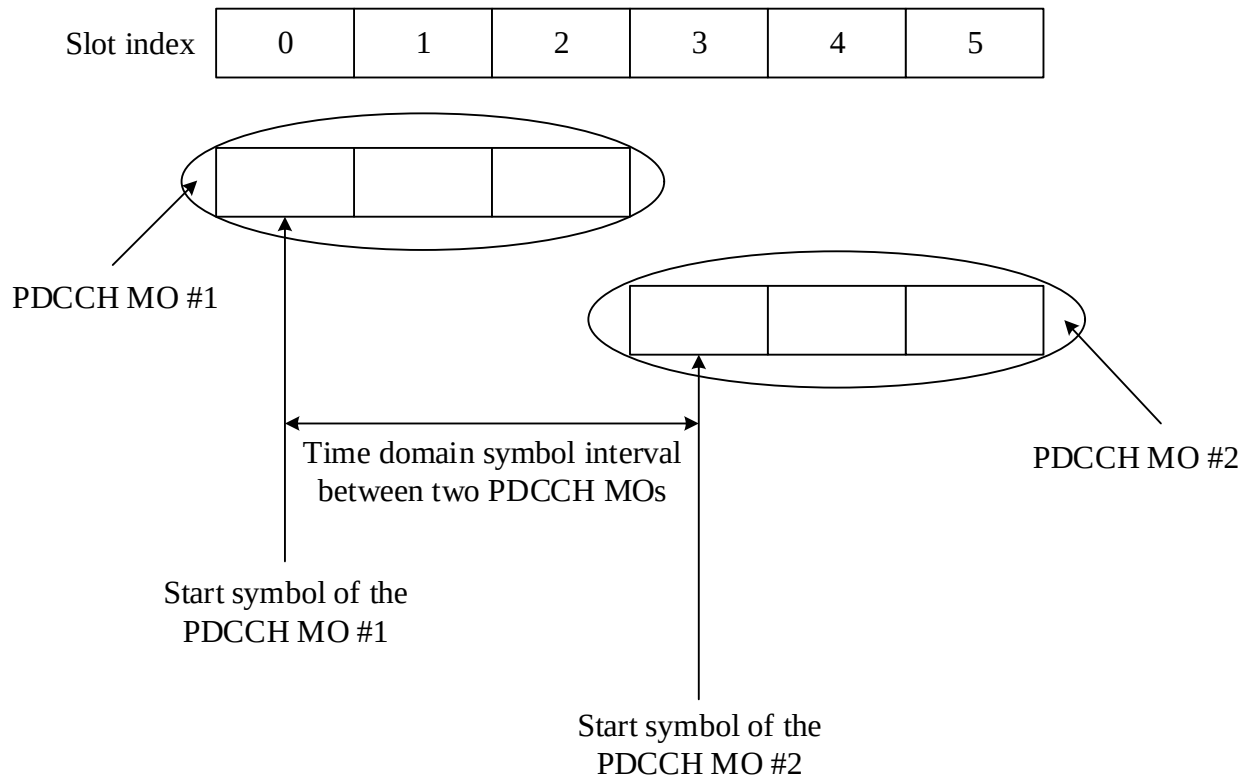


FIG. 6

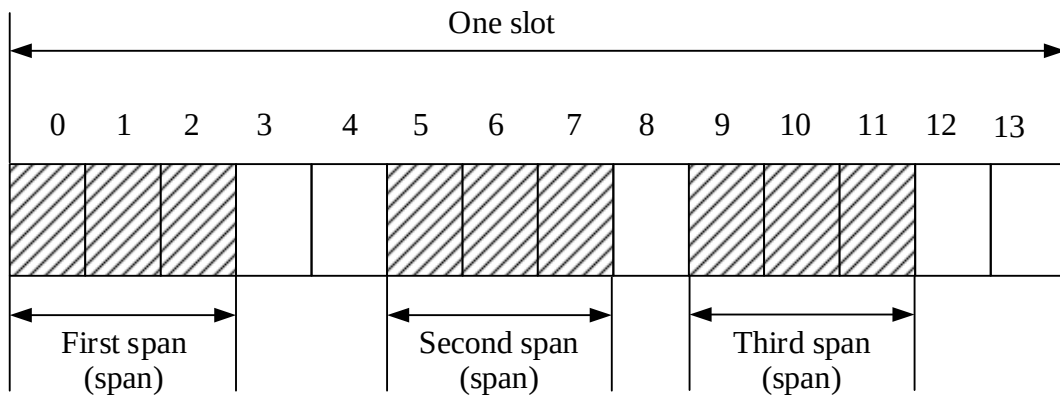


FIG. 7

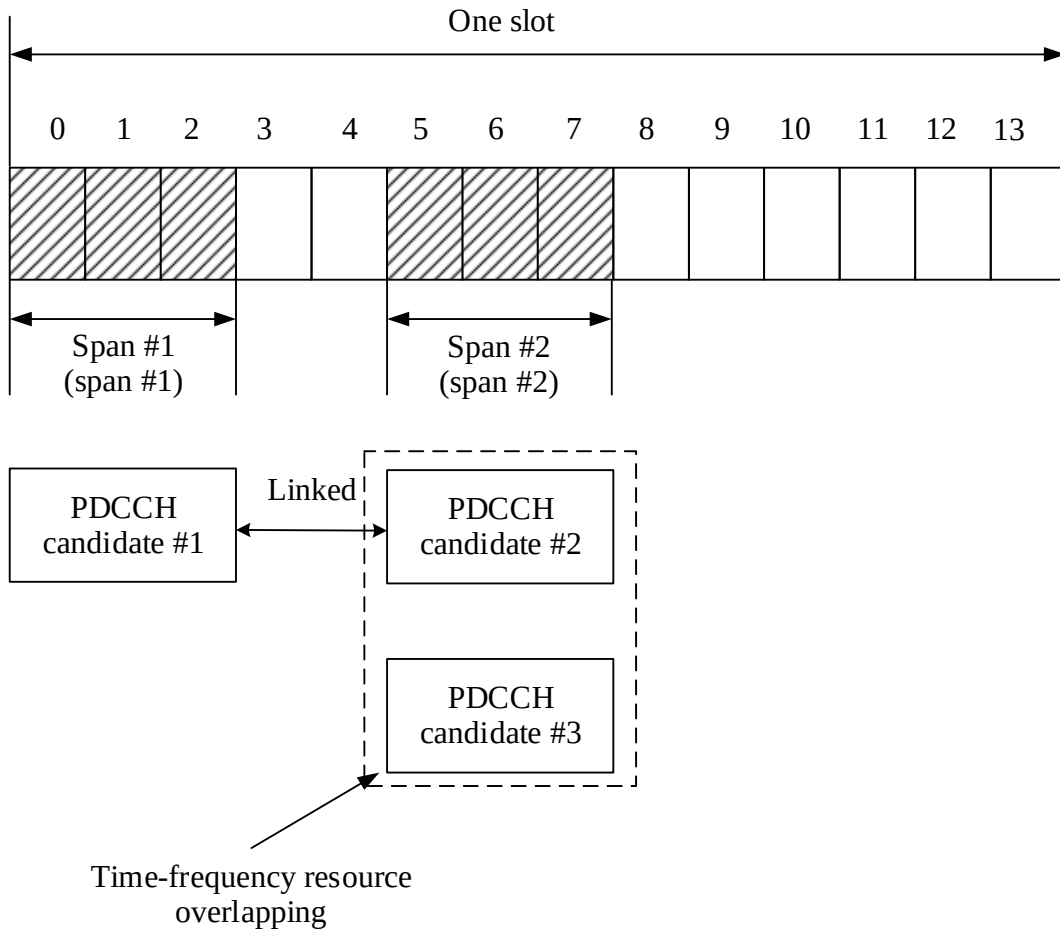


FIG. 8

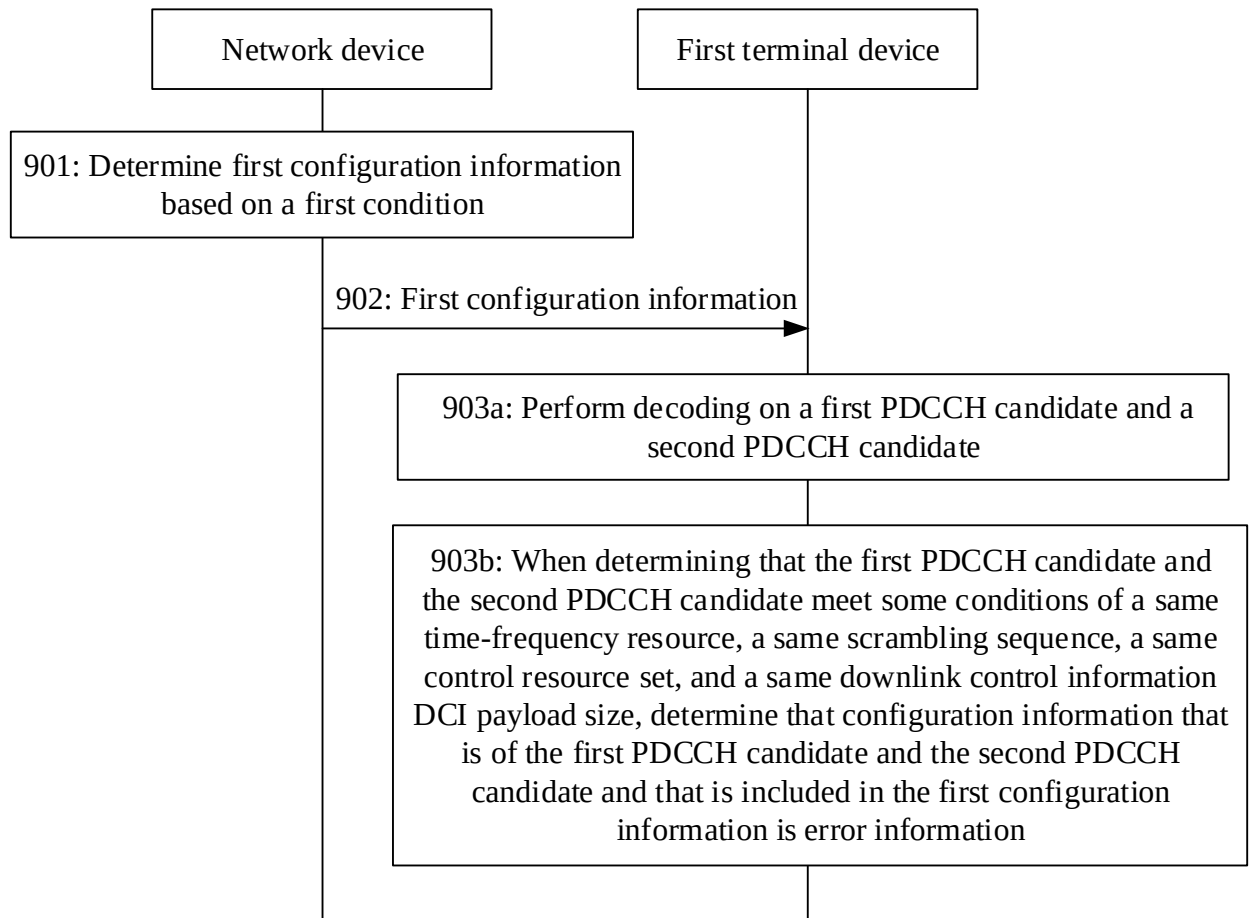


FIG. 9

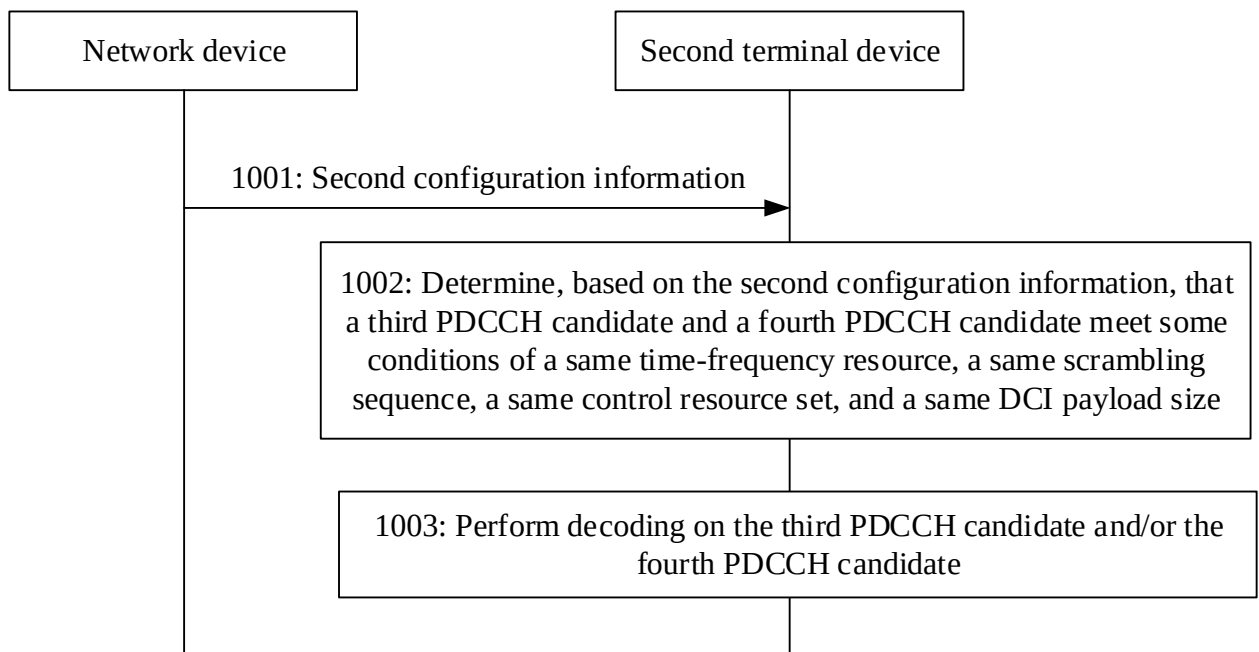


FIG. 10

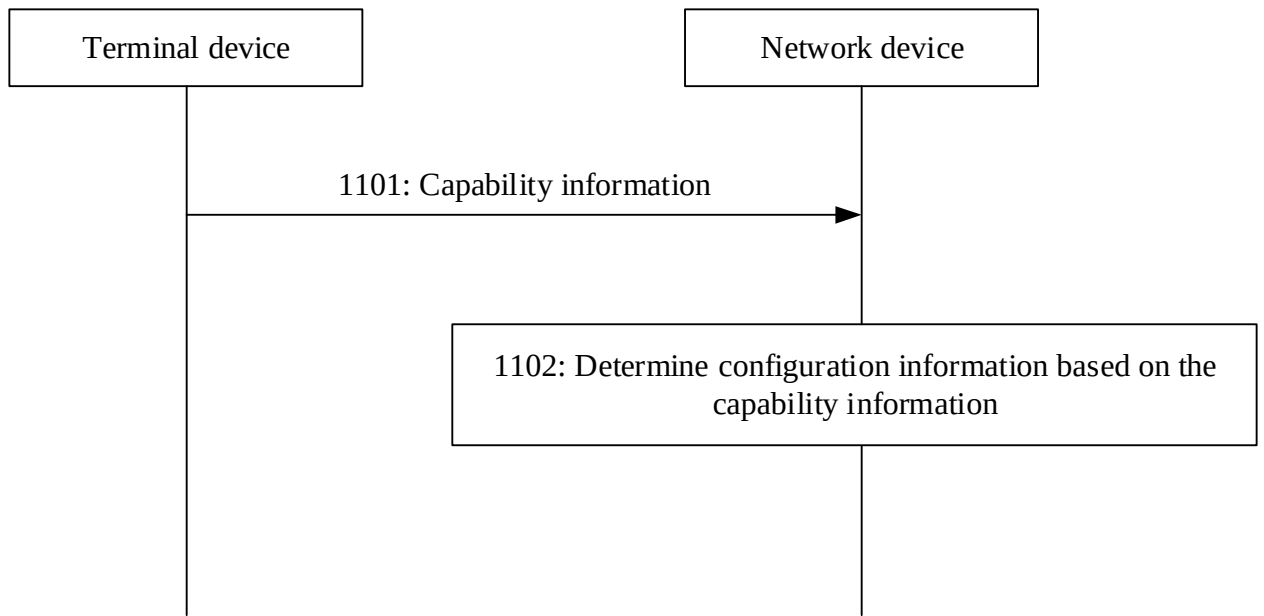


FIG. 11

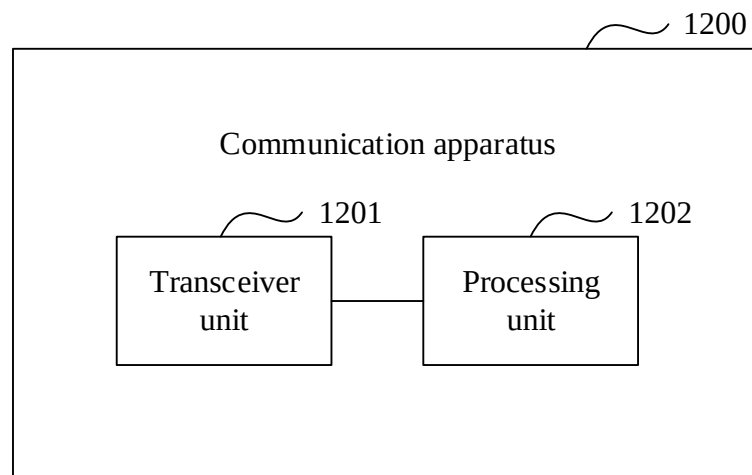


FIG. 12

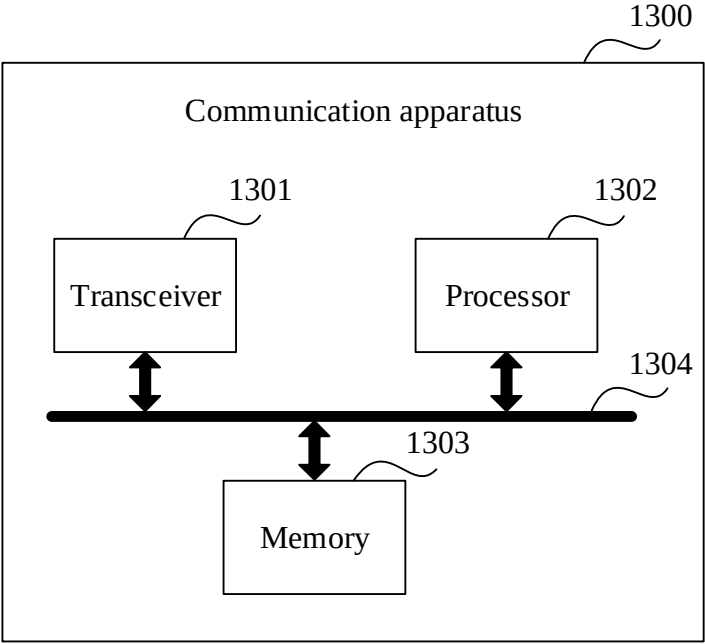


FIG. 13