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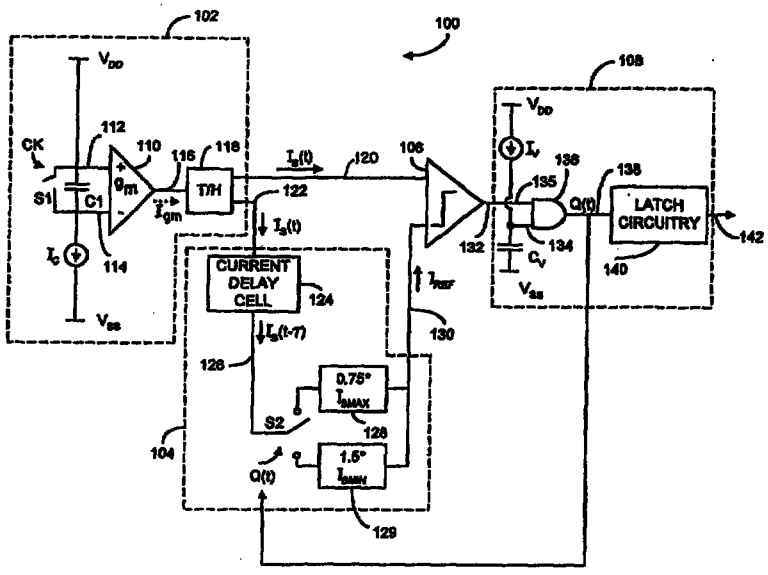
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(54) Title: FSK DEMODULATOR USING A SUPER LINEAR INTEGRATOR



(57) Abstract

A method of and apparatus for decoding an encoded signal are disclosed. A first bit of the encoded signal is received and integrated with a super linear integrator (502) to provide a first integration signal (120, 122). A first reference signal (130) is provided as a function of a previous integration signal (126) associated with a previous bit of the encoded signal by multiplying the previous integration signal by an amount greater than one if the previous bit has a first value, and by multiplying the previous integration signal by an amount less than one if the previous bit has a second value. The first integration signal (120) is compared to the first reference signal (130) and a first bit of an output signal (142) is provided based upon the comparison. The first bit of the output signal is indicative of information encoded in the first bit of the encoded signal.