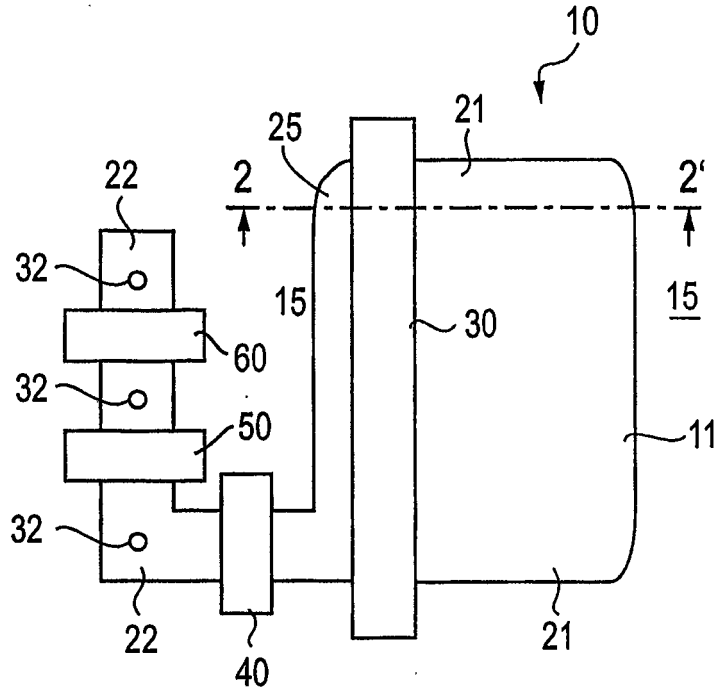

10
21
25
22
2
2'
32
15
30
15
60
32
50
11
32
22
40
21

FIG. 1

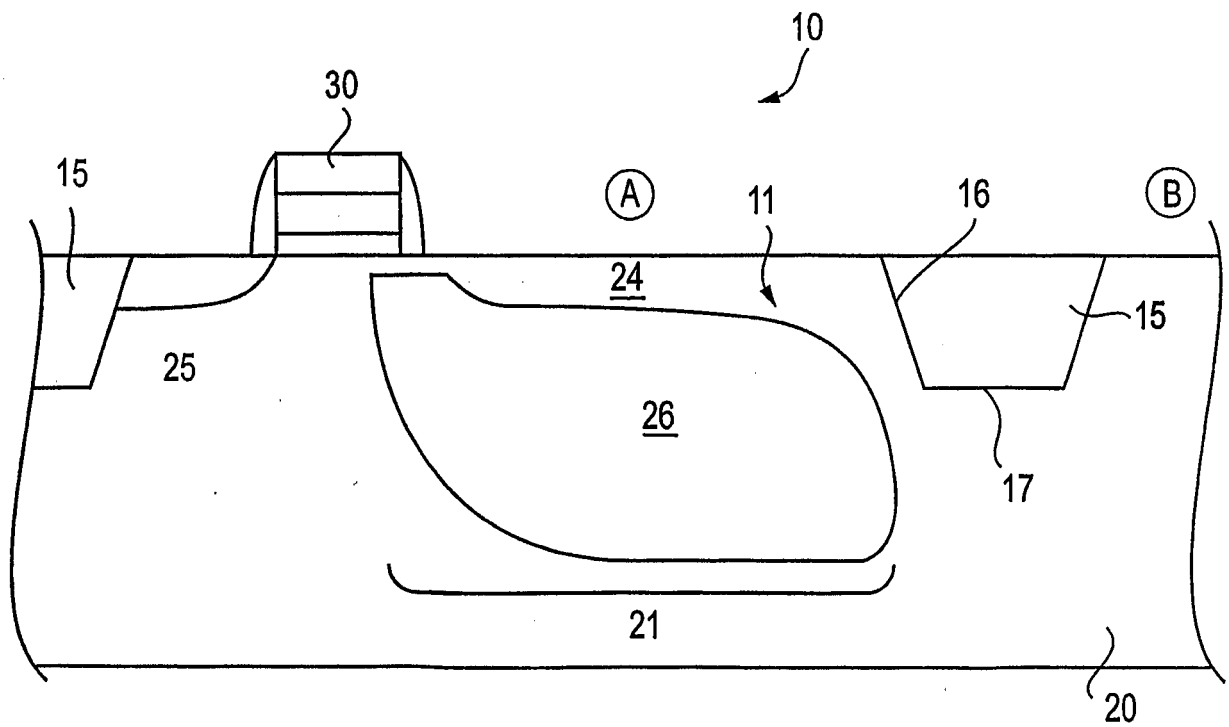

10
30
15
A
11
16
B
24
15
25
26
17
21
20

FIG. 2

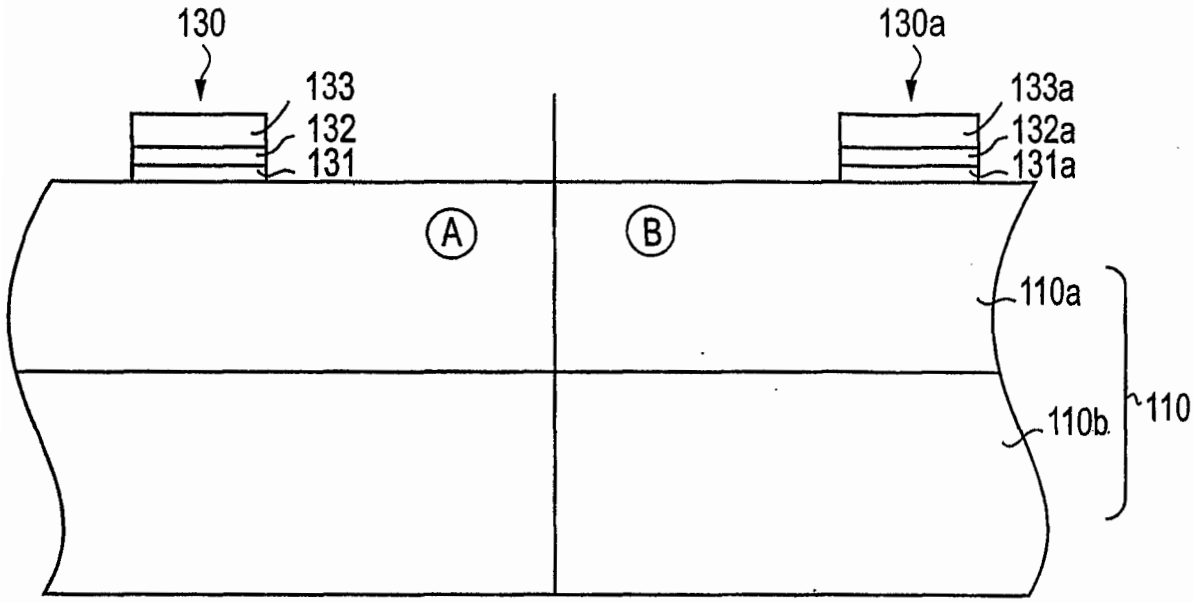

130
130a
133
133a
132
132a
131
131a
A
B
110a
110b
110

FIG. 3

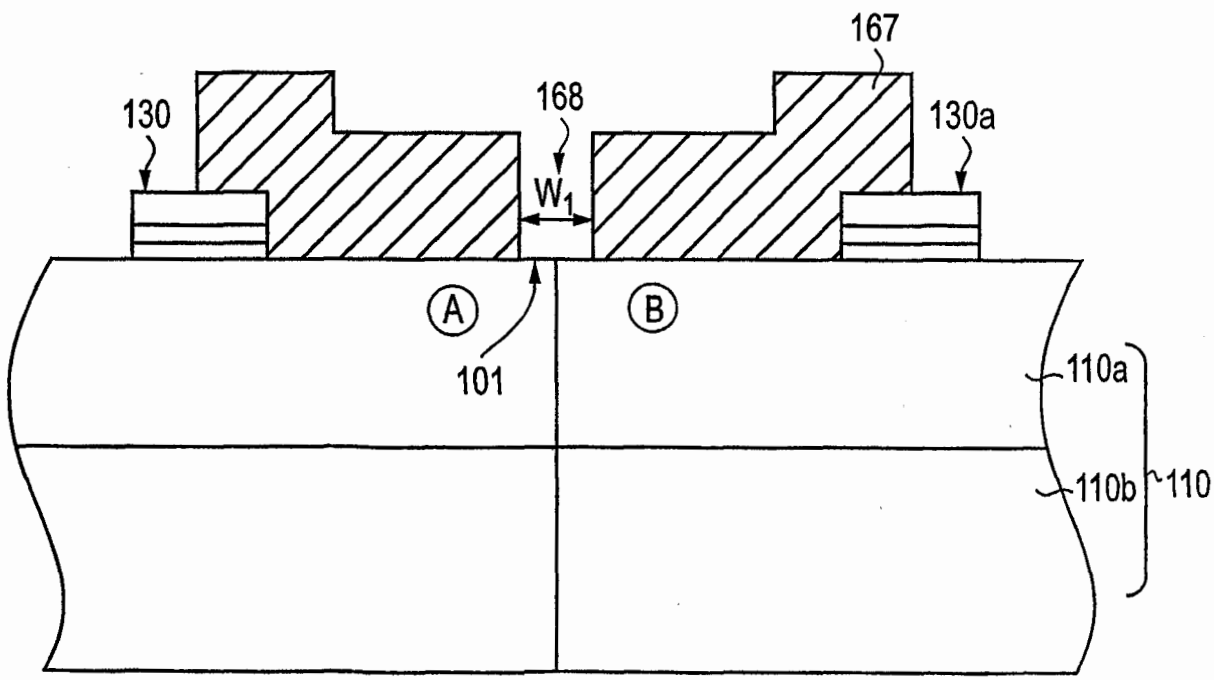

167
130
168
130a
W1
A
B
101
110a
110b
110

FIG. 4

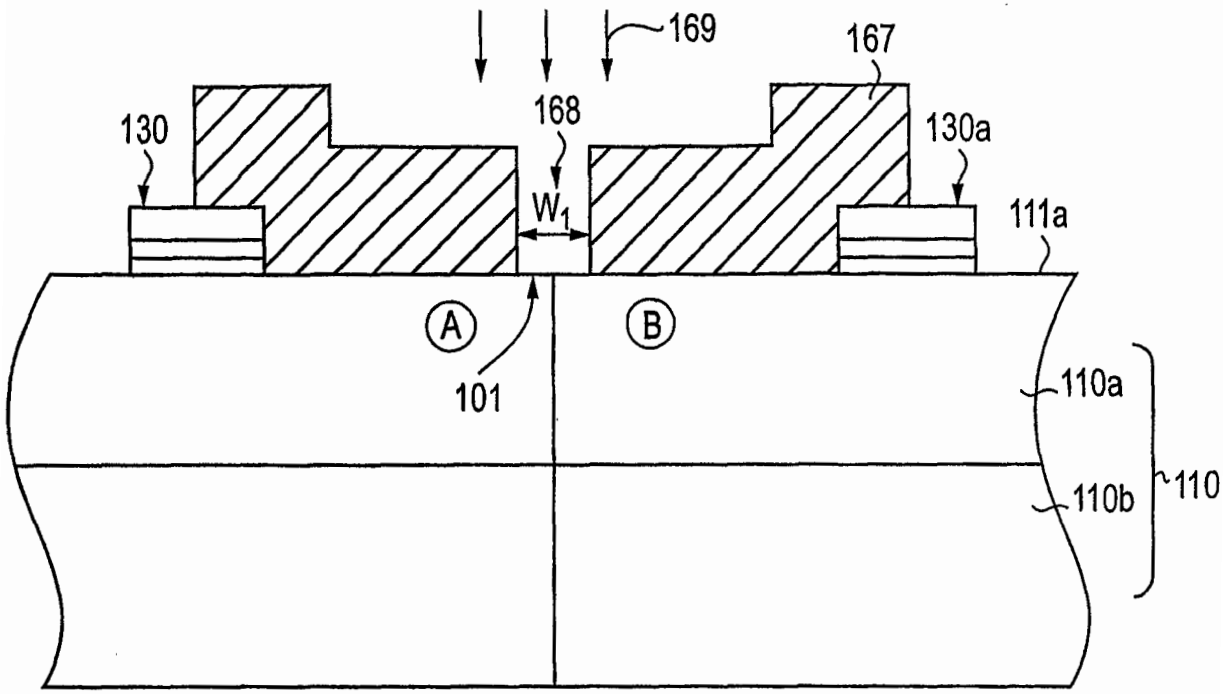

169
167
168
130
130a
W_1
111a
A
B
101
110a
110b
110

FIG. 5

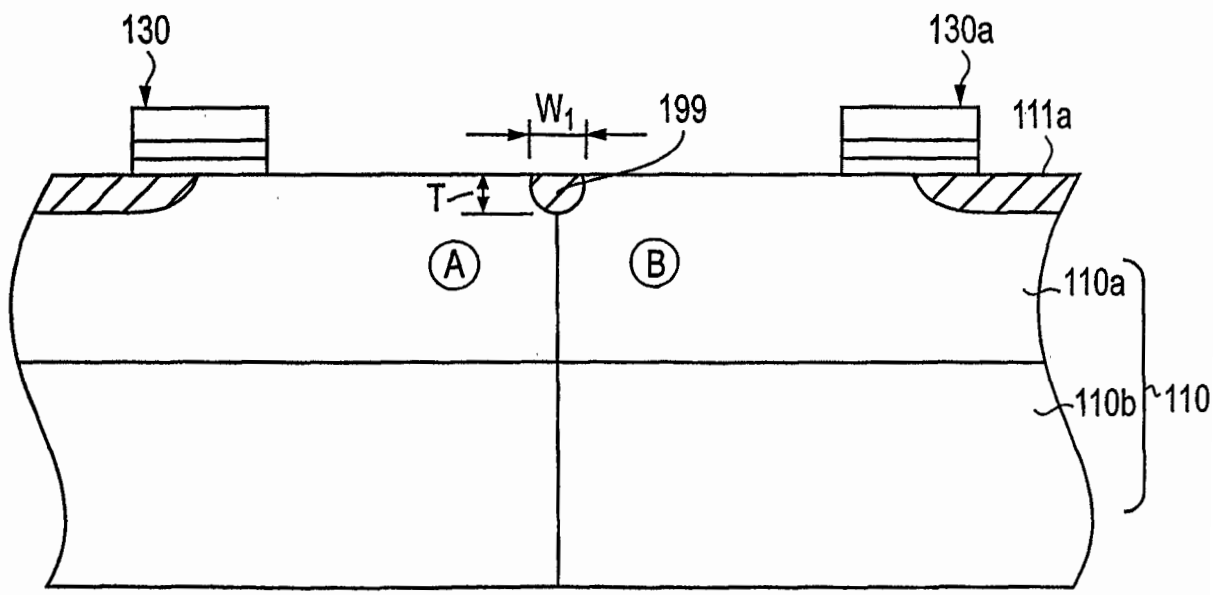

130
130a
W_1
199
111a
T
A
B
110a
110b
110

FIG. 6

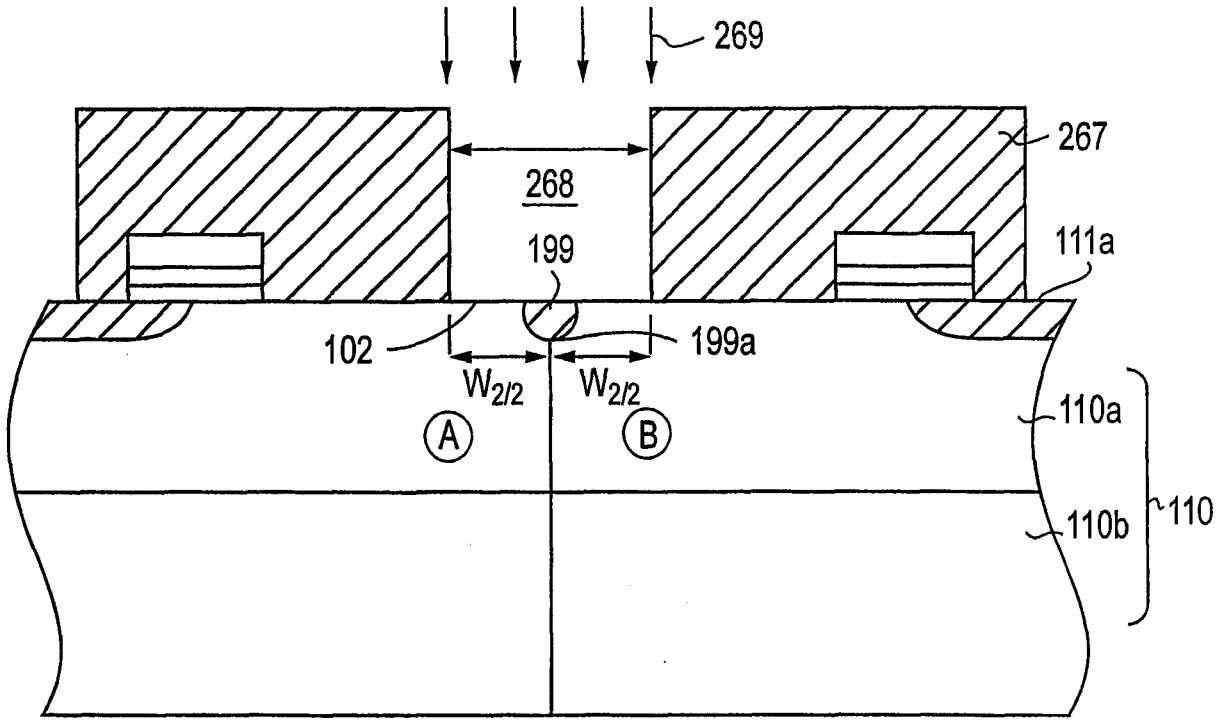

269
267
268
199
111a
102
199a
$W_{2/2}$
$W_{2/2}$
A
B
110a
110b
110

FIG. 7

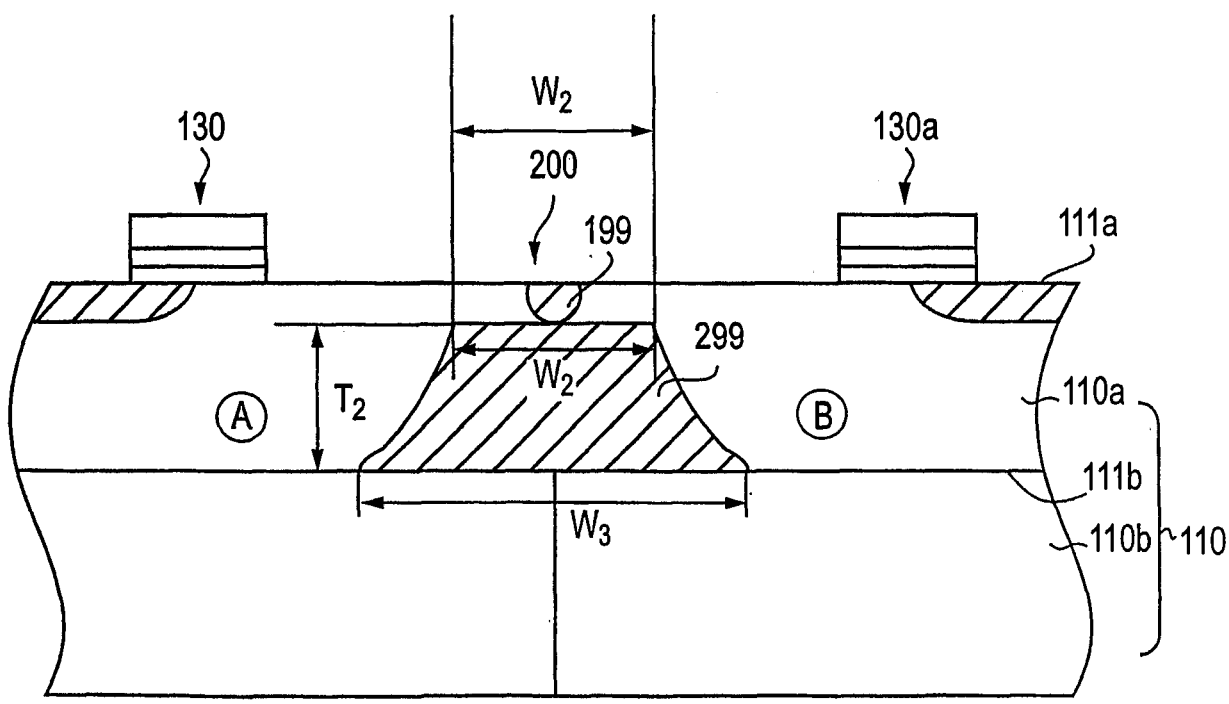

W_2
130
130a
200
199
111a
299
W_2
T_2
A
B
110a
111b
W_3
110b
110

FIG. 8

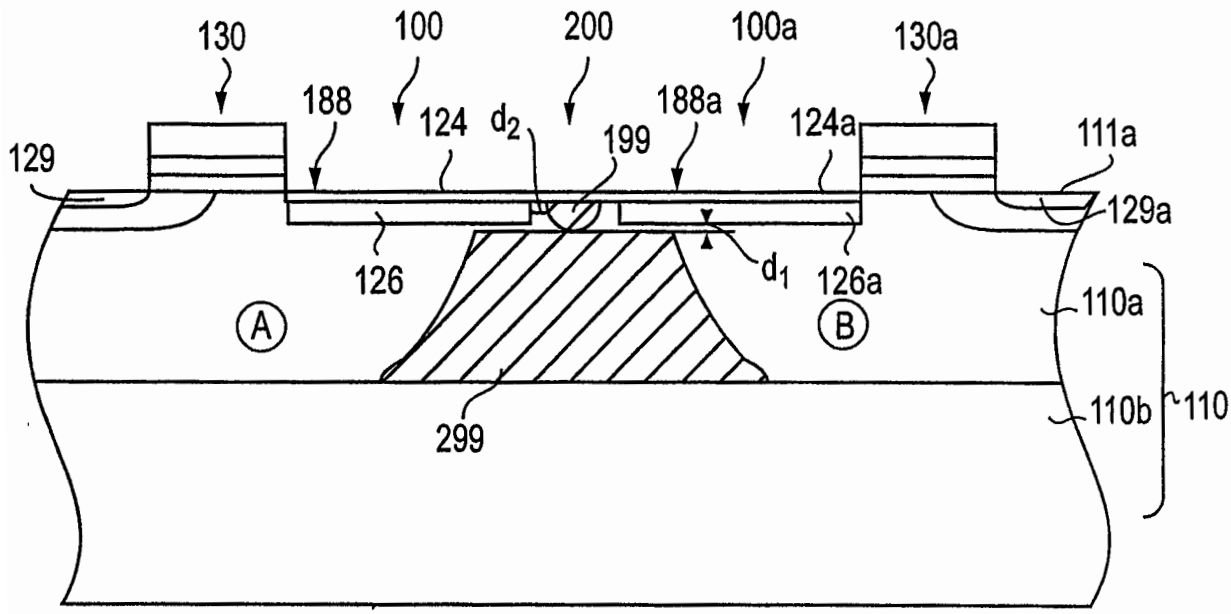
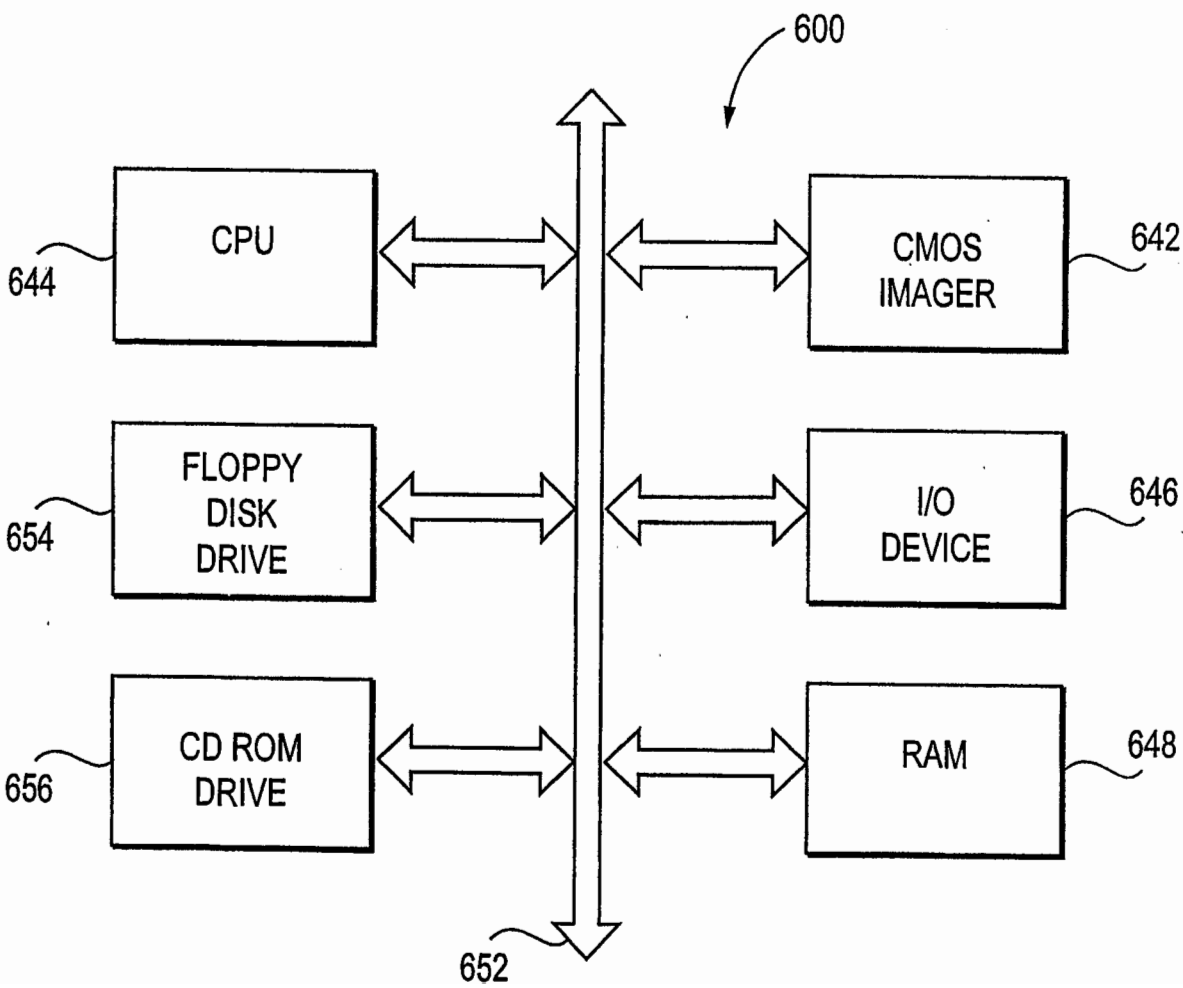

130
100
200
100a
130a
188
124
d_2
199
188a
124a
111a
129
129a
126
d_1
126a
A
B
110a
110b
110
299

FIG. 9


600
CPU
644
CMOS IMAGER
642
FLOPPY DISK DRIVE
654
I/O DEVICE
646
CD ROM DRIVE
656
RAM
648
652