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(54) Title: MOVING PICTURE CAMERA WITH UNIVERSAL SERIAL BUS INTERFACE

(57) Abstract

A camera (110, 110') is provided that can be connected to a processing system (120, 120') via an external connector (154) outside of the housing of the processing system. The camera (110, 110') includes a camera housing (100). An imaging device (111) is provided inside the camera housing (100) that converts moving pictures to a video signal. A bit rate reduction circuit (115) is also provided inside the camera housing (100) and connected to the imaging device (111). The bit-rate reduction circuit (115) reduces a bit rate of the moving picture signal so as to produce a bit-rate reduced video signal having a lower bandwidth than the video signal prior to bit rate reduction.

